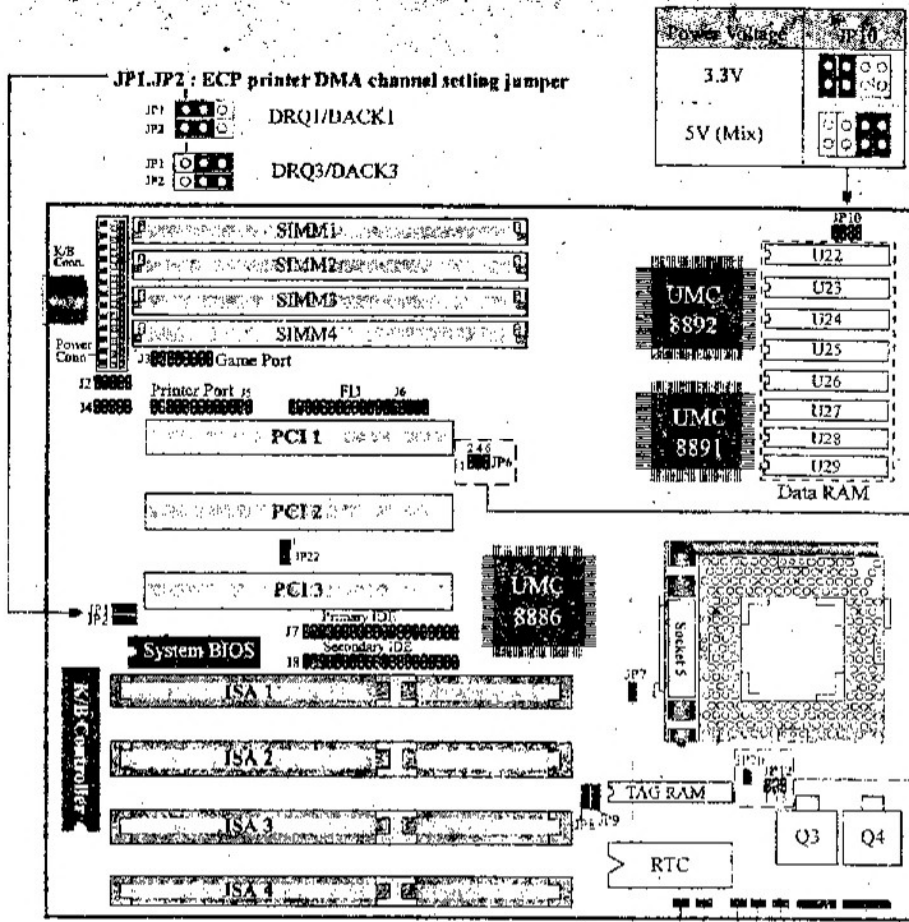


# Quick Reference Jumper Setting



CPU Type	S-spec.	Power Voltage Version	Power Voltage spec.	JP12	Power Voltage	System clock (MHz)	JP6	JP28
P54C-75	Q0-666 SX-961	B5-STD	3.135~ 3.465		3.3	50		
	Q0-700 SX-969	C2-STD	3.135~ 3.465		3.3			
P54C-90	Q0-653 SX-957	B5-STD	3.135~ 3.465		3.3	60		
	Q0-654 SX-958	B5-VR	3.3~ 3.465		3.4			
	Q0-655	B5-VRE	3.45~ 3.6		3.5			
	Q0-699 SX-968	C2-STD	3.135~ 3.465		3.3			
P54C-100	Q0-697 SX-963	C2-STD	3.135~ 3.6		3.3	66		
	Q0-698 SX-970	C2-VRE	3.45~ 3.6		3.5			
P54C-120	Q0-732 SX-994	C2-VRE	3.45~ 3.6		3.5	60		
P54CQS-120		(VRE)	3.45~ 3.6		3.5	60		

JP8, JP9 : Cache size setting jumper

Cache size	Jumper setting	TAG SRAM	DATA SRAM
256KB		8K*8	32K*8
512KB		16K*8	64K*8
1MB		32K*8	128K*8

- JP19 : Keyboard lock connector
- JP18 : Speaker connector
- JP17 : Turbo switch connector
- JP16 : Turbo LED connector
- JP15 : Reset switch connector
- JP14 : H/D LED connector
- JP13 : Green LED connector
- JP7 : Break switch connector

\* JP22 : Reserved for some compatibility problem, normally 2-3 short  
 For some PCI bridge-bridge SCSSIDE (such as AHA 3940) may conflict with some PCI card (such as S3 3864) which doesn't support type 1 configuration cycle.  
 Please put non-type 1 PCI card in the PCI slot3 and short JP22 1-2 pins.

\* Due to diverse IC manufacture processing, the Pentium CPUs have different power specifications, such as STD, VR and VRE Version.

*Handwritten notes:*  
 71 - 38  
 Pent  
 1-2