

# Jumper Settings for 5STX

## CPU Speed Selection

### FOR INTEL PENTIUM CPU

Internal CPU Speed (MHz)	External Bus Speed (MHz)	JP4	JP5	Frequency Ratio	JP11	JP12
75	50	[3-5],[4-6]	[2-3]	2.5x	[3-5],[4-6]	[1-2]
90	60	[1-3],[4-6]	[2-3]	1.5x	[1-3],[2-4]	[1-2]
100	66	[3-5],[2-4]	[2-3]	1.5x	[1-3],[2-4]	[1-2]
120	60	[1-3],[4-6]	[2-3]	2x	[1-3],[4-6]	[1-2]
133	66	[3-5],[2-4]	[2-3]	2x	[1-3],[4-6]	[1-2]
150	60	[1-3],[4-6]	[2-3]	2.5x	[3-5],[4-6]	[1-2]
166	66	[3-5],[2-4]	[2-3]	2.5x	[3-5],[4-6]	[1-2]
200	66	[3-5],[2-4]	[2-3]	3x	[3-5],[2-4]	[1-2]
233	66	[3-5],[2-4]	[2-3]	3.5x	[1-3],[2-4]	[1-2]

### FOR CYRIX / IBM 6X86 CPU

Internal CPU Speed (MHz)	External Bus Speed (MHz)	JP4	JP5	Frequency Ratio	JP11	JP12
PR133	55	[3-5],[4-6]	[1-2]	2x	[1-3],[4-6]	[1-2]
PR166	66	[3-5],[2-4]	[2-3]	2x	[1-3],[4-6]	[1-2]
PR200	75	[1-3],[4-6]	[1-2]	2x	[1-3],[4-6]	[1-2]
PR233	66	[3-5],[2-4]	[2-3]	3x	[3-5],[2-4]	[1-2]
PR233	75	[1-3],[4-6]	[1-2]	2.5x	[3-5],[4-6]	[1-2]

### FOR AMD K5 CPU

Internal CPU Speed (MHz)	External Bus Speed (MHz)	JP4	JP5	Frequency Ratio	JP11	JP12
PR90	60	[1-3],[4-6]	[2-3]	1.5x	[1-3],[2-4]	[1-2]
PR100	66	[3-5],[2-4]	[2-3]	1.5x	[1-3],[2-4]	[1-2]
PR133	66	[3-5],[2-4]	[2-3]	1.5x	[1-3],[2-4]	[1-2]
PR150	60	[1-3],[4-6]	[2-3]	1.75x	[3-5],[4-6]	[1-2]
PR166	66	[3-5],[2-4]	[2-3]	1.75x	[3-5],[4-6]	[1-2]

## FOR AMD K6 CPU

Internal CPU Speed (MHz)	External Bus Speed (MHz)	JP4	JP5	Frequency Ratio	JP11	JP12
PR166	66	[3-5],[2-4]	[2-3]	2.5x	[3-5],[4-6]	[1-2]
PR200	66	[3-5],[2-4]	[2-3]	3x	[3-5],[2-4]	[1-2]
PR233	66	[3-5],[2-4]	[2-3]	3.5x	[1-3],[2-4]	[1-2]
PR266	66	[3-5],[2-4]	[2-3]	4.0x	[1-3],[4-6]	[2-3]
PR300	66	[3-5],[2-4]	[2-3]	4.5x	[3-5],[4-6]	[2-3]

## CPU Voltage Selection

CPU Type	Voltage Requirements
Intel Pentium Cyrix/IBM 6x86 AMD K5	3.4V(STD) / 3.5V(VRE)
Intel Pentium with MMX Cyrix/IBM 6x86L	2.8V
Cyrix/IBM 6x86MX AMD-K6-PR166, PR200	2.9V
AMD-K6-PR233	3.2V
AMD-K6-PR266, PR300	2.2V

JP15	JP14	Voltage
[1-3],[4-6]	[1-3],[2-4]	2.2V
[1-3],[2-4]	[1-3],[4-6]	2.8V
[3-5],[2-4]	[1-3],[4-6]	2.9V
[1-3],[2-4]	[3-5],[4-6]	3.2V
[1-3],[4-6]	[3-5],[4-6]	3.4V
[3-5],[4-6]	[3-5],[4-6]	3.5V

### CAUTION:

For MMX or other split power plane CPU, JP8 must be fully opened before turning on the system power. Failure to do so may cause damages to your CPU. For Non-MMX or Non split power plane CPU, JP8 must keep closed.

JP9 is used to hold the jumpers when JP8 is fully opened

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