Chaintech 5SSV 1MB cache upgrade

- The TAG is already sufficiently sized
- Replace Cache SRAM with EliteMT LP61L64128F-4
- Install R293 0-Ohm bridge to connect TAG A14 to CPU A19
- Install R140 0-Ohm bridge to connect CACHE A16 to CPU A19

The BIOS / Chipset does auto-sizing so no further modifications are necessary.

This guide can be used for other SiS 530-based motherboards, provided they contain the correct TAG SRAM.