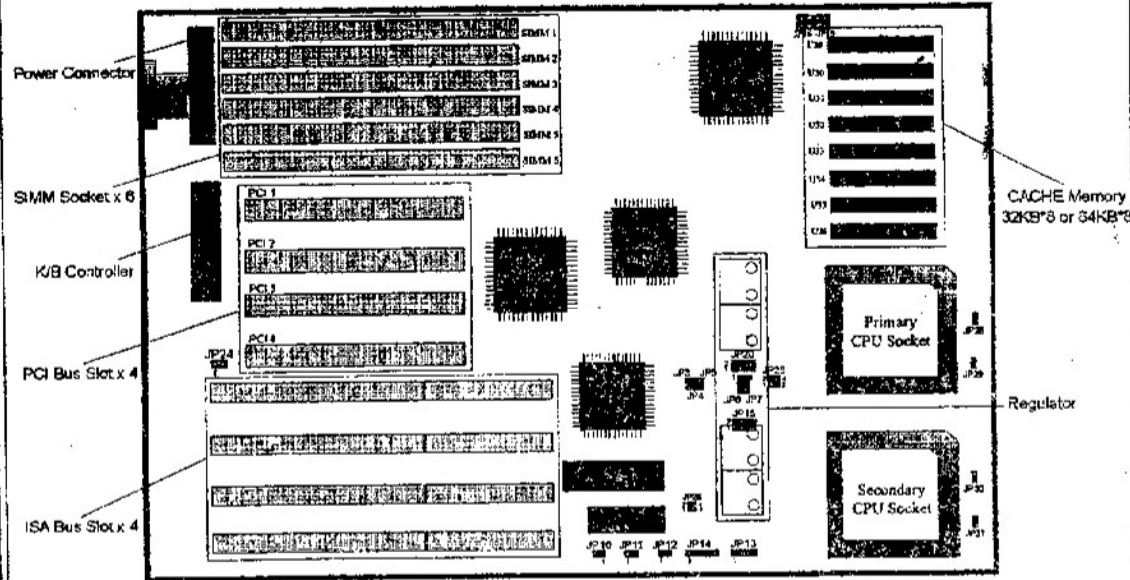


# Jumper Setting Quick Reference



CPU	POWER VOLTAGE VERSION NO.	Core Vcc		I/O Vcc		SYSTEM CLOCK	CPU Int. Speed = X x SYS Speed												
		CPU#1 JP20	CPU#2 JP15	CPU#1 JP26	CPU#2 JP25		JP3,4,5	CPU#1 JP28 JP29	CPU#2 JP30 JP31										
P54C-75	STD Q0-686 SX-961 Q0-700 SX-969	3.4	[Diagram]	3.4	[Diagram]	50	[Diagram]	x1.5	[Diagram]										
										P54C-90	STD Q0-653 SX-857 Q0-699 SX-968	3.4	[Diagram]	3.4	[Diagram]	60	[Diagram]	x1.5	[Diagram]
P54C-100	STD Q0-697 SX-963	3.5	[Diagram]	3.5	[Diagram]	66	[Diagram]	x1.5	[Diagram]										
										VRE Q0-698 SX-97C	3.5	[Diagram]	3.5	[Diagram]					
P54C-120	STD VRE	3.5	[Diagram]	3.5	[Diagram]	60	[Diagram]	x2	[Diagram]										
										VRE	3.5	[Diagram]	3.5	[Diagram]					
P54C-133	STE VRE	3.5	[Diagram]	3.5	[Diagram]	66	[Diagram]	x2	[Diagram]										
										VRE	3.5	[Diagram]	3.5	[Diagram]					
P54C6-160	STD VRE	3.5	[Diagram]	3.5	[Diagram]	60	[Diagram]	x2.5	[Diagram]										
										VRE	3.5	[Diagram]	3.5	[Diagram]					

- JP10 : Turbo LED connector
- JP11 : Reset switch connector
- JP12 : Turbo switch connector
- JP13 : Speaker connector
- JP14 : Keyboard clock connector

- JP20, JP26 : Primary CPU Vcc setting jumper
- JP15, JP25 : Secondary CPU Vcc setting jumper

Cache size	Data SRAM	JPS, JP7
256KB	32K*8 x 8	[Diagram]
512KB	64K*8 x 8	[Diagram]

SRAM Type	JP16-JP19
3V SRAM	[Diagram]
Mix Mode SRAM	[Diagram]

Intel CPU Speed (MHz)	JP26/JP30	JP29/JP31
3 x System Clock	Short	Open
2.5 x System clock	Short	Short
2 x System Clock	Open	Short
1.5 x System Clock	Open	Open

CPU Core Voltage Setting (JP20/JP15)			
[Diagram]	[Diagram]	[Diagram]	[Diagram]
3.3V	3.4V	3.5V	Reserved for next generation CPU

CPU I/O Voltage Setting (JP26/JP25)	
[Diagram]	[Diagram]
3.4V	3.5V

\* Data SRAM's location from U29 - U30

\* Int. CPU Speed Setting

\* The power voltage version No. is on the top fo CPU.  
\* If you can't identify CPU type from above table, please contact your dealer.