SQ 593Mainboard User's Manual

User Notice

Static Electricity Precaution

Static electricity can easily damage your SQ59mainboard. By observing a few basic precautions can help safeguard against damage that could result in expensive repairs. Follow the simple measures below to protect your equipment from static electricity damage:

- 1. Keep the mainboard and other system components in their anti-static packaging until you are ready to install them.
- Touch a grounded surface before you remove any system component from its protective anti-static packaging. Unpacking and installation should be done on a grounded, anti-static mat. The operator should be wearing an anti-static wristband, grounded at the same points as the anti-static mat.
- 3. After removing themainboard from its original packaging, only place it on a grounded, anti-static surface component side up. Immediately inspect the board for damage. Due to shifting during shipping, it is suggested that the installer press down on all of the socket ICs to ensure they are properly seated. Do this only with the board placed on a firm flat surface.
- 4. During configuration and installation touch a grounded surface frequently to discharge any static electrical charge that may have built up in your body. The best precaution is to wear a grounded wrist strap. When handling the mainboard or an adapter card avoid touching its components. Handle the mainboard and adapter cards either by the edges or by the adapter card case mounting bracket.

The information presented in this publication has been carefully screened for reliability.

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Introduction

How to use this Manual

This manual provides information necessary for SQ593 mainboard and is organized into 4 chapters. Its purpose is to explain the installation procedures and operations of the mainboard as specified below.

Introduction	:	Manual information and checklist
Key Features	:	An overview of the specifications of this ainboard
Installation	:	Instructions on how to setup thenainboard
BIOS setup	:	BIOS software setup information

Item Checklist

The SQ593 mainboard should contain followings tick(\checkmark). Immediately, contact to your retailer if you discover any missing items, or any damage .

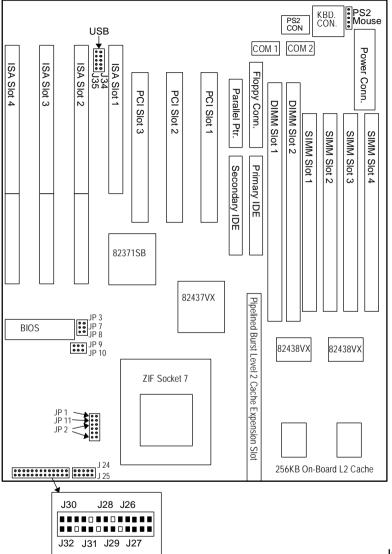
- ☑ The SQ593 mainboard
- ☑ This SQ593 user's manual
- \blacksquare 2 serial port ribbon cables attached to a mounting bracket
- ☑ 1 parallel ribbon cable attached to a mounting bracket
- ☑ 1 IDE ribbon cable
- ☑ 1 Floppy ribbon cable
- \square PS/2 mouse cable with mounting bracket (optional)
- □ Infrared module(optional)
- D Pipeline Burst Cache Module (optional)

USB(2-port)	cable	with	mounting	bracket(optional)
CDD(2 port)	cubic	vv itii	mounting	oracicoptional)

key Features

- ♦ Processor: The SQ593 supports Intel Pentium Processors P54C 75-200MHz, and Cyrix 6x86 Processors. The ZIF Socket 7 will support future Pentium Overdrives.
- L2 Cache: Optional 0KB cache upgradeable to 512KB. Standard package includes 256KB cache onboard upgradeable to 512KB using a Pipelined Burst L2 COAST (cache on a stick) Module inserted into the cache expansion slot.
- System memory: Supports four 72-pin SIMM modules using 4MB, 8MB, 16MB or 32MB for a maximum 128MB. Memory supported is both Fast Page Mode (FPM) and Extended Data Output (EDO). The memory must be 70ns (nano seconds) or faster.
- Synchronous DRAM (SDRAM) memory DIMM Modules: Two 168-pin DIMM memory modules are also featured on the SQ593 in addition to the four 72-pin SIMM modules. The DIMM module sizes can be 1Mx64, 2Mx64, or 4Mx64. The DIMM modules must be 3.3 volts and can be EDO DRAM or Synchronous DRAM(SDRAM). The memory must be 60ns or faster. The SQ593 can only support eitherSIMMs or DIMMs, not both.
- ☆ Intel Chipset: Intel 82430VX PCIset as the core Chipset with a Winbond 83877F/AF super 1/O (input/output) controller chip.
- ★ Expansion Slots: Three 16-bit ISA slots, one 8-bit ISA slot and three 32-bit PCI slots (includes one shared).
- Super Multi-I/O: Two high-speed UART compatible serial ports and one parallel port with ECP and EPP compatibility. One FDD header supporting either 5.25. or 3.5.(1.2, 1.44 or 2.88MB) floppy drives without requiring an external I/O card. One fast IrDA TX/RX infrared port.
- PCI Bus Master IDE Controller: Onboard PCI Bus Master IDE controller with 2 ports to support 4 IDE devices. This controller supports PIO mode 3 and 4 with a data transfer rate up to 17MB per second. The Bus Master IDE DMA mode 2 transfer up to 22MB per second.
- ♦ <u>PCI BIOS: With Green, Plug and Play function</u>.
- ☆ <u>Universal Serial Bus: Two standard USB headers supporting up to 48MHz and 127</u> peripheral devices . Optional USB cable set.
- ♦ Optional IrDA and PS/2 mouse: Optional Fast IrDA (Infrared) or on IrDA TX/RX module for wireless interface and PS/2 mouse connector.

Mainboard Layout



Installation

Installation Preview

Before you install the SQ593 mainboard into the system chassis, you may find it convenient to first configure the mainboard hardware. This section describes how to configure the jumper settings, install memory modules and how to attach the various system components. Before using your computer you must review the following 6 steps:

- Jumpers settings.
- Installation of DRAM memory modules.
- Installation of the CPU.
- Installation of IDE-I/O cables and the power supply connection.
- Installation of expansion cards.
- Setup of the system BIOS software.

Jumpers:

JP1(VR)	Voltage regulator output selection.			
JP11(2.5V/2.8V)	Intel P55C 2.5V/2.8V, Cyrix 6x86 regulator output selection.			
JP2	Intel P54C/P55C CPU & Cyrix 6x86 selection.			
JP3(CLR CMOS)	CMOS RAM (Operation/Clear CMOS data).			
JP7(SO); JP8(S1)	CPU external clock (Bus) frequency selection.			
JP9(= BF0)	CPU (Bus) frequency ratio.			
JP10(= BF1)	CPU (Bus) frequency ratio.			

Expansion Slots:

SIMM Sockets	DRAM SIMM module sockets.				
DIMM Sockets	SDRAM/EDO DIMM module sockets.				
Cache Expansion	Slot for L2 External Pipelined SRAM Module.				
ZIF Socket 7	Socket for Central Processing Unit (CPU).				
ISA Slot 1	8-bit ISA Bus expansion slot.				
ISA Slots 2,3,4	16-bit ISA Bus expansion slots.				
PCI Slots 1,2,3	32-bit PCI Bus expansion slots.				

Connectors

Keyboard	Keyboard connector (5-pin female) or PS/2 keyboard connector (6-pin female) (Optional).
PS/2 Mouse	PS/2 Mouse Header (5-pin) or PS/2 Mouse connector (6-pin female option).
Parallel Port	Parallel port connector (26-pin Block).
Serial Port	Serial ports COM1 & COM2 (10-pin Block).
Floppy Drive	Floppy drive connecto(34-pin Block).
Power Input	Power connector (12-pin Block).

Primary IDE	Primary IDE connector (40-pin Block).
Secondary IDE	Secondary IDE connector (40-pin Block).
USB Header(J34/J35)	2 USB ports USB 0 & USB 1 (5-pin).
Fast IrDA(J24)	Fast IrDA connector (5-pin).
IrDA(J25)	IrDA connector (5-pin).
Key Lock(J26)	Keyboard lock switch connector (5-pins).
Speaker(27)	Speaker connector (4-pin).
Speaker(27) Green LED(J28)	Speaker connector (4-pin). Green LED connector (2-pin).
-	
Green LED(J28)	Green LED connector (2-pin).
Green LED(J28) SMI Switch(J29)	Green LED connector (2-pin). SMI suspend switch conn e tor (2-pin).

WARNING: Do not apply power if the mainboard appears damaged or items are missing from the mainboard .

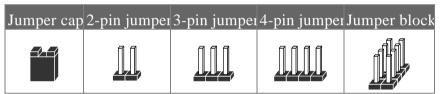
Installation Steps

Jumper Settings

You can configure the hardware options by setting jumper on the mainboard. A jumper is a set of two or more metal pins in a plastic base attached to the mainboard. A plastic Jumper cap. with a

metal (conductive) plate inside fits over two pins to create an electrical contact or short between them. This contact establishes a hardware setting and is referred to as a .closed. jumper setting. Some jumpers have two pins while others may have three or more. Jumpers are sometimes combined into sets called jumper blocks where all the jumpers in the block must be set together to establish a hardware setting. In this manual the jumper settings will be described as graphically using a (s)triangle always marking Pin 1. Those jumpers with two pins will be shown as closed and open. A jumper is referred to as closed by placing the plastic jumper cap over the two jumper pins and as open by removing the jumper cap. Some jumpers are oriented vertically and other horizontally with Pin 1 marked as (s=P1).

Jumpers, Jumper Caps, and Jumper Blocks



For Setting 3-pin jumpers



Jumper Pins 1 & 2 are closed with a jumper cap



Jumper Pins 2 & 3 are closed with a jumper cap

Setting 2-pin jumpers



This jumper is closed with the jumper cap is placed over the 2 pins



This jumper is open with the jumper cap removed from the 2 pins

WARNING: Some pins are used for connectors or power sources. These are clearly marked separately from the jumpers listed inMainboard Layout. Any improper placing of jumper caps over these connectors will result in damage to your motherboard.

CPU Voltage Selection Jumpers

Intel Pentium Selection	JP1	JP11	JP2
P54C		חח	
STD	P2 P1	P2 P1	P2
(3.135V-3.6V)	P2 P1	r 2 r 1	P6 P8
P54C	חח	חח	
VRE	P2 P1	P2 P1	P 2
(3.4-3.6V)			P6 P8
P55C			สโป
(2.5V)	P2 P1	P2 P1	P 2 P 4
	1211	1211	P6 P8
P55C		11	สโป
(2.8V)	P2 P1	P2 P1	P 2 P 4 P 5
			P8

WARNING: Pins 1 thru 8 of JP2 must always be open whether the voltage is set to 2.5V or 2.8V. This applies to both Intel P55C CPU and the Cyrix 6x86 CPU.

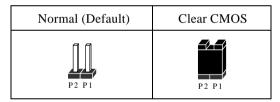
Cyrix 6x86 Selection	JP1	JP11	JP2
STD		11	
(3.15-3.6)	P2 P1	P2 P1	P2 P4 P6 P8
VRE	11	11	
(3.4-3.6)	P2 P1	P2 P1	P2 P4 P6 P8
2.5V			afal
(Future)	P2 P1	P2 P1	P2 P4 P6 P8
2.8V		11	afal
(Future)	P2 P1	P2 P1	P2 P4 P6 P8

REMARK: Effective April 24, 1996 Cyrix changed their previous part markings to their new markings (example: 028 into 3.52V(028)) for their nominal voltage specification of 3.52 volts(also known as the VRE spec.). The table below shows Cyrix old Vs new markings. Be sure that your mainboard voltage jumper settings are set accordingly.

Previous Marking	Previous Recommended Nominal Voltage	New Marking	New Recommended Nominal Voltage
Blank	3.3V	3.3V or 3.52V	3.52V
016	3.3V	N/A	N/A
028	3.52V	3.52V(028)	3.52V

CMOS Settings(Operation/Clear CMOS Data) (JP3)

The JP3 allows you to clear the SQ593 mainboard CMOS memory and Real Time Clock (RTC) data. The CMOS memory maintains the system configuration information and RTC provides the system with the date and time. Make sure this jumper is open for normal operation.



Clear CMOS procedure :

The SQ593 mainboard uses a Benchmarq BQ3285S chip. To clear the stored CMOS data, power on with the JP3 closed and then do the following :

1) With the system turned off, close JP3 \Rightarrow 2) Power on for a few seconds \Rightarrow 3) Power off \Rightarrow 4) Open JP3 \Rightarrow 5) Power on \Rightarrow 6) Re-setup the BIOS (refer to enter the BIOS setup menu, hold down during the system boot sequence).

NOTICE: Under some circumstances it is possible that the CMOS configuration settings may be lost or corrupted causing the system to malfunction. This is not a serious problem. If this happens, run the BIOS setup utility and re-enter your configuration settings. When you restart the computer, the system should work normally.

CPU Type and Selection {JP7,JP8,JP9(=BF0),JP10(= BF1)}

The system speed depends upon the frequency of CLOCK GENERATOR which is determined by jumpers. These jumpers tell the system what speed to run at. Currently, this mainboard speed range is from 75MHz to 200MHz. The CPU input frequency must match the frequency of CLOCK GEN or it will cause the system to malfunction. For example, setting a 75MHz CPU to run at 90MHz will cause the system to malfunction.

Intel CPU Setting	JP7	JP8	JP9(BF0)	JP10(BF1)
75MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
90MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
100MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
120MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
133MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1

150MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
166MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
180MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
200MHz	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1

Cyrix 6X86 Setting	JP7	JP8	JP9(BF0)	JP10(BF1)
100MHz(=P120+)	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
110MHz(=P133+)	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
120MHz(=P150+)	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1
133MHz(=P166+)	P2 P1	P2 P1	P3 P2 P1	P3 P2 P1

NOTICE: When installing the CPU into the CPU socket, be sure that PIN 1 of the CPU is in the same corner as the PIN 1 of socket. The CPU is an extremely sensitive electric component and can be easily damaged by static electricity.

Expansion Slots

The SQ593 has 7 expansion Slots On-board, there are 3 16-bit ISA, 1 8-bit ISA, and 3 32-bit PCI expansion Slots (includes 1 shared).

SIMM Slots-System Memory (DRAM)

You can configure the system memory size in a variety of ways by in using different combinations of the four 72-pin DRAM SIMM modules. The memory must be 70ns (nano seconds) or faster using either Fast Page Mode or Extended Data Output (EDO) types. The memory chart below shows the different memory size combinations available. Please pay attention to the following restrictions:

- You must use one pair of sockets at a time in sequence (i.e. SIMM1 and SIMM2, or all four sockets at once). Each pair of modules must be the same size and speed and may be either single sided or double-sided.
- 2) Module sizes: Single-sideSIMMs: 4MB, 16MB
- 3) Double-sideSIMMs: 8MB, 32MB, 64MB

IMPORTANT: To support parity check detection, you must use True 36-bit parity type DRAM (i.e. 8 data chips + 4 parity chips). Mixing 32-bit non parity DRAM (i.e. 8 chips) and 36-bit parity DRAM will cancel the parity check detection feature.

Memory Module Combinations

Total Memory (Slot 1-4)	Bank 0 (Slot 1 & 2)	Bank 1 (Slot 3 & 4)
8MB	4MBx2	None
16MB	8MBx2	None

32MB	16MBx2	None
64MB	32MBx2	None
8MB	None	4MBx2
16MB	None	8MBx2
32MB	None	16MBx2
64MB	None	32MBx2
16MB	4MBx2	4MBx2
24MB	4MBx2	8MBx2
40MB	4MBx2	16MBx2
72MB	4MBx2	32MBx2
24MB	8MBx2	4MBx2
32MB	8MBx2	8MBx2
48MB	8MBx2	16MBx2
80Mb	8MBx2	32MBx2
40MB	16MBx2	4MBx2
48MB	16MBx2	8MBx2
64MB	16MBx2	16MBx2
96MB	16MBx2	32MBx2
72MB	32MBx2	4MBx2
80MB	32MBx2	8MBx2
96MB	32MBx2	16MBx2
128MB	32MBx2	32MBx2
128MB	64MBx2	None
128MB	None	64MBx2

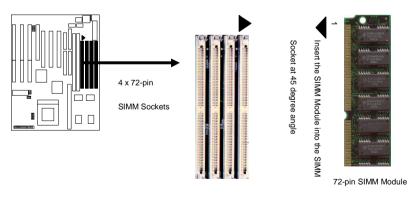
256MB	64MBx2	64MBx2
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Installation Procedure for System Memory

1. PIN1 of the SIMM module must match with the PIN1 of the SIMM socket.

- 2. The module will only insert into the socket one way. An orientation cut-out will prevent you from inserting it the wrong way.
- 3. Insert the DRAM module into the SIMM socket at a 45 degree angle. If Pin 1 of the SIMM does not line up with Pin 1 of the socket, the SIMM will not insert into the socket. After inserting the SIMM module completely into socket, push the SIMM module into a vertical position.
- 4. The module should click into place with the retaining clips at each end of the socket snapping behind the module to secure it.
- 5. To release the memory module push both retaining clips outwards and carefully rock the module forward.

IMPORTANT: Do not use SIMM modules that use an extra TTL chip to convert the memory module from asymmetric to symmetric.



SDRAM Memory DIMM Sockets

The SQ593 mainboard features two 168-pin DIMM modules with each DIMM supporting 8MB to 32MB of Synchronous DRAM using 1Mx64, 2Mx64 and 4Mx64 DIMM modules (Asymmetrical single-and double -sided). The DRAM interfaces on a 64-bit wide data path and supports unbuffered Extended Data Out(EDO) 3.3V DRAM 60ns or faster. Also supported is 3.3 volts 66.67MHz unbuffered Synchronous DRAM(SDRAM). EDO DRAM DIMM and SDRAM DIMM are defined as below:

Туре	Capacity	Organization	EDO DRAM	Clock Speed
168-pin 3.3Vunbuffered EDO DRAM DIMM w/ serial Presence Detect	8MB	1Mx64	1Mx16 1 bank (DIMM1)	60ns
168-pin 3.3Vunbuffered EDO DRAM DIMM w/ serial Presence Detect	16MB	2Mx64	1Mx16 2 banks (DIMM 1& 2)	60ns
168-pin 3.3Vunbuffered EDO DRAM DIMM w/ serial Presence Detect	16MB	2Mx64	2Mx8 1 bank (DIMM 1)	60ns
168-pin 3.3Vunbuffered EDO DRAM DIMM w/ serial Presence Detect	32Mb	4Mx64	2Mx8 2 banks (DIMM1 & 2)	60ns

Table 1 : EDO DRAM DIMM Configurations :

Table 2: SDRAM DIMM Configurations:

Type Capacity Organization	SDRAM	Clock Speed
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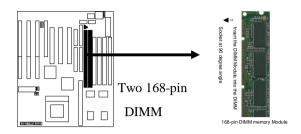
168-pin 3.3V unbuffered SDRAM DIMM with serial Presence Detect	8MB	1Mx64	1Mx16 1 bank (DIMM1)	66.67MHz
168-pin 3.3V unbuffered SDRAM DIMM with serial Presence Detect	16MB	2Mx64	1Mx16 2 banks(DIMM 1& 2)	66.67MHz
168-pin 3.3V unbuffered SDRAM DIMM with serial Presence Detect	16MB	2Mx64	2Mx8 1 bank (DIMM 1)	66.67MHz

WARNING: You cannot install both SIMM and DIMM memory modules at the same time.

Installation Procedure (SDRAM/EDO DRAM)

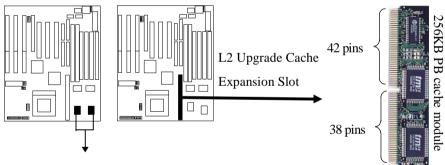
- 1. PIN1 of the DIMM module must match with the PIN1 of the DIMM socket.
- The module will only insert into the socket one way. An orientation cut-out will prevent you from inserting it the wrong way.
- J. Insert the SDRAM module into the DIMM socket at a 90 degree angle. If Pin 1 of the DIMM does not line up with Pin 1 of the socket, the DIMM will not insert into the socket. After inserting the DIMM module completely into socket, push up on the socket latches securing the DIMM into place.
- 4. To release the memory module push both latches down and carefully rock the module forward.

IMPORTANT: Do not use DIMM which use an extra TTL chip to convert the memory module from asymmetric to symmetric.



Cache Expansion- L 2 ExternalPipelined Static RAM(SRAM)

The SQ593 comes standard with 256KB Pipelined burst SRAM cache. You may upgrade to 512KB using an additional 256KB COAST (cache on a stick) module. To upgrade to 512KB insert the 256KB module as shown below. Since the number of pins are different on each side of the cache module, the module will only fit one way as described in the memory section above.



On board 256KB Pipelined Burst Cache

ZIF Socket 7 for Central Processing Unit(CPU)

The SQ593 comes with a 321-pin ZIF Socket 7 for installing the CPU. The Socket 7 will also support future Pentium up-grade processors. It is strongly recommended that a heatsink and CPU cooling fan be used to prevent the CPU from overheating. (Tip) Applying a thermal of jelly between the CPU and the heatsink/fan will further cool the CPU.

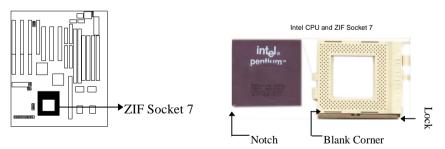
Installation Procedure(CPU)

To install a CPU, remember to take careful precaution against static electric discharge. The basic procedure is as follows:

IMPORTANT: you must set jumpers JP7, JP8, JP9 & JP10 to correct Bus frequency ratio as instructed on page 6-7 of this section

- 1. Turn off your system and disconnect the power source.
- 2. Remove the existing CPU from the ZIF socket by pulling the ZIF lever upwards at a 90 -degree angle.
- 3. Insert the CPU with the correct orientation as below shown. Use the notched corner of the CPU as your guide. The notch in the corner of the CPU should correspond with PIN1 of the ZIF socket (Notice: Pin 1 is the blank corner of the ZIF socket with one pin hole missing).
- 4. Once the CPU is inserted close the socket's lever.

WARNING: Without a fan, the CPU can overheat and will cause damage to both the CPU and the SQ593 motherboard.



ISA & PCI Slots

The SQ593 mainboard has one 8-bit ISA, three 16-bit ISA and three 32-bit PCI Slots.

Installation Procedure:

To install expansion cards, please read the expansion card's documentation for instructions.

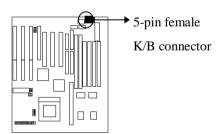
NOTICE: Some expansion cards require an IRQ to work and may cause a conflict. There are a total of 16 IRQs but only 6 are free for expansion cards. In case of a conflict please contact the system manufacturer for technical support.

- 1. There are two types of ISA expansion cards, design-Legacy and P-n-P (Plug & Play). For Legacy cards you must set the card's jumpers manually. For Plug and Play cards, your system will arrange the IRQs automatically. You can verify the IRQ allocation either by using Microsoft's Diagnostic (MSD.EXE) utility which is in the Windows directory or through the Windows 95 resources menu.
- 2. An IRQ is automatically assigned to PCI expansion cards. All of the PCI slots on the mainboard use an interrupt. Be sure that the jumpers on your PCI cards are set to interrupt A.

Connectors

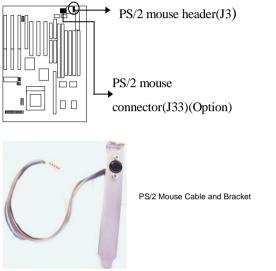
Keyboard

This connector supports a standard 101 enhanced IBM-compatible keyboard.



PS/2 Mouse Connector & PS/2 Mouse header(J33 & J3)

This SQ593 mainboard provides a default PS/2 mouse header that supports an optional PS/2 mouse connector cable & bracket







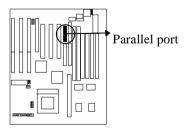
PS/2 Mouse Connector



Parallel Port (26-pin Block)

This connector supports a parallel port ribbon cable and mounting bracket. Connect the ribbon cable to this connector and mount the bracket to the back of the case.

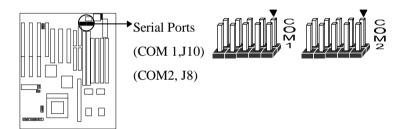
WARNING: When you connect the ribbon cable to this port, you must ensure PIN 1 of the cable matches PIN 1 of the connector.



Serial Ports (10-pin block)

These connectors support two serial port ribbon cables (Com1& Com2). Connect the ribbon cable to these connectors and mount the bracket to the back of the case.

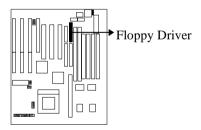
WARNING: When you connect the ribbon cable to this port, you must ensure PIN 1 of the cable matches PIN 1 of the connector.



Floppy Driver (34-pin block)

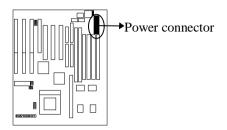
This connector supports the floppy drive via a floppy drive ribbon cable. The ribbon cable can support one to two floppy drives.

WARNING: When you connect the ribbon cable to this port, you must ensure PIN 1 of the cable matches PIN 1 of the connector.



Power Input (12-pin block)

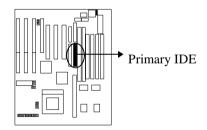
This connector supports a standard power supply. Make sure that the power cord is unplugged when you connect it. There are two power plugs, each with six colored wires. The black wires on each plug must be placed together in the middle and then pressed into the power connector on the nainboard.



Primary IDE (40-pin block)

This connector supports two primary IDE devices via a ribbon cable. When two IDE devices are installed using the primary IDE connector make sure that the second IDE device is adjusted to *slave mode* as instructed in the device's manual.

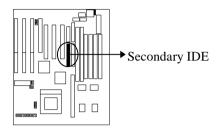
WARNING: When you connect the ribbon cable to this port, you must ensure PIN 1 of the cable matches PIN 1 of the connector.



Secondary IDE (40-pin block)

This connector supports two secondary IDE devices via a ribbon cable. When two IDE devices are installed using the secondary IDE connector make sure that the second IDE device is adjusted to *slave mode* as instructed in the device's manual.

WARNING: When you connect the ribbon cable to this port, you must ensure PIN 1 of the cable matches PIN 1 of the connector.



Universal Serial Bus (USB) Header (J35/J34)

This SQ593 mainboard has two USB headers onboard. USB devices provide a more convenient operating environment and improve data transferring capacity. True Plug-and-Play, this new bus technology will support over 127 different peripherals through a Hub.

WARNING: When you connect the cable to this port, you must ensure PIN 1 of the cable matches PIN 1 of the connector.

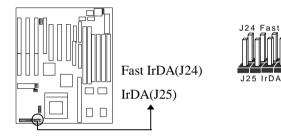


Cables USB connectors

Fast IrDA and IrDA connector (J24 & J25)

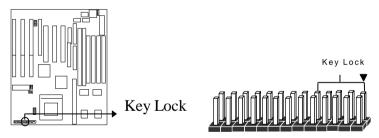
The SQ593 mainboard offers both Fast IrDA and IrDA functions. IrDA is the wireless transmitting and receiving data via an optional infrared module. The efficient operating distance is 100CM and the data transfer rates for IrDA is $\,$ 1.44M KB/sec and Fast IrDA , 4M KB/sec.

IMPORTANT: You must configure this feature through the BIOS (Please refer to item 4.9 on page 16 of section 4) by selecting whether UART2 is allocated for use by COM2 or for IrDA. The IR module is supported by the mainboard via a 5-pin connector and ribbon cable.



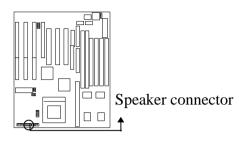
Key Lock (5-pin) (J26)

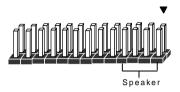
This connector supports the case-mounted switch for locking the keyboard for security purposes.



Speaker (4-pin) (J27)

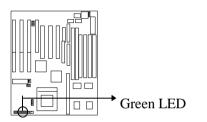
This connector supports the case-mounted speaker.

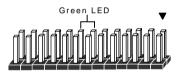




Green LED (2-pin) (J28)

This connector supports the case-mounted Green LED.





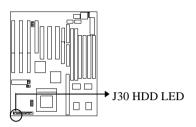
SMI Switch (2-pin)(J29)

This connector supports the case-mounted suspend switch allowing you to manually place the system into a suspend mode or "Green" mode. During Green mode, the system activity will be decreased to save energy when the system is not in use. If you want to use this feature, the "Suspend Switch" in the Power Management Setup of the BIOS should be enabled.



HDD LED (4-pin)(J30)

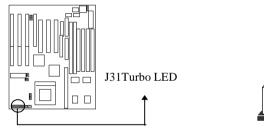
This connector supports the hard disk activity indicator light on the case.

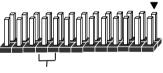




Turbo LED (2-pin)(J31)

This connector supports the turbo LED. It will always be is on because the mainboard's turbo function is always on.

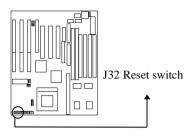






Reset Switch (2-pin)

This connector supports the case-mounted reset. It is advised that the reset switch be used for rebooting the system in order to extend the life of the system's power supply.





BIOS Setup

The Award's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backup RAM (CMOS RAM) so that it retains the Setup information when the power is turned off.

Entering Setup

Power on the computer, the below message will appear briefly at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press <Ctrl>, <Alt>, and <Esc> keys.

Press **DEL** to enter SETUP,**ESC** to skip memory test

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not reboot, an error message will be displayed at the bottom of the screen and you will again be asked to,

Press F1 to continue, DEL to enter SETUP

Control Keys

<1>, <↓>	Move to previous or next item
$\langle \leftrightarrow \rangle, \langle \rightarrow \rangle$	Move to the item in the left or right hand
<esc></esc>	Main Menu. Quit and not save changes into CMOS
	Status Page Setup Menu and Option Page Setup Menu Exit current page and return to Main Menu
<pgup> / <+></pgup>	Increase the numeric value or make changes
<pgdn> / <-></pgdn>	Decrease the numeric value or make changes
<f1></f1>	General help, only for Status Page Setup Menu and Option Page Setup Menu
Shift <f2></f2>	Change color from total 16 colors. F2 to select color forward, (Shift) F2 to select color backward
<f5></f5>	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu
<f6></f6>	Load the default CMOS value from BIOS default table, only for Option Page Setup Menu
<f7></f7>	Load the Setup default, only for Option Page Setup Menu
<f10></f10>	Save all the CMOS changes, only for Main Menu
<f3><f4></f4></f3>	Reserved
<f8><f9></f9></f8>	

Getting Help

Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

Status Page Setup Menu/Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <F1> or <Esc>.

The Main Menu

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 1) will appear on the screen. The Main Menu allows you to select from ten setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

Standard CMOS setup

This setup page includes all the items of standard CMOS setup features.

BIOS features setup

This setup page includes all the items of Award special enhanced features.

Chipset features setup

This setup page includes all the items of Chipset special features.

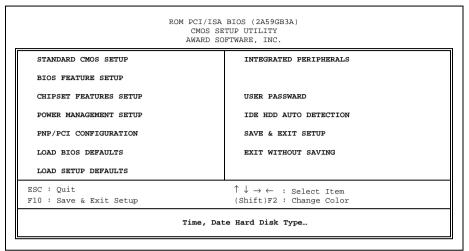


Figure 1

Power Management setup

This category determines how much power consumption for system after selecting.

PNP/PCI Configuration

This category specifies all the items of PCI/ISA devices' resources configuration and assignment.

Load BIOS defaults

BIOS defaults indicates the most appropriate value of the system parameter which the system would be in minimum performance.

Load setup defaults

Setup defaults indicates the values required by the system for the maximum performance.

Integrated peripherals

This setup page includes all the items of peripherals I/O setup features.

User Password

Change, set, or disable password. It allows you to limit access to the system and Setup, or just to Setup.

IDE HDD auto detection

Automatically configure hard disk parameters.

Save & exit setup

Save CMOS value changes to CMOS and exit setup.

Exit without save

Abandon all CMOS value changes and exit setup.

Standard CMOS Setup Menu

The items in Standard CMOS Setup Menu are divided into 10 categories. Each category includes no, one or more than one setup items. Use the arrow keys to highlight the item and then use the $\langle PgUp \rangle$ or $\langle PgDn \rangle$ keys to select the value you want in each item.

ROM PCI/ISA BIOS (2A59GB3A) STANDARD CMOS SETUP AWARD SOFTWARE, INC. Date (mm:dd:yy) : Sun, May 12 19996 Time (hh:mm:ss) : 10 : 10 : 00 HARD DISKS TYPE SIZE CYLS HEAD PRECOMP LANDZ SECTOR MODE Primary Master : Auto Primary Slave : Auto 0 0 0 0 0 0 AUTO 0 0 0 0 0 0 AUTO Secondary Master : Auto AUTO 0 0 0 0 0 0 Secondary Slave : Auto 0 0 0 0 0 0 AUTO Drive A : 1.44MB, 3.5 in. Base Memory: 640K Drive B : None Extended Memory: 31744K Other Memory: 384K Video : EGA/VGA Halt On : All Errors Total Memory: 32768K ESC : Quit $\uparrow \downarrow \rightarrow \leftarrow : \texttt{Select Item}$ PU/PD/+/- : Modify F1 : Help (Shift)F2 : Change Color

Figure 2

Date

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	The day of week, from Sun to Sat, determined by the	
	BIOS, is read only	
date	The date, from 1 to 31 (or the maximum allowed in the	
	month), can key in the numerical / function key	
month	The month, Jan through Dec.	
year	The year, depend on the year of BIOS	

Time

The time format is <hour> <minute> <second>. which accepts both function key or numerical key The time is calculated based on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

Hard Disks

The categories identify the types of hard disks that have been installed in the computer. There are 45 predefined types, 1 user definable type and 1 Auto type. The Auto Type can auto configure your hard disks.

Primary Master/Slave and Secondary Master/Slave

There are 4 items in this categories to identify the types of 2 hard disk channel that have been installed in the computer.

Press PgUp/<+> or PgDn/<-> to select a numbered hard disk type or type the number and press <Enter>. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use User Type to define your own drive type manually.

If you select User Type, related information is asked to be entered to the following items. Enter the information directly from the keyboard and press <Enter>. This information should be provided in the documentation from your hard disk vendor or the system manufacturer.

CYLS.	number of cylinders
HEADS	number of heads
PRECOMP	write precom
LANDZON	landing zone
Е	

SECTORS	number of sectors
MODE	HDD access mode

If the controller of HDD interface is ESDI, the selection shall be.Type 1..

If the controller of HDD interface is SCSI, the selection shall be.None.

If the controller of HDD interface is CD-ROM, the selection shall be.None.

If a hard disk has not been installed select NONE and press <Enter>.

Drive A/B type

The category identifies the types of floppy disk drive A or drive B that have been installed in the computer.

None	No floppy drive installed
360K, 5.25	5-1/4 inch PC-type standard drive; 360 kilobyte
in	capacity
1.2M, 5.25	5-1/4 inch AT-type high-density drive; 1.2 megabyte
in	capacity
720K, 3.5	3-1/2 inch double-sided drive; 720 kilobyte capacity
in	
1.44M, 3.5	3-1/2 inch double-sided drive; 1.44 megabyte
in	capacity

2.88M, 3.5	3-1/2 inch double-sided drive; 2.88 megabyte
in	capacity

Video

The category selects the type of adapter used for the primary system monitor that must match your video display card and monitor. Although secondary monitors are supported, you do not have to select the type in Setup.

You have two ways to boot up the system:

When VGA as primary and monochrome as secondary, the selection of the video tape is.VGA Mode.

When monochrome as primary and VGA as secondary, the selection of the video type is Monochrome mode.

EGA/VG	Enhanced Graphics Adapter/video Graphics Array.
А	For EGA, VGA, SEGA, or PGA monitor adapters.
CGA 40	Color Graphics Adapter, power up in 40 column mode
CGA 80	Color Graphics Adapter, power up in 80 column mode
MONO	Monochrome adapter, includes high resolution
	monochrome adapters

Halt on

The category determines whether the computer will stop if an error is detected during power up.

No errors	Whenever the BIOS detects a non-fatal error the
	system will be stopped and you will be
	prompted.

All errors The system boot will not be stopped for any	
	error that may be detected.
All, But	The system boot will not stop for a keyboard
Keyboard	error; it will stop for all other errors.
All, But	The system boot will not stop for a disk error; it
Diskette	will stop for all other errors.
All, But	The system boot will not stop for a keyboard or
Disk/Key	disk error; it will stop for all other errors.

Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

Base Memory

The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system. The value of the base memory is typically 512K for systems with 512K memory installed on the motherboard, or 640K for systems with 640K or more memory installed on the motherboard.

Extended Memory

The BIOS determines how much extended memory is present during the POST. This is the amount of memory located above 1MB in the CPU's memory address map.

Other Memory

This refers to the memory located in the 640K to 1024K address space. This is memory that can be used for different applications. DOS uses this area to load device drivers to keep as much base memory free for application programs. Most use for this area is Shadow RAM.

Total Memory

System total memory is the sum of base memory, extended memory, and other memory.

BIOS Features Setup Menu

	BIOS FE AWARD SC	BIOS (2A59GB3A) ATURES SETUP PFTWARE, INC.
Virus Warning CPU Internal Cache External Cache Quick Power On Self Test Boot Sequence Swap Floppy Drive Boot Up Floppy Seek Boot Up Numlock Status Boot Up System Speed	: Enabled : Enabled : C,A : Disabled : Enabled : On	Video BIOS Shadow : Enabled C8000-CBFFF Shadow : Disabled C0000-CFFFF Shadow : Disabled D0000-D3FFF Shadow : Disabled D4000-D7FFF Shadow : Disabled D8000-DBFFF Shadow : Disabled D8000-DFFFF Shadow : Disabled
Typematic Rate Setting Typematic Rate (Chars/Sec) Typematic Delay (Msec) Security Option PS/2 mouse function control PCI/VGA Palette Snoop OS Select For DRAM > 64MB	: 6 : 250 : Setup : Enabled : Disabled	ESC : Quit $\uparrow \downarrow \rightarrow \leftarrow$: Select Item F1 : Help PU/PD/+/- : Modify F5 : Old Values (Shift)F2 : Change Color F6 : Load BIOS Defaults F7 : Load Setup Defaults

Figure 3

Virus Warning

This category flashes on the screen. During and after the system boots up, any attempt to write to the boot sector or partition table of the hard disk drive will halt the system and the following error message will appear, in the mean time, you can run an anti-virus program to locate the problem.

! WARNING !

Disk boot sector is to be modified

Type "Y" to accept write or "N" to abort write

Award Software, Inc.

Enabled	Activates automatically when the system boots up causing
	a warning message to appear when anything attempts to
	access the boot sector or hard disk partition table.
Disabled	No warning message to appear when anything
	attempts to access the boot sector or hard disk
	partition table.

Note: This function is available only for DOS and other OSes that do not trap INT13.

CPU Internal Cache/External Cache

These two categories speed up memory access. However, it depends on CPU/Chipset design. The default value is Enable. If your CPU without Internal Cache then this item .CPU Internal Cache. will not be show.

Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power on the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST.

Boot Sequence

This category determines which drive computer searches first for the disk operating system (i.e., DOS).

C,A	System will first search for hard disk drive then floppy dis	k
	drive.	

A,C System will first search for floppy disk drive then hard disk drive.

Note: This function is only available for IDE type, SCSI type is always boot from A.

Swap Floppy Drive

This category allows you to swap two floppy disk drive for reading or writing data. When enabled the BIOS swaps floppy disk drive assignments, so that Drive A becomes Drive B, and Drive B becomes A.

Boot Up Floppy Seek

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360K type is 40 tracks while 760K, 1.2M and 1.44M are all 80 tracks.

Enabled	BIOS searches for floppy disk drive to determine if it is 40		
	or 80 tracks. Note that BIOS can not tell from 720K,		
	1.2M or 1.44M drive type as they are all 80 tracks.		
Disable	BIOS will not search for the type of floppy disk drive by		
d	track number. Note that there will not be any warning		
	message if the drive installed is 360K.		

Boot Up NumLock Status

Keypad is number keys when it is on and arrow keys when it is off.

Boot Up System Speed

It selects the default system speed - the speed that the system will run at immediately after power up.

Typematic Rate Setting

Enabled	Enable typematic rate and typematic delay		
	programming		
Disabled	Disable typematic rateand typematic delay programming.		
	The system BIOS will use default value of this 2 items		
	and the default is controlled by keyboard.		

Typematic Rate (Chars/Sec)

It controls the speed at which the system registers repeated keystrokes. The range is from 6 to 30 characters per second.

Typematic Delay (Msec)

It controls the time between the display of the first and second characters. There are for delay rates: 250ms, 500ms, 750ms and 1000ms.

250	250 msec
500	500 msec
750	750 msec
1000	1000 msec

Security Option

This category allows you to limit access to the system and Setup, or just to Setup.

Syste	The system will not boot and access to Setup will be
m	denied if the correct password is not entered at the
	prompt.

Setup	The system will boot, but access to Setup will be denied if
	the correct password is not entered at the prompt.

Note: To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.

PS/2 mouse function control

This category allows you to Enable or disable PS/2 Mouse function.

Enabled	Enable PS/2 mouse function
Disabled	Disable PS/2 mouse function

PCI/VGA Palette Snoop

Some display cards which are non-standard VGA (such as graphics accelerators or MPEG Video Cards) may not show colors properly. This setting enabled should fix this problem.

OS select for DRAM > 64MB

When using OS/2 operating systems with installed DRAM of greater than 64MB, you have to enable this option.

Video BIOS Shadow

It determines whether video BIOS will be copied to RAM, however, it is optional from Chipset design. Video Shadow will increase the video speed.

C8000 - DFFFF Shadow

These categories determine whether other expansion cards ' ROM BIOS will be copied to Main memory by 16K byte or 32K byte per/unit and the size depends on Chipset. Which segment should be chose depending on cards ' ROM BIOS address decoding and chip's size.

Chipset Features Setup Menu

	CHIPSET F	BIOS (2A59GB3A) EATURES SETUP DFTWARE, INC.
Auto Configuration	: Disabled	
DRAM RAS# Precharge Time DRAM R/W Leadoff Timing Fast RAS To CAS Delay DRAM Read Burst (EDO/FP) DRAM EDO Path Select Fast MA to RAS# Delay CLK Fast EDO Path Select Refresh RAS# Assertion ISA Bus Clock	: 6 : 3 : X333/X444 : Disabled : 2 : Disabled : 4 Clks	
System BIOS Cacheable Video BIOS Cacheable 8 bit I/O Recovery Time 16 bit I/O Recovery Time Memory Hole At 15M-16M	: Enabled : 2 : 2	ESC : Quit $\uparrow \downarrow \rightarrow \leftarrow$: Select Item F1 : Help $PU/PD/+/-$: Modify F5 : Old Values (Shift)F2 : Color F6 : Load BIOS Defaults F7 : Load Setup Defaults

Figure 4

Auto configuration

It defines the configuration of the settings based on the CPU clock speed.

DRAM RAS#Precharge Time

Select RAS#precharge time for DRAM access

DRAM R/W Leadoff Timing

Determine the leadoff time for R/W to the cache

Fast RAS To CAS Delay

Select the RAS-to-CAS delay time for DRAM access

DRAM Read Burst EDO/FP

Select the timing for burst read to the cache

DRAM EDO Path Select

Select the DRAM EDO path

Fast MA to RAS# Delay CLK

Select the DRAM Row Miss timings. 1=1 clock (MA setup to RAS# assertion). 0=2 clocks. Note: the timing adjustments are independent of DLT timing adjustment.

Fast EDO Path Select

Define whether or not the Fast EDO Path be selected.

Refresh RAS# Assertion

Determine the refresh rate of RAS# delay CLK.

ISA Bus Clock

Determine the clock rat of ISA bus.

System BIOS Cacheable

Define whether or not the System area to be cached by the on board cache RAM

Video BIOS Cacheable

Define whether or not the Video BIOS area to be cached by the on board cache RAM

8 Bit I/O Recovery Time

Select recovery time for 8 bit I/O

16 Bit I/O Recovery Time

Select recovery time for 16 bit I/O

Memory Hole At 15M-16M

For special old ISA card

Power Management Setup Menu

This category determines how much power consumption for system after selecting below items.

ROM PCI/ISA BIOS (2A59GB3A) POWER MANAGEMENT SETUP AWARD SOFTWARE, INC.					
Power Management PM Control by APM Video Off Method Doze Mode Standby Mode Suspend Mode HDD Power Down	: Yes : V/H SYNC+Blank : Disable : Disable : Disable	** Power Down & Resume Events ** IRQ3 (COM 2) : ON IRQ4 (COM 1) : ON IRQ5 (LPT 2) : ON IRQ5 (Floppy Disk) : ON IRQ7 (LPT1) : ON IRQ8 (RTC Alarm) : OFF IRQ9 (IRQ2 Redir) : ON			
** Wake Up Events Ir IRQ3 (Wake-Up Event IRQ4 (Wake-Up Event IRQ8 (Wake-Up Event IRQ12 (Wake-Up Event) : ON) : ON) : ON	IRQ10 (Reserved) : ON IRQ11 (Reserved) : ON IRQ12 (PS/2 Mouse) : ON IRQ13 (Coprocessor) : ON IRQ14 (Hard Disk) : ON IRQ15 (Reserved) : ON			
		ESC : Quit ↑↓→← : Select Item F1 : Help PU/PD/+/- : Modify F5 : Old Values (Shift)F2 : Color F6 : Load BIOS Defaults F7 : Load Setup Defaults			



Power Management

Options	Descriptions
1. Disable	Global Power Management will be disabled
2. User	Users can configure their own power management
Define	
3. Min	Pre-defined timer values are used such that all timers
Saving	are in their MAX value
4. Max	Pre-defined timer values are used such that all timers
Saving	MIN value

PM Control by APM

Options	Descriptions
1. No	System BIOS will ignore APM when
	power managing the system
2. Yes	System BIOS will wait for APM's
	prompt before it enter any PM mode
	e.g. DOZE, STANDBY or SUSPEND

Video Off Method

1. Blank Screen	The system BIOS will only blanks off
	the screen when disabling video
2. V/H SYNC+Blank	In addition to (1), BIOS will also turn
	off the V-SYNC & H-SYNC
	signals form VGA cards to monitor
3. DPMS	This function is enabled for only the VGA
	card supporting DPM

Doze, Standby, Suspend Mode

Defines the continuous idle time before the system entering DOZE mode. The range is from 10 min to Hr. If any item defined in (J) is enabled & active, STANDBY timer will be reloaded

Note: Normally, these modes will put the system into low speed or 8 MHz, screen may be off depend on (E)

HDD Power Down

Defines the continuous HDD idle time before the HDD entering power saving mode (motor off). The range is from 1 to 15 Mins. When it is suspended, BIOS will turn the HDD's motor off when system is in SUSPEND mode. Note: When HDD is in power saving mode, any access to the HDD will wake the HDD up.

Wake Up Events In Doze & Standby

The specified event's activity causes the PM Timers to be reloaded (i.e. the Power Management Unit (PMU) monitors the specified activities as PM events).

Power Down & Resume Events

The specified event's activity causes the PM Timers to be reloaded (i.e. the Power Management Unit (PMU) monitors the specified activities as PM events).

PCI Configuration Setup Menu

You can manually configure the PCI Device's IRQ. The following pages tell you the options of each item & describe the meanings of each options.

			AWAILD 50	FTWARE, INC.		
	es Controlled onfigruation		: Manual : Disabled	PCI IRQ Actived PCI IDE IRQ Map To Primary IDE INT	:	Level PCI-SLOT1 B
IRQ-3 IRQ-4 IRQ-5 IRQ-7 IRQ-9 IRQ-10 IRQ-11 IRQ-12 IRQ-14 IRQ-15	assigned to assigned to assigned to assigned to assigned to assigned to assigned to assigned to assigned to	: : : : : : : : : : : : : : : : : : : :	PCI/ISA PnP Legacy ISA PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP Legacy ISA Legacy ISA	Secondary IDE INT		
DMA-0 DMA-1 DMA-3 DMA-5 DMA-6 DMA-7	assigned to assigned to assigned to assigned to assigned to assigned to	::	PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP	F1 : Help	PU/PI (Shif ults	- : Select Item D/+/- : Modify t)F2 : Color

Figure 6

Resources Controlled

The default setting is Auto, you can change it to Manual for specifying individual IRQ# and DMA# for a 'Legacy' (non Plug Play) ISA card.

Reset Configuration

This setting is always disabled. The function is used for reset ESCD (Extended System Configuration Data) buffering during the POST phase on system reboot once you have enabled it.

PCI IRQ Activated by

To tell the Chipset the IRQ signals input is level or edge trigger

PCI IDE IRQ Map To

PCI-AUTO	PCI-AUTO	
	The BIOS will:	
	- scan for PCI IDE devices & determine	
	the location of the PCI IDE device	

PCI-AUTO	PCI-AUTO
	The BIOS will:
	- scan for PCI IDE devices & determine
	the location of the PCI IDE device
PCI-SLOT1	- assign IRQ 14 for primary IDE INT #
PCI-SLOT2	- assign IRQ 15 for secondary IDE INT
PCI-SLOT3	#

Primary and Secondary IDE INT#

To tell which INT3 does the PCI IDE card is using for its interrupts

Remarks: Master Arbitration Protocol, CPU->PCI Mem Post Write Buf, CPU->PCI Memory Burst Write, and PCI Master Burst Read/Write please see the referenceChipset Data Sheet.

Integrated peripherals Menu

		EGRATED PI	BIOS (2A59GB3A) ERIPHERALS SETUP FTWARE, INC.	
IDE HDD Block Mode IDE Primary Master PIO IDE Primary Slave PIO IDE Secondary Master PIO IDE Secondary Slave PIO On-Chip Primary PCI IDE On-Chip Secondary PCI IDE PCI Slot IDE 2nd Channel	: Auto : Auto : Auto : Auto : Enab : Enab	o o oled		
Onboard FDD Controller Onboard Serial Port 1 Onboard Serial Port 2 UART 2 Mode Onboard Parallel Port Onboard Parallel Mode	: 3F8/ : 2F8/ : Stan	IRQ4 IRQ3 Mard	ESC : Quit	^↓→← : Select Item
			F1 : Help	PU/PD/+/- : Modify (Shift)F2 : Color faults

Figure 6

IDE HDD Block Mode

The default setting is Enabled, this allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive (HDD).

Enabled	IDE controller uses block mode.
Disabled	IDE controller uses standard mode.

IDE PIO

IDE hard drive controllers can support up to two separate hard drives. These drives have a master/slave relationship which are determined by the cabling configuration used to attach them to the controller. Your system supports two IDE controllers--a primary and a secondary--so you have to ability to install up to four separate hard disks.

PIO means Programmed Input Output. Rather than have the BIOS issue a series of commands to effect a transfer to or from the disk drive, PIO allows the BIOS to tell the controller what it wants and then let the controller and the CPU perform the complete task by themselves. This simpler and more efficient (and faster).

Your system supports five modes, numbered from 0 (default) to 4, which primarily differ in timing. When *Auto* is selected, the BIOS will select the best available mode. This is true for the next four setup items:

IDE Primary Master PIO

IDE Primary Slave PIO

IDE Secondary Master PIO

IDE Secondary Slave PIO

On-Chip Primary PCI IDE

As stated above, your system includes two built-in IDE controllers, both of which operate on the PCI bus. This setup item allows you either to enable or disable the primary controller. You might choose to disable the controller if you were to add a higher performance or specialized controller.

Enabled	Primary HDD controller used Default
Disabled	Primary HDD controller not used.

On-Chip Secondary PCI IDE

As above for the Primary controller, this setup item you either to enable or disable the secondary controller. You might choose to disable the controller if you were to add a higher performance or specialized controller.

Enabled	Primary HDD controller used
Disabled	Primary HDD controller not used.

PCI Slot IDE 2nd Channel

This item allows you designate an IDE controller board inserted into one of the physical PCI slots as your secondary IDE controller.

Enabled	External IDE controller designated as the secondary
	controller
Disabled	No IDE controller occupying a PCI slot.

Onboard FDC Controller

This should be enabled if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. Even when so equipped, if you add a higher performance controller, you will need to disable this feature.

Enabled	Onboard floppy disk controller active (Default)
Disabled	Either onboard floppy disk controller absent of not to
	be used.

Onboard Serial Port 1

This allows you to determine how the serial port number one installed on yourmainboard is to be configured.

COM1, 3F8	IRQ 4 (Default)
COM2, 2F8	IRQ 3
COM3, 2E8	IRQ 3
COM4, 3E8	IRQ 4
Disabled	Serial port 1 disabled

Onboard Serial Port 2

This allows you to determine how the serial port number two installed on yourmainboard is to be configured.

COM1, 3F8	IRQ 4(Default)
COM2, 2F8	IRQ 3
COM3, 2E8	IRQ 3
COM4, 3E8	IRQ 4
Disabled	Serial port 2 disabled

UART 2 Mode

Makes serial port 2 available to infrared applications. Access onboard serial port 2 controller with which I/O address, along with J24 & J25. The default setting isStandard'.

Onboard Parallel Port

This can be used to change the default port address of the onboard parallel (printer) port.

378/IRQ7	Standard LPT1 address (Default)
278/IRQ5	Standard LPT2 address
3BC/IRQ7	Alternate LPT address. NOTE: Cannot
	use EPP (or ECP+EPP) with this
	configuration.
Disabled	Printer parallel port disabled

Onboard Parallel Mode

This allows you to select the operation mode of the onboard printer port.

Normal	Standard parallel port mode (Default)
EPP	Bi-directional mode
ECP	Fast, buffered

EPP/ECP Bi-directional and buffered

Password Setting

When you select this function, the following message will appear at the center of the screen to assist you in creating a password

ENTER PASSWORD:

Type the password, up to eight characters, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED.

If you select System at Security Option of BIOS Features Setup Menu, you will be prompted for the password every time the system is rebooted or any time you try to enter Setup. If you select Setup at Security Option of BIOS Features Setup Menu, you will be prompted only when you try to enter Setup.

IDE HDD Auto Detection

BIOS setup will display all possible modes that supported by the HDD including NORMAL, LBA & LARGE.

if HDD does not support LBA modes, no 'LBA' option will be shown.

if no of cylinders is less than or equal to 1024, no 'LARGE' option will be show. Users can select a mode which is appropriate for them.

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