GA - 486AS

USER'S MANUAL

; CPU VOLTAGE SELECTION; j

VOLTAGE	JP29
3.3V	Open
5V	Short

PCI-Bus 486SX / DX / DX2 / S-Series / OverDrive / P24D / P24T / DX4 Mainboard

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1INTRODUCTION

1.1 PREFACE

Welcome to use the **GA-486AS** motherboard. The motherboard is a 128 KB - 1 MB CACHE 486DX series PC/AT compatible system with ISA bus, PCI (Peripheral Component Interconnect) bus and has been designed to be the fastest 486 PC/AT system and the GREEN FUNCTION (Power-Down Mode). There are some new features allowing you to operate the system with just the performance you want.

This manual also explains how to install the motherboard for operation, and how to set up your CMOS CONFIGURATION with BIOS SETUP program.

1.2 KEY FEATURES

- 80486 based PC/AT compatible mainboard with ISA and PCI Bus.
- Supports 3 Master PCI bus slots .
- Supports 486SX / DX / DX2 / S-Series / OverDrive / P24D / P24T / DX4 running at 25 - 100 MHz.
- Supports True Green Function.
- Supports Intel, AMD, Cyrix and UMC CPU.
- Supports 237 pins (Socket 3) ZIF White socket / LIF socket on board.
- Supports 128 / 256 / 512 KB / 1 MB 2nd cache memory operated in BURST mode.
- Supports Write-Through / Write-Back cache operation.
- Supports 1 128 MB DRAM memory on board.
- Supports 2 channels Enhance PCI IDE ports for 4 IDE Devices.
- Supports 2 x COM (16550) ports, 1 x LPT (EPP / ECP) port, 1 x Floppy port.
- Supports shadow RAM for BIOS & VIDEO BIOS.
- Supports shadow RAM cacheable function to improve performance.
- Supports Hardware & Software speed change function.
- Licensed AWARD BIOS.
- 2/3 Baby AT size (22 cm x 25 cm) with 4 AT slots and 3 PCI slots .

1.3 PERFORMANCE LIST

The following performance data list is the testing results with some popular benchmark testing programs. These datas are just reference for users and there is no responsibility for different testing data values gotten by users.(The different H/W & S/W configuration will result in different benchmark testing results.)

System Benchmarks At DOS Environment with IDE Controller

OOS\SETVER.EXE C:\DOS\SMARTDRV.EXE /X
OS\HIMEM.SYS PROMPT \$p\$g
PATH C:\;C:\DOS
SET TEMP = C:\DOS

i °Using Intel DX4-100 / 75

Software	Item	Unit	P24T 63 WB	DX4-100x3	DX4-75x3
LandMark Speed	CPU	MHz	361.38	364.10	273.05
V2.0	FPU	MHz	1202.89	885.38	663.97
	Video	chr/ms	7620.00	10031.00	7620.00
Norton SI	CPU	index	198.4	198.5	148.8
V8.0	DISK	index	15.5	15.5	15.5
	Overall	index	Unit P24T 63 WB Hz 361.38 Hz 1202.89 r/ms 7620.00 Jex 198.4 Jex 15.5 Jex 137.0 ps 28.2 MUs 45015.0 3/sec 32233.8 MUs 4888.7 Jex 579.96 Jex 111.40 Jex 384.71	137.4	104.3
Power Meter	MIPS	Mips	28.2	28.7	21.7
V1.81	CPU/MEM Agg.	PMUs	45015.0	39212.0	29354.0
	Data-Xfer	KB/sec	32233.8	32233.8	32233.8
	DISK Agg.	PMUs	4888.7	4883.6	4886.6
PC Bench mark	DOS Mark	index	579.96	605.81	484.26
V9.0	CPU mark 16	index	111.40	113.52	84.78
	Video Score	index	3417.32	4431.14	3481.2
	DISK Score	index	384.71	392.68	337.60

Software	Item	Unit	DX2-66	DX2-50
LandMark Speed	CPU	MHz	223.52	167.63
V2.0	FPU	MHz	590.23	442.64
	Video	chr/ms	10031.00	7620.00
Norton SI	CPU	index	144.3	108.2
V8.0	DISK	index	15.6	15.6
	Overall	index	101.3	77.2
Power Meter	MIPS	Mips	21.1	15.7
V1.81	CPU/MEM Agg.	PMUs	26242.3	19423.7
	Data-Xfer	KB/sec	32233.8	32233.8
	DISK Agg.	PMUs	4883.6	4885.3
PC Bench mark	DOS Mark	index	481.40	378.25
V9.0	CPU mark 16	index	84.07	62.78
	Video Score	index	4411.34	3461.05
	DISK Score	index	322.00	267.76

Introduction



1.5 INTRODUCE THE PCI-BUS

Connecting devices to a local bus of CPU can dramatically increase the speed of I/Obound peripherals with only a slight increase in cost over traditional systems. This price/performance point has created a vast market potential for local bus products. The main barrier to this market has been the lack of an accepted standard for local bus peripherals.

The PCI-bus standard, under development since Jun. 1992, which is designed to bring workstation-level performance to standard PC platform. The PCI-bus removes many of the bottlenecks that have hampered PC for several years. On the PCI-bus, peripherals operate at the native speed of the computer system, thus enabling data transfer between peripherals and the system at maximum speed. This performance is critical for bandwidth-constrained devices such as video, multimedia, mass storage and networking adapters.

PCI-bus standard provide end-users with a low-cost, extendible and portable local bus design, which will allow systems and peripherals from different manufacturers to work together.

2 SPECIFICATION

2.1 HARDWARE

• CPU	 80486SX/DX/DX2/S-Series, 80487SX, OverDrive, P24D, P24T, DX4.
	 – 237 pins (Socket 3) ZIF white socket / LIF socket on board.
 COPROCESSOR 	 Coprocessor included in 80486DX.
• SPEED	 25 / 33 MHz system 25 - 33 MHz PCI- bus speed. 8 MHz (programmable) AT Bus speed. Hardware and Software speed switchable function.
GREEN FUNCTION	 Support Suspend mode 10 sec to 40 min. Power Down Timer from 0.25 min to 512 min. When enter Power Down Mode, 16 MHz system speed for non S-Series and 0 MHz system speed for Intel & Cyrix S-Series.
	 Ext. Power Control Port for Monitor Power ON / OFF Support IDE Hard Disk Standby Mode control. Wake Up by all IRQ, DMA, and PCI Master Devices. Support Green LED Indicator and Green Switch.
DRAM MEMORY	 4 banks 72 pins SIMM module sockets on board. DRAM type can be 256K / 512K x 32 / 36, 1M / 2M x 32 / 36, 4M / 8M x 32 / 36. Support Fast Page DRAM access mode.
CACHE MEMORY	 8 KB cache memory included in 80486 DX / SX / DX2 / SX2 and AMD DX4. 16 KB cache memory included in INTEL DX4. 128 / 256 / 512 KB / 1 MB 2nd cache memory on board. Support 486 Burst mode on 2nd cache memory access.
SHADOW RAM	 Main BIOS shadow function programmable. Video BIOS shadow function programmable. Shadow RAM cacheable function programmable.
IDE PORTS	 2 Enhanced IDE channels on board.(Using IRQ14,15) Support Mode 4 IDE & ATAPI CD-ROM. Driver Support DOS, WINDOWS, OS/2, NT, Novell.
• I/O PORTS	 Supports 2 x 16550 COM ports. (Using IRQ4,3) Supports 1 x EPP/ECP LPT port. (Using DMA3 & IRQ7) Supports 1 x Floppy port. (Using DMA2 & IRQ6)
• I/O BUS SLOTS	– 4 AT-bus. – 3 PCI-bus.
 DIMENSION 	- 2/3 Baby AT size (22 cm x 25 cm).

Specification

2.2 SOFTWARE

• BIOS	 Licensed AWARD BIOS.

- AT CMOS Setup, Advanced / Chipset Setup and Power Management included.
- O. S. Operation with MS-DOS, WINDOWS NT,OS/2, NOVELL, SCO UNIX.

2.3 ENVIRONMENT

• AMBIENT TEMP. - °C to +50 C (operating).

_

- 0 to +85% (operating).
- ALTITUDE
- VIBRATION
- •

•

- 0 to 1,000 Hz. - 4.9 V to 5.2 V.
 - 3 A to 5 A current.

3 HARDWARE INSTALLATION

3.1 UNPACKING

The ainboard ackage hould ontain he ollowing:

- The A-486AS ainboard
- User's manual
- A Driver Diskette

The mainboard contains sensitive electric components which can be easily damaged by static electricity, so the mainboard should be left in its original packing until it is installed.

Unpacking and installation should be done on a grounded anti-static mat. The operator should be wearing an anti static wristband, grounded at the same point as the anti-static mat.

Inspect the mainboard carton for obvious damage. Shipping and handling may cause damage to your board. Be sure there are no shipping and handling damages on the board before proceeding.

After opening the mainboard carton, extract the system board and place it only on a grounded anti-static surface component side up. Again inspect the board for damage. Press down on all of the socket IC's to make sure that they are properly seated. Do this only on with the board placed on a firm flat surface.

Do not apply power to the board if it has been damaged..

You are now ready to install your mainboard. The mounting hole pattern on the mainboard matches the IBM-XT system board. It is assumed that the chassis is designed for a standard IBM XT/AT mainboard mounting.

Place the chassis on the anti-static mat and remove the cover. Take the plastic clips, Nylon stand-off and screws for mounting the system board, and keep them separate.

A summary of Jumper Setting

i iIntel 486 Series i j

CPU Type	JP14	RN1	RN2	RN3	JP30	JP31	JP20	JP23	JP27	JP28	JP29
SX-25/SX2-50	Х	Х	0	0	Х	0	Х	0	2-3	2-3	0
DX-25/DX2-50	Х	Х	0	0	Х	0	Х	0	2-3	1-2	0
DX-33/DX2-66	0	Х	0	0	Х	0	Х	0	2-3	1-2	0
P24T-63 WT	Х	Х	0	0	Х	0	Х	0	2-3	1-2	0
P24T-63 WB	Х	Х	0	0	Х	0	Х	Х	2-3	1-2	0
P24T-83 WT	0	Х	0	0	Х	0	Х	0	2-3	1-2	0
P24T-83 WB	0	Х	0	0	Х	0	Х	Х	2-3	1-2	0
P24D WT	0	Х	0	0	Х	0	Х	0	2-3	1-2	0
P24D WB	0	Х	0	0	Х	0	Х	0	1-2	1-2	0
DX2-50 OverDrive	Х	Х	0	0	Х	0	Х	0	2-3	1-2	0
DX2-66 OverDrive	0	Х	0	0	Х	0	Х	0	2-3	1-2	0
DX4-75 OverDrive	Х	Х	0	0	Х	0	Х	0	2-3	1-2	0
DX4-100 OverDrive	0	Х	0	0	Х	0	Х	0	2-3	1-2	0
DX4-75 ; \$.3 V/ ^	Х	Х	0	0	Х	0	Х	0	2-3	1-2	Х
DX4-100 3.3 V/	0	Х	0	0	Х	0	Х	0	2-3	1-2	Х

; iAMD 486 Series; j

 CPU Type
 JP14
 RN1
 RN2
 RN3
 JP30
 JP20
 JP23
 JP27
 JP28
 JP29

Hardware Installation

Enhanced DX2-66 WT; \$.3 V; ^	0	Х	0	0	Х	Х	0	0	2-3	1-2	Х
Enhanced DX2-66 WB 3.3 V ^	0	Х	0	0	Х	Х	0	0	1-2	1-2	Х
Enhanced DX4-100 WT; 3.3 V/ /	0	Х	0	0	Х	Х	Х	0	2-3	1-2	Х
Enhanced DX4-100 WB; 3.3 V/	0	Х	0	0	Х	Х	Х	0	1-2	1-2	Х
DX2-66 N. E.; 3.3 Vi ^	0	Х	0	0	Х	Х	Х	0	2-3	1-2	Х
DX4-100 N. E.; 3.3 Vi ^	0	Х	0	0	Х	Х	Х	0	1-2	1-2	Х

i Cyrix 486 Series i j

CPU Type	JP14	RN1	RN2	RN3	JP30	JP31	JP20	JP23	JP27	JP28	JP29
DX2-50	Х	0	Х	Х	0	Х	Х	0	2-3	1-2	0
DX2-66	0	0	Х	Х	0	Х	Х	0	2-3	1-2	0
DX2-66 (3.3V)	0	0	Х	Х	0	Х	Х	0	2-3	1-2	Х

iUMC 486 Series i j

CPU Type	JP14	RN1	RN2	RN3	JP30	JP31	JP20	JP23	JP27	JP28	JP29
U5SX-33	0	Х	0	0	Х	Х	Х	0	2-3	2-3	0

O: Jumper or Resistor Installed

X: Jumper or Resistor Not Installed

3.2 MAINBOARD LAYOUT





3.3 JUMPERS AND CONNECTORS SETTING

 J2: Res 	et Connector	
Open	Normal operation.	
Close	Hardware reset system.	
+ J3: Turb	o Switch Connector	
1 - 2	Low speed.	
2 - 3	High speed. (Default)	
+ J5: Exte	rnal Battery Connector	
1	Battery Anode (+).	
2	NC.	
3	GND.	
4	Battery Cathode(-).	
◆ JP1: HD	D LED (On board layout, "+" is LED anode)	
	Onboard IDE Hard disk Read/Write indicator.	
+ JP2: Tu	rbo LED (On board layout, "+" is LED anode)	
	This jumper is for Turbo LED.	
	· · ·	
 JP3: Sp 	eaker Connector	
1	Data.	
2	GND.	
3	GND.	
4	VCC (+5V).	
 JP5: CN 	IOS Data Clear / Power Supply Jumper.	
1 - 2	Close for Normal Operation.	
2 - 3	Close for Not Supplying Power to CMOS RTC (Clear).	
 J4: Pow 	er LED & Key-Lock Connector	
1	LED anode (+).	
2	NC.	
3	LED cathode (-).	
4	Key-lock.	
5	GND.	
 J9: Keyl 	poard Connector	
1	Keyboard clock.	
2	Keyboard data.	
3	NC.	
4	VCC (+5V)	
5	GND	
U		

Hardware Installation

I7: Power Supply Connector		
- 07.1 OWCI C		-
	Power good signal.	1
2,10,11,12	VCC (+5V).	l
3	+12V.	
4	-12V.	
5,6,7,8	GND.	
9	-5V.	

GREEN FUNCTION JUMPER SETTING

J1: Brake Switch		
	Shorting this jumper and then releasing it immediately will force system into Green mode right away.	

 JP4: Gree 	n LED (On board layout, "+" is LED anode)	
	When system gets into Green, the LED on this jumper will be light.	

 JP24: Green Power Connector (On board layout, "+" is positive voltage) 		
	If your power supply has signal to control the A.C. output, and then the signal can be connected to this connector; when system gets into	
	Green mode, your power supply A.C. output will be turned OFF. By this way, you can control your monitor ON/OFF or other devices.	

3.4SRAM INSTALLATION AND JUMPERS SETUP

The cache memory system consists of two parts, one is TAG SRAM, the other is DATA SRAM. The TAG SRAM type used in this mainboard is 8Kx8, 16Kx8, 32Kx8 or 64Kx8 - 15ns, and the DATA SRAM type is 8Kx8-15ns, 32Kx8-15ns 64Kx8-20ns or 128Kx8-20ns.

The mainboard can be installed with 128, 256, 512 KB or 1MB cache memory when using 8Kx8 or 32Kx8 or 64Kx8 or 128Kx8 type DATA SRAM respectively.

1 BANK	128K			
2 BANK		256K	512K	1M
JP7	OFF	ON	ON	ON
JP8	OFF	OFF	ON	ON
JP9	OFF	OFF	OFF	ON
JP10	1 - 2	2 - 3	2 - 3	2 - 3
U15	8K x 8	16K x 8	32K x 8	64K x 8
U6-U9	32K x 8	32K x 8	64K x 8	128K x 8
U17-U20		32K x 8	64K x 8	128K x 8

CACHE MEMORY SIZE SETTING

3.5 CPU INSTALLATION AND JUMPERS SETUP

The CPU is a sensitive electric component and it can be easily damaged by static electricity, so users must keep it away from metal surface when the CPU is installed onto mainboard.

When the user installs the CPU on socket, please notice the PIN 1 of CPU is in the same corner as the PIN 1 of socket!

Before the CPU is installed, the mainboard must be placed on a flat plane in order to avoid being broken by the pressure of CPU installation.

• CPU CLOCK SETTING

The system speed depends on the frequency of CLOCK GENERATOR. The user can change the clock selection jumper to set up the system speed at 25, 33 ,40 or 50 MHz for different CPU speeds.

The mainboard can use 80486DX, DX2, SX, SX2 OverDrive, P24T, P24D and DX4 CPU, and the CPU speed must match with the frequency of CLOCK GEN. It will cause system hanging up if the CLOCK GEN.'S frequency is higher than CPU's.

Refer to the following table to set up the CPU clock:

CPUCLK	25 MHz	33 MHz
JP14	OFF	ON

CPU BRAND SELECTION

BRAND	JP30	JP31	RN1	RN2	RN3
AMD, UMC	OFF	OFF	OFF	Installed	Installed
Intel	OFF	ON	OFF	Installed	Installed
Cyrix	ON	OFF	Installed	OFF	OFF

CPU TYPE SELECTION

 JP20: 	
Short	DX4 x 2 for Intel & AMD Enhanced.
Open	DX4 x 3 for Intel & AMD Enhanced.

• JP23:		
Short	P24T WT.	
Open	P24T WB.	

◆ JP27:

01 21	•
1 - 2	P24D & AMD Enhance WB, AMD DX4 x 3 (Not Enhance Type).
2 - 3	P24D & AMD Enhance WB, AMD DX4 x 2 (Not Enhance Type).

+ JP28:	: DX or SX CPU Selection
1 - 2	For DX, DX2 OverDriver, DX4 Type CPU.
2 - 3	For SX Type CPU.

Note: Please remember set CPU internal cache to WB in BIOS CHIPSET FEATURES SETUP, if you already set JP23 or JP27 to support WB CPU.

CPU VOLTAGE SELECTION

	VOLTAGE	JP29
	3.3V	Open
ĺ	5V	Short

3.6 DRAM INSTALLATION

This mainboard can be installed with 1, 2, 4, 8, 16 or 32 MB 72 pins SIMM module DRAM. The DRAM speed of both mainboard is using 60/70 ns. The banks of memory system on the mianboard includes from SIMM 1 to SIMM 4.

The DRAM installation position refer to MAINBOARD LAYOUT, and notice the PIN -1 of SIMM module must match with the PIN -1 of SIMM socket when the DRAM SIMM module is installed.

Insert the DRAM SIMM module into the SIMM socket at 45 degree angle. If there is a wrong direction of PIN -1, the DRAM SIMM module couldn't be inserted into socket completely. After completely insert SIMM module into socket, then press the SIMM module in vertical direction until the left and right metal holders can keep the SIMM module standing up con-firmly.

3.7 SPEAKER CONNECTOR INSTALLATION

There is always a speaker in AT system for sound purpose. The 4-Pins connector (JP3) is used to connect speaker. The speaker can work well in both direction of connector when it is installed to the connector (JP3) on mainboard.

3.8 POWER LED & KEY LOCK CONNECTOR INSTALLATION

There are a system power LED lamp and a key on the panel of case. The power LED will light on when system is powered-on, and the key can lock the keyboard input or unlock it, both of them are connected to a 5 PIN connector. The connector should be installed to J4 of mainboard in correct direction.

3.9 TURBO SWITCH CONNECTOR INSTALLATION

The TURBO switch on the panel is used for controlling the system speed. Some program developed on XT should be executed with a low speed system, so a high speed system needs the speed switching function to change its running speed.

Because a 80486 CPU cannot accept the real clock speed change when program is executed, so the mainboard uses cache-enable or disable function to simulate TURBO switching function. The J3 on mainboard should be connected to the TURBO switch on panel, and user can push in or pop out the TURBO switch to enable or disable the cache function of system.

3.10TURBO LED CONNECTOR INSTALLATION

The TURBO LED on panel can indicate the current speed status of system. The TURBO LED connector should be installed to JP2 in correct direction.

3.11HARDWARE RESET SWITCH CONNECTOR INSTALLATION

The Reset switch on panel provides users with Hardware Reset function which is almost the same as power on / off. The system will do a cold start after the Reset switch is pushed and released at once. The Reset switch is a 2 PIN connector and should be installed to J2 on mainboard.

3.12GREEN FUNCTION INSTALLATION

For the purpose of power saving, there are two jumpers, J1 and JP4, to make sure the power saving function doing well. The JP4 is a indicator (Green LED) for Green function.

If the green LED is ON, the system is operating in green mode. The J1 is a switch to force the system into Green mode immediately.

3.13PERIPHERAL DEVICE INSTALLATION

If a PCI-bus device is to be installed in the system, any one of three PCI-bus slots can be used no matter what Slave or Master PCI-bus device being installed.

After the peripheral device installed, the user should check everything again, and prepare to power-on the system. Now, the mainboard can be mounted into the case and fixed with screws.

4 BIOS CONFIGURATION

Award's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS SRAM so that it retains the Setup information when the power is turned off.

4.1 ENTERING SETUP

Power ON the computer and press immediately will allow you to enter Setup. The other way to enter Setup is to power on the computer, when the below message appears briefly at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press <Ctrl>,<Alt>,and <Esc> keys.

• TO ENTER SETUP BEFORE BOOT PRESS CTRL-ALT-ESC OR DEL KEY

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" bottom on the system case. You may also restart by simultaneously press <Ctrl>,<Alt>,and keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to,

• PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP

4.2 CONTROL KEYS

Up arrow	Move to previous item			
Down arrow	Move to next item			
Left arrow	Move to the item in the left hand			
Right arrow	Move to the item in the right hand			
Esc key	Main Menu - Quit and not save changes into CMOS			
	Status Page Setup Menu and Option Page Setup Menu - Exit current			
	page and return to Main Menu			
PgUp key	Increase the numeric value or make changes			
PgDn key	Decrease the numeric value or make changes			
F1 key	General help, only for Status Page Setup Menu and Option Page			
	Setup Menu			
F2 key	Change color from total 16 colors			
F3 key	Calendar, only for Status Page Setup Menu			
F4 key	Reserved			
F5 key	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu			
F6 key	Load the default CMOS value from BIOS default table, only for			
	Option Page Setup Menu			
F7 key	Load the default			
F8 key	Reserved			
F9 key	Reserved			
F10 key	Save all the CMOS changes, only for Main Menu			

4.3 GETTING HELP

4.3.1Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

4.3.2Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlight item. To exit the Help Window press <Esc>.

4.4 THE MAIN MENU

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 1) will appear on the screen. The Main Menu allows you to select from nine setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

; Figure 1; j

ROM PCI/ISA BIOS (2A4IBG01) CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	PASSWORD SETTING				
BIOS FEATURES SETUP	IDE HDD AUTO DETECTION				
CHIPSET FEATURES SETUP	HDD LOW LEVEL FORMAT				
POWER MANAGEMENT SETUP	SAVE & EXIT SETUP				
PCI/IDE CONFIGURATION SETUP	EXIT WITHOUT SAVING				
LOAD BIOS DEFAULTS					
LOAD SETUP DEFAULTS					
ESC : Quit $\land \lor \lor \diamond \leftarrow$: Select Item					
F10 : Save & Exit Setup	(Shift)F2 : Change Color				
Time, Date, Hard Disk Type					

• Standard CMOS setup

This setup page includes all the items in a standard compatible BIOS.

BIOS features setup

This setup page includes all the items of Award special enhanced features.

• Chipset features setup

This setup page includes all the items of chipset special features.

Power Management Setup

This setup page includes all the item of power management features.

• PCI/IDE Configuration Setup

This setup page includes all the configurations of PCI slots parameters and onboard I/O setting.

Load BIOS Defaults

BIOS defaults indicates the most appropriate value of the system parameter which the system would be on more safety operation.

• Load SETUP Defaults

SETUP defaults indicate the most appropriate value of the system parameter which the system would be in maximum performance.

Password setting

Change, set, or disable password. It allows you to limit access to the system and Setup, or just to Setup.

IDE HDD auto detection

Automatically configure hard disk parameter.

HDD low level format

HDD low level format utility.

Save & exit setup

Save CMOS value changes to CMOS and exit setup.

Exit without save

Abandon all CMOS value changes and exit setup.



4.5 STANDARD CMOS SETUP MENU

The items in Standard CMOS Setup Menu (Figure 2) are divided into 11 categories. Each category includes none, one or more than one setup items. Use the arrows to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

; Figure 2; j

ROM PCI/ ISA BIOS (2A4IBG01) STANDARD CMOS SETUP AWARD SOFTWARE, INC.

Date (mm:dd:yy) Time (hh:mm:ss)	: M : 1	on, May 5 : 55	08 1	995				
HARD DISKS	TYPE	SIZE	CYLS	HEAD	PRECOM	IP LANDZ	SECTOR	MODE
Primary Master	: User	540	1048	16	65535	1047	63	NORMAL
Primary Slave	: None	0	0	0		0 0	0	
Secondary Master	: None	0	0	0		0 0	0	
Secondary Slave	: None	0	0	0		0 0	0	
Drive A : 1.44 Drive B : 1.2 M	M, 3.5 in. 4, 5.25 in.				Ext	Base Memor	y: 6 7: 317	540 K 44 K
Video : EGA	/VGA					Other Memor	y: 3	084 K
Halt On : No E	rrors					Total Memor	y: 327	68 K
ESC : Quit		\uparrow	\downarrow \rightarrow	- Sel	ect Item		PU/PD/+/-	: Modify
F1 : Help		(Sh	ift)F2	: Chan	ge Color			

• Date

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	The day, from Sun to Sat, determined by					
	the BIOS and is display-only					
dat	The date, from 1 to 31 (or the maximum					
e	allowed in the month)					
mon	The month, Jan. through Dec.					
th						

BIOS Configuration

yea	The year,	from 1900 t	through	2099
r				

• Time

The time format in <hour> <minute> <second>. The time is calculated base on the 24-hour military-time clock. For example, 1 p.m is 13:00:00.

• Primary Master / Slave, Secondary Master / Salve

The category identify the types of hard disk drive C or drive F 4 devices that has been installed in the computer. There are 46 predefined types and a user definable type. Type 1 to Type 46 are pre-defined. Type User is userdefinable.

Press PgUp or PgDn to select a numbered hard disk type or type the number and press <Enter>. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.

If you select Type User, related information is asked to be entered to the following items. Enter the information directly from the keyboard and press <Enter>. Those information should be provided in the documentation form your hard disk vendor or the system manufacturer.

CYLS	number of cylinders	
HEAD	number of heads	
PRECOMP	write precom	
LANDZ	landing zone	
SECTOR	number of sectors	

If a hard disk has not been installed select NONE and press <Enter>.

• Drive A type / Drive B type

The category identify the types of floppy disk drive A or drive B that has been installed in the computer.

None	No floppy drive installed				
360K,	5-1/4 inch PC-type double-sided				
5.25 in.	drive; 360 kilobyte capacity				
1.2M,	5-1/4 inch AT-type double-sided and				
5.25 in.	high-density drive; 1.2 megabyte				
	capaci ty				
720K,	3-1/2 inch double-sided drive; 720				
3.5 in.	kilobyte capacity				
1.44M,	3-1/2 inch double-sided and high-				
3.5 in.	density drive; 1.44 megabyte capacity				
2.88M	3-1/2 inch double-sided drive; 2.88				
3.5 in.	megabyte capacity				

• Video

The category detects the type of adapter used for the primary system monitor that must match your video display card and monitor. Although secondary monitors are supported, you do not have to select the type in setup.

EGA/VG	Enhanced Graphics Adapter/Video
Α	Graphics Array. For EGA, VGA, SVGA, or
	PGA monitor adapters.
CGA 40	Color Graphics Adapter, power up in 40
	column mode.
CGA 80	Color Graphics Adapter, power up in 80

	colum mode.			
MON0	Monochrome	adapter,	includes	hi gh
	resolution r	monochrome a	adapters.	

• Halt on

The category determines whether the computer will stop if an error is detected during power up.

All errors	Whenever the BIOS detects a non-					
	fatal error the system will be					
	stopped and you will be prompted					
No errors	The system boot will not stop for					
	any error that may be detected					
All, But	The system boot will not stop for					
Keyboard	a keyboard error; it will stop for					
-	all other errors					
All, But	The system boot will not stop for					
Diskette	a disk error; it will stop for all					
	other errors					
All, But	The system boot will not stop for					
Di sk/Key	a keyboard or disk error; it will					
-	stop for all other errors					

• Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

Base Memory

The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system The value of the base memory is typically 512 K for systems with 512 K memory installed on the motherboard, or 640 K for

systems with 640 K or more memory installed on the motherboard.

Extended Memory

The BIOS determines how much extended memory is presented during the POST. This is the amount of memory located above 1 MB in the CPU's memory address map.

Expanded Memory

Expanded Memory in memory defined by the Lotus/Intel/Microsoft (LIM) standard as EMS. Many standard DOS applications can not utilize memory above 64KB, the Expanded Memory Specification (EMS) swaps memory which is not utilized by DOS with a section, or frame, so these applications can access to all of the system memory. Memory can be swapped by EMS is usually 64KB within 1 MB or memory above 1 MB, depending on the chipset design.

Expanded memory device driver is required to use memory as Expanded Memory.

Other Memory

This refers to the memory located in the 640KB to 1024KB address space. This is the memory that can be used for different applications. DOS uses this area to load device drivers to keep as much base memory free for application programs. Most use for this area is Shadow RAM

BIOS Configuration

4.6 BIOS FEATURES SETUP

ROM PCI/ISA BIOS (2A4IBG01)						
BIOS FEATURES SETUP						
AWARD SOFTWARE, INC.						
Security Option	: Setup	Video BIOS Shadow	: Enabled			
Virus Warning	: Disabled	C8000 - CFFFF Shadow	: Disabled			
CPU Internal Cache	: Enabled	D0000 - D7FFF Shadow	: Disabled			
External Cache	: Enabled	D8000 - DFFFF Shadow	: Disabled			
Quick Power On Self Test	: Enabled					
Boot Sequence	: A, C					
Swap Floppy Drive	: Disabled					
Boot Up Floppy Seek	: Enabled					
Boot Up NumLock Status	: Off					
Turbo SW Function Enable	: Yes					
		ESC : Quit	\rightarrow \leftarrow : Select Item			
		F1 : Help PU	J/PD/+/- : Modify			
		F5 : Old Values (SI	nift)F2 : Color			
		F6 : Load BIOS Defaults				
		F7 : Load Setup Defaults	ŝ			

Security Option

The default value is Setup.

Setup	Asking password when enter CMOS Setup.
System	Asking password when enter CMOS Setup and boot system.

• Virus Warning

This category flashes on the screen. During and after the system boots up, any attempt to write to the boot sector or partition table of the hard disk drive will halt the system and the following error message will appear, in the mean time, you can run anti-virus program to locate the problem. The default value is Disabled.

Enabled	Activate automatically when the system boots up and causes a warning message to appear when anything attempts to access to the boot sector or hard disk partition table.
Disabled	No warning message to appear when anything attempts to access to the boot sector or hard disk partition table.

• CPU Internal Cache / External Cache

These two categories speed up memory access. However, it depends on CPU / chipset design. The default value is Enabled.

Enabled	Enable cache.
Disabled	Disable cache.



Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power on the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST. The default value is Enabled.

Enabled	Enable quick POST.
Disabled	Normal POST.

Boot Sequence

This category determines which drive computer searches first for the disk operating system (i.e., DOS). The default value is A,C.

A,C	System will first search for floppy disk drive and then hard disk drive.
C,A	System will first search for hard disk drive and then floppy disk drive.

Swap Floppy Drive

The default value is Disabled.

Enabled	Floppy A & B will be swapped under DOS.
Disabled	Floppy A & B will be normal definition.

Boot Up Floppy Seek

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360 type is 40 tracks while 720,1.2 and 1.44 are all 80 tracks.

The default value is Enabled.

Enabled	BIOS searches for floppy disk drive to determine if it is 40 or 80 tracks, Note that BIOS cannot tell from 720, 1.2 or 1.44 drive type as they are all 80 tracks.
Disabled	BIOS will not search for the type of floppy disk drive by track number. Note that there will not be any warning message if the driver installed is 360.

Boot Up NumLock Status

The default value is Off.

On	Keypad is number keys.
Off	Keypad is arrow keys.

Turbo SW Function Enable

The default value is Yes.

Yes	Enable IDE HDD Block Mode.
No	Disable IDE HDD Block Mode.

Video BIOS Shadow

It determines whether video BIOS will be copied to RAM, however, it is optional from chipset design. Video Shadow will increase the video speed. The default value is Enabled.

Enabled	Video shadow is enabled.
Disabled	Video shadow is disabled.

• C8000 - CFFFF Shadow / D0000 - DFFFF Shadow

These categories determine whether optional ROM will be copied to RAM by 16K byte. The default value are Disabled.

Enabled	Optional shadow is enabled.
Disabled	Optional shadow is disabled.

4.7 CHIPSET FEATURES SETUP

ROM PCI/ISA BIOS (2A4IBG01) CHIPSET FEATURES SETUP AWARD SOFTWARE, INC.

Auto Configuration	: Enable	Onboard FDD Controller	: Enabled
ISA Bus Clock	: 1/4 PCLK	Onboard Parallel Mode	: SPP Mode
Cache Write Cycle	: 2 CCLK	Onboard Parallel Port	: 378H
Cache Burst Read Cycle	: 1 CCLK	Onboard Serial Port 1	: COM1
L2 Cache / DRAM Cycle WS	: 2 CCLK	Onboard Serial Port 2	: COM2
DRAM Speed	: Fastest		
CPU Internal Cache	: Write Thru		
		ESC : Quit $\land \lor \lor \rightarrow$: Select Item
		F1 : Help PU/PD/+/-	: Modify
		F5 : Old Values (Shift)F2	: Color
		F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

Auto Configuration

The default value is Enable.

Enable	To Enable auto configuration function.
Disable	To Disable auto configuration function.

ISA Bus Clock

The default value is 1/4 PCLK.

1/3 PCLK	For 25 MHz system.
1/4 PCLK	For 33 MHz system.

Cache Write Cycle

The default value is 2 CCLK.

2 CCLK	For system speed ;	Ø33 MHz.
3 CCLK	For system speed ;	ÖJ3 MHz.

Cache Burst Read Cycle • Cache Burst Read Cycle can be set to 1 CCLK, 2 CCLK. The default value is 1 CCLK. 2 CCLK For system speed ; Ø3 MHz. 3 CCLK For system speed ; ÖJ3 MHz. • L2 Cache / DRAM Cycle WS The default value is 2 CCLK. 2 CCLK For system speed ; Ø33 MHz. 3 CCLK Ö33 MHz. For system speed ; DRAM Speed The default value is Fastest. When adaptec 3940 or PCI to PCI Bridge CARD installed. SupFast System speed ; Ø33 MHz. Fastest Faster System speed ; Ö3 MHz. CPU Internal Cache ٠ This option is for Cyrix or P24T CPU only. The default value is Write Thru. Using write through for the configuration of CPU internal cache. Write Thru Write Back Using write back for the configuration of CPU internal cache Onboard FDD Controller The default value is Enabled.

Enabled	Enable Onboard FDD Controller.
Disabled	Disable Onboard FDD Controller.

Onboard Parallel Mode

The defaults value is SPP .

Disabled	Disable onboard LPT port.
SPP Mode	Using Parallel port as Standard Printer port.
EPP Mode	Using Parallel port as Enhanced Parallel port.
ECP Mode	Using Parallel port as Extended Capabilities port.

Onboard Parallel Port

The defaults value is 378H.

378H	Enable onboard LPT port and address is 378H.
278H	Enable onboard LPT port and address is 278H.
NONE	Disable onboard LPT port.

Onboard Serial Port 1

The defaults value is COM1.

COM1	Enable onboard Serial port1 and address is 3F8H.
COM3	Enable onboard Serial port1 and address is 3E8H.
NONE	Disable onboard Serial port1.

Onboard Serial Port 2

The defaults value is COM2.

COM2	Enable onboard Serial port2 and address is 2F8H.
COM4	Enable onboard Serial port2 and address is 2E8H.
NONE	Disable onboard Serial port2.

4.8 POWER MANAGEMENT SETUP

ROM PCI/ISA BIOS (2A4IBG01) POWER MANAGEMENT SETUP

AWARD SOFTWARE, INC.				
Power Management	: Enable	Monitor IRQ3		: Enable
PM Control by APM	: No	Monitor IRQ5		: Disable
Viode Off Method	: Blank Screen	Monitor IRQ7		: Disable
Network Card	: Not installed	Monitor IRQ9		: Disable
HDD Off After	: Disable	Monitor IRQ10		: Disable
Suspend Mode	: 3 Min	Monitor IRQ12		: Disable
VGA Activity	: Enable			
		ESC : Quit	$\wedge\psi\rightarrow$	\leftarrow : Select Item
		F1 : Help	PU/PD/+/-	: Modify
		F5 : Old Values	(Shift)F2	: Color
		F6 : Load BIOS E	Defaults	
		F7 : Load Setup I	Defaults	

Power Management

The default value is Enable.

Enable	Enable Green function.
Disable	Disable Green function.

Please disable Green Function for Non-S CPU in OS/2, Unix, Window NT & Novell system. (For example AMD not Enhance CPU.)

• PM Control by APM

The default value is No.

Yes	BIOS will combine DOS 6.2 (power.exe) & Windows 3.1 (DOS with APM) to get into Green mode.
No	BIOS will not combine DOS 6.2 (power.exe) & Windows 3.1 (DOS with APM) to get into Green mode.

Video Off Method

The default value is Blank Screen.

Blank Screen	BIOS will only blank monitor when gets into Green mode.
V/H SYNC + Blank	BIOS will turn off H/V SYNC & blank monitor when gets into Green mode for Green monitor power saving.

Network Card

The default value is Not installed.

Installed	Network Card Installed.
Not installed	Network Card not Installed.

If you set "Network Card Installed" to "Yes", the JP4 Green LED will not be light when system get into Green mode.

HDD Off After

The default value is Disable.

Disable	Disable HDD Power Down mode function.		
1 - 15 Mins	Enable HDD enter Power Down mode between 1 to 15 mins.		
Suspend	HDD power down when system get into suspend mode.		

Suspend Mode

The default value is 3 min.

Disable	Disable Suspend mode.
10 sec - 40 min	Enable Suspend mode.

VGA Activity

The default value is Enable.

Enable	Enable VGA Activity.
Disable	Disable VGA Activity.

Monitor IRQ#

Monitor IRQ status. The default value is Enable IRQ3.

Enable	Enabled Monitor IRQ Action.	
Disable	Disabled Monitor IRQ Action.	



BIOS Configuration

4.9 PCI / IDE CONFIGURATION SETUP

ROM PCI/ISA BIOS (2A4IBG01) PCI / IDE CONFIGURATION SETUP AWARD SOFTWARE, INC.			
Slot 1 Using INT#	: AUTO	Onboard 496B IDE Port	: Both
Slot 2 Using IN1# Slot 3 Using INT#	: AUTO : AUTO	IDE 0 Master Mode IDE 0 Salve Mode	: Auto : Auto
GA410 Using INTA# in 1st Available IRQ	: N/A : 9	IDE 1 Master Mode IDE 1 Salve Mode	: Auto : Auto
2nd Available IRQ 3rd Available IRQ 4th Available IRQ PCI IRQ Actived By PCI IDE IRQ Map To Primary IDE INT# Secondary IDE INT# CPU-> PCI Mem Post Write Buf	: 10 : 11 : 12 : Edge : PCI-AUTO : A : B : Enable	IDE Prefetch Read Buffer IDE HDD Block Mode	: Disable : Disable
CPU-> PCI Memory Burstt Write PCI Master Burst Read/Write	: Enable : Enable	$\begin{array}{c c} \text{ESC}: \text{Quit} & & & & & & \\ \hline F1: \text{Help} & & & & & \\ \text{F5}: \text{Old Values} & (\text{Shift})\text{F2} & : \text{Col} \\ \text{F6}: \text{Load BIOS Defaults} \\ \text{F7}: \text{Load Setup Defaults} \end{array}$	Select Item dify or

SLOT # Using INT#

The default value is AUTO.

AUTO	The BIOS auto detect the PCI device using INTA ~ D and auto set up a available IRO to let device use
A ~ D	For some not PCI specification compliant device, the user must set up IRQ manually.

• GA410 Using INTA # in

The default value N/A.

The GA-410 NCR 810 PCI SCSI card has a Jumper to select the card to be Primary or Secondary card. If the card is been set up to Primary then the user must set up the selection correctly (and the INT#A must be used). If the card is been set up to Secondary then the user only set up the above selections.

Available IRQ

The default value is shown on the above table.

These available IRQs are Mapped to be PCI INT# by BIOS for PCI device automatically. If some one IRQ is used by ISA device then the user must keep the IRQ out of the available table.

PCI IRQ Actived By

The default value is Edge.

Edge	Normal Operation.	
Level	For some PCI SCSI or Lan device using same PCI INT.	

PCI IDE IRQ Map To

You will see this option if you Disable Onboard PCI/IDE.

The default value is PCI-AUTO.

PCI-AUTO	Map PCI IDE IRQ to PCI slot automatically.
ISA	Map PCI IDE IRQ to ISA slot.
PCI-SLOT1	Map PCI IDE IRQ to PCI slot1.
PCI-SLOT2	Map PCI IDE IRQ to PCI slot2.
PCI-SLOT3	Map PCI IDE IRQ to PCI slot3.

• Primary / Secondary IDE INT#

You will see this option if you set PCI IDE $\;$ IRQ MAP TO PCI-slot1, PCI-slot2, PCI-slot3 or PCI-AUTO.

Set INT# for Primary / Secondary PCI IDE. The Default value is A / B.

А	Set INTA for Primary / Secondary PCI IDE.
В	Set INTB for Primary / Secondary PCI IDE.
С	Set INTC for Primary / Secondary PCI IDE.
D	Set INTD for Primary / Secondary PCI IDE.

● CPU; PCI Mem Post Write Buf

The default value is Enable.

Enable	Enable CPU	PCI Mem Post Write Buf.	
Disable	Disable CPU	PCI Mem Post Write Buf.	

• CPU; PCI Memory Burst Write

The default value is Enable.

Enable	Enable CPU:	PCI Memory Burst Write function.
Disable	Disable CPU	PCI Memory Burst Write function .

• PCI Master Burst Read / Write

The default value is Enable.

Enable	Enable PCI Master Burst Read/Write function.
Disable	Disable PCI Master Burst Read/Write function.

• Onboard 496B IDE Port

The default value is Both.

Both	Enable both Primary and Secondary IDE Function onboard.
Primary	Enable only Primary IDE Function onboard.
Secondary	Enable only Secondary IDE Function onboard.
Disable	Disable onboard PCI IDE Function.

IDE 0/1 Master / Slavve Mode

The default value is Auto.

Auto	BIOS detect HDD mode Automatically.
0 - 4	Set HDD Maunally.

• IDE Prefetch Read Buffer

The default value is Disable.

Disable	Disable IDE Prefetch Read Buffer.
Enable	Enable IDE Prefetch Read Buffer.

IDE HDD Block Mode

The default value is Disable.

Disable	Disable IDE HDD Block Mode.
Enable	Enable IDE HDD Block Mode.

4.10LOAD BIOS DEFAULTS

ROM PCI/ISA BIOS (2A4IBG01) CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	PASSWORD SETTING				
BIOS FEATURES SETUP	IDE HDD AUTO DETECTION				
CHIPSET FEATURES SETUP	HDD LOW LEVEL FORMAT				
POWER MANAGEM	ETUP				
PCI/IDE CONFIGURA	SAVING				
LOAD BIOS DEFAULTS					
LOAD SETUP DEFAULTS					
ESC: Quit $\uparrow \downarrow \rightarrow \leftarrow$: Select ItemF10: Save & Exit Setup: Change Color					
Load BIOS Defaults except Standard CMOS SETUP					

Load BIOS Defaults

To load BIOS defaults value to CMOS SRAM, enter "Y". If not, enter "N".

4.11LOAD SETUP DEFAULTS



Load Setup Defaults

To load Setup defaults value to CMOS SRAM, enter "Y". If not, enter "N".

4.12PASSWORD SETTING

PASSWORD SETTING

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.



Type the password, up to eight characters, and press <Enter>. The password typed now will clear and previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not to enter a password.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED

If you select System at Security Option of BIOS Features Setup Menu, you will be prompted for the password every time the system is rebooted or any time you try to enter Setup. If you select Setup at Security Option of BIOS Features Setup Menu, you will be prompted only when you try to enter Setup.

4.13IDE HDD AUTO DETECTION

ROM PCI/ ISA BIOS (2A4IBG01) STANDARD CMOS SETUP AWARD SOFTWARE, INC.

HARD DISKS	TYPE	SIZE	CYLS	HEAD	PRECOMP	LANDZ	SECTOF	MODE
Primary Master	r:							
		S	elect Primary	Master Opt	ion (N=Skip): N		
OPTIONS	SIZE	CYLS	HEAD	PRECOM	P LA	NDZ	SECTOR	MODE
2(Y)	539	523	32	() 10)45	63	LAB
1	539	1046	16	65535	5 10)45	63	NORMAL
3	539	523	32	65535	5 10)45	63	LARGE
ESC : Skip								

Type "Y" will accept the H.D.D. parameter reported by BIOS. Type "N" will keep the old H.D.D. parameter setup. If the hard disk cylinder NO. is over 1024, then the user can select LBA mode or LARGER mode for DOS partition LARGE than 528 MB.

BIOS Configuration

4.14HDD LOW LEVEL FORMAT

ROM PCI/ISA BIOS (2A4IBG01) CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	PASSWORD SETTING				
BIOS FEATURES SETUP	IDE HDD AUTO DETECTION				
CHIPSET FEATURES SETUP	HDD LOW LEVEL FORMAT				
POWER MANAGEMENT SETUP	SAVE & EXIT SETUP				
PCI/IDE CONFIGURATION SETUP	EXIT WITHOUT SAVING				
LOAD BIOS DEFAULTS					
LOAD SETUP DEFAULTS					
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item				
F10 : Save & Exit Setup	(Shift)F2 : Change Color				
Hard disk low level format utility					

This utility can low level format HDD.

4.15SAVE & EXIT SETUP



Type "Y" will quit the Setup Utility and save the user setup value to RTC CMOS SRAM. Type "N" will return to Setup Utility.

4.16EXIT WITHOUT SAVING



Type "Y" will quit the Setup Utility without saving to RTC CMOS SRAM. Type "N" will return to Setup Utility.

4.17KEYBOARD SETTING FUNCTION

After booting the O.S., there are some special functions used by keyboard as follows:

"CTRL_ALT_DEL"	Pressing these keys simultaneously will cause system a
	WARM START (Soft Reset).
"CTRL_ALT_[+]"	If short J3 pin1 & 2, pressing these keys simultaneously will change the system speed to high speed (TURBO, all cache memory enabled).
"CTRL_ALT_[-]"	If short J3 pin1 & 2, pressing these keys simultaneously will change the system speed to low speed (Normal, disable cache memory).

5 AT TECHNICAL INFORMATION

5.1 BUS CONNECTOR PIN OUT

5.1.1ISA BUS SLOT PIN OUT

GND	B01	A01	I/O CH CHK				
RESET	B02	A02	SD07				
+5V	B03	A03	SD06				
IRQ9	B04	A04	SD05				
-5V	B05	A05	SD04				
DRQ2	B06	A06	SD03				
-12V	B07	A07	SD02				
0WS	B08	A08	SD01				
+12V	B09	A09	SD00				
GND	B10	A10	-I/O CH RDY				
-SMEMW	B11	A11	AEN				
-SMEMR	B12	A12	SA19				
-IOW	B13	A13	SA18				1
-IOR	B14	A14	SA17	-MEMCS16	D01	C01	SBHE
-DACK3	B15	A15	SA16	-I/OCS16	D02	C02	LA23
-DRQ3	B16	A16	SA15	IRQ10	D03	C03	LA22
-DACK1	B17	A17	SA14	IRQ11	D04	C04	LA21
-DRQ1	B18	A18	SA13	IRQ12	D05	C05	LA20
-REFRESH	B19	A19	SA12	IRQ15	D06	C06	LA19
BCLK	B20	A20	SA11	IRQ14	D07	C07	_LA18
IRQ7	B21	A21	SA10	-DACK0	D08	C08	_LA17
IRQ6	B22	A22	SA09	DRQ0	D09	C09	MEMR
IRQ5	B23	A23	SA08	-DACK5	D10	C10	-MEMW
IRQ4	B24	A24	SA07	DRQ5	D11	C11	SD08
IRQ3	B25	A25	SA06	-DACK6	D12	C12	SD09
-DACK2	B26	A26	SA05	DRQ6	D13	C13	SD10
T/C	B27	A27	SA04	-DACK7	D14	C14	SD11
BALE	B28	A28	SA03	DRQ7	D15	C15	SD12
+5V	B29	A29	SA02	+5V	D16	C16	SD13
OSC	B30	A30	SA01	-MASTER	D17	C17	SD14
GND	B31	A31	SA00	GND	D18	C18	SD15
	l		1				

AT Technical Information

5.1.2PCI BUS SLOT PIN OUT

-12V	B01	A01	NC			
NC	B02	A02	+12V			
GND	B03	A03	NC			
NC	B04	A04	NC			
VCC	B05	A05	VCC			
VCC	B06	A06	INTA#			
INTB#	B07	A07	INTC#			
INTD#	B08	A08	vcc			
PST#1	B09	A09	NC			
NC	B10	A10	VCC			
PST#2	B11	A11	NC			
GND	B12	A12	GND			
GND	B13	A13	GND			
NC	B14	A14	NC			
GND	B15	A15	RST#			
CLK	B16	A16	VCC			
GND	B17	A17	GNT#			
REO#	B18	A 18	GND			
VCC	B10	A 19	NC			
AD 31	B20	A20	AD 30			
AD_31	B20	A20	NC NC			
AD_29	B21	121				
AD 27	B22	A22	AD_28			
AD_27	B23	A24	AD_20			
AD_25	B24	A24				
CDE#2	B25	A25	AD_24			
AD 22	B20	A20	IDSEL			
AD_23	D27	A27				
GND	B28	A28	AD_22			
AD_21	D29	A29	AD_20			
AD_19	B30	A30	GND			
NC	B31	A31	AD_18			
AD_1/	B32	A32	AD_16			
CEB#2	835	A35				
GND	B34	A34	FRAME#			
IRDY#	B35	A35	GND			
NC	B36	A36	TRDY#			
DEVSEL#	B37	A37	GND			
GND	B38	A38	STOP#	AD_08	B52	A5
LOCK#	B39	A39	NC	AD_07	B53	A5
PERR#	B40	A40	SDONE	NC	B54	A5
NC	B41	A41	SBO#	AD 05	B55	A5
SERR#	B42	A42	GND	AD_03	B56	A5
NC	B43	A43	PAR	GND	B57	A5
CBE#1	B44	A44	AD_15	AD 01	B58	A5
AD_14	B45	A45	^{NC}	VCC	B59	A5
GND	B46	A46	AD_13	NC	B60	A6
AD_12	B47	A47	AD_11	VCC	B61	A6
AD_10	B48	A48	GND	VCC	B62	A6
GND	B49	A49	AD_09		505	

			_
AD_08	B52	A52	CBE#0
AD_07	B53	A53	NC
NC	B54	A54	AD_06
AD_05	B55	A55	AD04
AD_03	B56	A56	GND
GND	B57	A57	AD02
AD_01	B58	A58	AD00
VCC	B59	A59	vcc
NC	B60	A60	NC
VCC	B61	A61	vcc
VCC	B62	A62	vcc

5.2 I/O & MEMORY MAP

MEMORY MAP:	[000000- 009FFF] [00A000- 00BFFF] [00C0000- 00EFFF] [00E000- 00EFFF] [00F000- 00FFFF] [010000- BFFFFF]	System memory used by DOS and application program. Display buffer memory for VGA/EGA/CGA/MONOCHROME adapter. Reserved for I/O device BIOS ROM or RAM buffer. Reserved for PCI device ROM. System BIOS ROM. System extension memory.				
I/O MAP:	[000-01F] [060-06F] [070-07F] [080-09F] [0A0-0BF] [0F0-0FF] [1F0-1F8] [278-27F] [280-2DF] [2F8-2FF] [360-36F] [378-37F] [3D0-3DF] [3D0-3DF] [3F0-3F7] [3F8-3FF]	DMA controller.(Master) INTERRUPT controller.(Master) CHIPSET control registers I/O ports. TIMER control registers. KEYBOARD interface controller.(8042) RTC ports & CMOS I/O ports. DMA register. INTERRUPT controller.(Slave) DMA controller.(Slave) MATH COPROCESSOR HARD DISK controller. PARALLEL port-2. GRAPHICS adapter controller. SERIAL port-2. NETWORK ports. PARALLEL port-1 MONOCHROME & PRINTER adapter. EGA adapter. FLOPPY DISK controller. SERIAL port-1.				
5.3 TIMER & DMA CHANNELS MAP						

5.3 TIMER & DMA CHANNELS MAP

TIMER MAP:	TIMER Channel-0 System timer interrupt TIMER Channel-1 DRAM REFRESH request TIMER Channel-2 SPEAKER tone generator
DMA CHANNELS:	DMA Channel-0 Available DMA Channel-1 IBM SDLC DMA Channel-2 FLOPPY DISK adapter DMA Channel-3 Available DMA Channel-4 Cascade for DMA controller 1 DMA Channel-5 Available DMA Channel-6 Available DMA Channel-7 Available

5.4 INTERRUPT MAP

NMI: IRQ (H/W): Parity check error System TIMER interrupt from TIMER-0 KEYBOARD output buffer full Cascade for IRQ 8-15 SERIAL port 2 SERIAL port 1 PARALLEL port 2 FLOPPY DISK adapter PARALLEL port 1 RTC clock Available Available Available Available MATH coprocessor HARD DISK adapter Available

5.5 RTC & CMOS RAM MAP

	Seconds
	Second alarm
	Minutes
	Minutes alarm
	Hours
	Hours alarm
	Day of week
	Day of month
	Month
	Year
0A	Status register A
0B	Status register B
0C	Status register C
0D	Status register D
0E	Diagnostic status byte
0F	Shutdown byte
	FLOPPY DISK drive type byte
	Reserve
	HARD DISK type byte
	Reserve
	Equipment byte
	Base memory low byte
	Base memory high byte
	Extension memory low byte
	Extension memory high byte
19-2d	
2E-2F	
	Reserved for extension memory low byte
	Reserved for extension memory high byte
24 2E	
34-3F 40-7f	Reserved for CHIPSET SETTING DATA
-+0-71	Reserved for Orm GET GET TING DATA

APPENDIX A: POST MESSAGE

When the BIOS encounters an error that requires the user to correct something, either a beep code will sound or a message will be displayed in a box in the middle of the screen and the message PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP will be shown in the information box at the bottom.

POST BEEP

Currently there is only one beep code in BIOS. This code indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps.

ERROR MESSAGE

Once or more of the following messages may be displayed if the BIOS detects an error during the POST. This list includes message for both the ISA and the EISA BIOS.

S CMOS BATTERY HAS FAILED

CMOS battery is no longer functional. It should be replaced.

CMOS CHECKSUM ERROR

Checksum of CMOS is incorrect. This can indicate that CMOS has been corrupted. This error may have been caused by a weak battery. Check the battery and replace it if necessary.

☑> DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER

No boot device was found. Insert a system disk into Drive A: and press <Enter>. If you assumed the system would boot from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also be sure the disk is formatted as a boot device. Then reboot the system.

☑ DISKETTE DRIVES OR TYPES MISMATCH ERROR - RUN SETUP

Type of diskette drive installed in the system is different from the CMOS definition. Run Setup to re-configure the drive type correctly.

DISPLAY SWITCH IS SET INCORRECTLY

Display switch on the motherboard can be set to either monochrome or color. This indicates the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper, or enter Setup and change the VIDEO selection.

☑ DISPLAY TYPE HAS CHANGED SINCE LAST BOOT

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.

➢ ERROR ENCOUNTERED INITIALIZING HARD DRIVE

Hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also be sure the correct hard drive type is selected in Setup.

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ERROR INITIALIZING HARD DISK CONTROLLER

Cannot initialize controller. Make sure the cord is correctly and firmly installed in the bus. Be sure the correct hard drive type is selected in Setup. Also check to see if any jumper needs to be set correctly in the hard drive.

S FLOPPY DISK CNTRLR ERROR OR NO CNTRLR PRESENT

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there is no floppy drive installed, be sure the Diskette Drive selection in Setup is set to NONE.

➢ KEYBOARD ERROR OR NO KEYBOARD PRESENT

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during the boot.

If you are purposely configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

Memory Address Error at ...

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

➢ MEMORY SIZE HAS CHANGED SINCE LAST BOOT

Memory has been added or removed since the last boot. In EISA mode use Configuration Utility to re-configure the memory configuration. In ISA mode enter Setup and enter the new memory size in the memory fields.

S Memory Verify Error at ...

Indicate an error verifying a value already written to memory. Use the location along with your system memory map to locate the bad chip.

OFFENDING ADDRESS NOT FOUND

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment with problem cannot be isolated.

➢ OFFENDING SEGMENT:

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment with problem has been isolated.

PRESS A KEY TO REBOOT

This will be displayed at the bottom screen when an error occurs that requires you to reboot. Press any key and the system will reboot.

PRESS F1 TO DISABLE NMI, F2 TO REBOOT

When BIOS detects a Non-maskable Interrupt condition during boot, this will allow you to disable the NMI and continue to boot, or you can reboot the system will the NMI enabled.

SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT ...

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

APPENDIX B: POST CODES

 $\ensuremath{\,^{\ensuremath{\mathcal{P}}}}$ ISA POST codes are typically output to port address 80h.

POST	Name	Description
C0	Turn Off Chipset Cache	OEM Specific-Cache control.
1	Processor Test 1	Processor Status (1 FLAGS) Verification. Test the following processor status flags carry, zero, sign, overflow, The BIOS will set each of these flags, verify they are set, then turn each flag off and verify it is off.
2	Processor Test 2	Read/Write/Verify all CPU registers except SS, SP, and BP with data pattern FF and 00.
3	Initialize Chips	Disable NMI, PIE, AIE, UEI, SQWV. Disable video, parity checking, DMA. Reset math coprocessor. Clear all page registers, CMOS shutdown byte. Initialize timer 0, 1, and 2, including set EISA timer to a known state. Initialize DMA controllers 0 and 1. Initialize interrupt controllers 0 and 1. Initialize EISA extended registers.
4	Test Memory Refresh Toggle	RAM must be periodically refreshed in order to keep the memory from decaying. This function assures that the memory refresh function is working properly.
5	Blank video, Initialize keyboard	Keyboard controller initialization.
6	Reserved	
7	Test CMOS Interface and Battery Status	Verifies CMOS is working correctly, detects bad battery.
BE	Chipset Default Initialization	Program chipset registers with power on BIOS defaults.
C1	Memory presence test	OEM Specific-Test to size on-board memory.
C5	Early Shadow	OEM Specific-Early Shadow enable for fast boot.
C6	Cache presence test	External cache size detection.
8	Setup low memory	Early chip set initialization. Memory presence test. OEM chip set routines. Clear low 64 K of memory. Test first 64 K memory.
9	Early Cache Initialization	Cyrix CPU initialization. Cache initialization.
A	Setup Interrupt Vector Table	Initialize first 120 interrupt vectors with SPURIOUS_INT- HDLR and initialize INT 00h-1Fh according to INT_TBL.
В	Test CMOS RAM Checksum	Test CMOS RAM Checksum, if bad, or insert key pressed, load defaults.
С	Initialize keyboard	Detect type of keyboard controller (optional). Set NUM_LOCK status.
D	Initialize Video Interface	Detect CPU clock. Read CMOS location 14h to find out type of video in use. Detect and Initialize Video Adapter.
E	Test Video Memory	Test video memory, write sign-on message to screen. Setup shadow RAM - Enable shadow according to Setup.
F	Test DMA Controller 0	BIOS checksum test. Keyboard detect and initialization.
10	Test DMA Controller 1	

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Appendix B:Post Codes

	T I DNAA D	T IDMAD D II
11	Test DMA Page	Test DMA Page Registers.
	registers	
12-13	Reserved	
14	Test Timer Counter 2	Test 8254 Timer 0 Counter 2.
15	Test 8259-1 Mask Bits	Verify 8259 Channel 1 masked interrupts by alternative
		turning off and on the interrupt lines.
16	Test 8259-2 Mask Bits	Verify 8259 Channel 2 masked interrupts by alternative
-		turning off and on the interrupt lines.
17	Test Stuck 8259's	Turn off interrunts then verify no interrunt mask register is
	Interrupt Bits	on
18	Test 8250 Interrupt	Earce an interrupt and verify the interrupt occurred
10	Functionality	Torce an interrupt and verify the interrupt occurred.
10	Toot Stuck NMI Bito	Varify NMI can be alcored
19	(Derity/IO Cheek)	Venity Nivil can be cleared.
4.4	(Fally/IO Check)	
1A		Display CPU clock.
1B-1E	Reserved	
20	Enable Slot 0	Initialize slot 0 (System Board).
21-2F	Enable Slots 1-15	Initialize slot 1 through 15.
30	Size Base and	Size base memory from 256 K to 640 K extended memory
	Extended Memory	above 1 MB.
31	Test Base and	Test base memory from 256 K to 640 K and extended
	Extended Memory	memory above 1 MB using various patterns.
	,	This will be skipped in EISA mode and can be "skipped"
		with ESC key in ISA mode.
33-3B	Reserved	
30	Setup Enabled	
30	Initialize & Install	Detect if mouse is present initialize mouse install
30	Mouso	interrunt vectors
25	Setur Casha Controllar	
JE DE	Setup Cache Controller	
3F	Reserved	
BF	Chipset Initialization	Program chipset registers with Setup values.
40		Display virus protest disable or enable.
41	Initialize Floppy Drive & Controller	Initialize floppy disk drive controller and any drives.
42	Initialize Hard Drive &	Initialize hard drive controller and any drives.
	Controller	
43	Detect & Initialize	Initialize any serial and parallel ports (also game port)
	Serial/Parallel Ports	
44	Reserved	
45	Detect & Initialize Math	Initialize math confocessor
45	Coprocessor	
46	Beconved	
40	Reserved	
47	Reserved	
48-4D	Reserved	
4E	Manufacturing POST	Reboot if Manufacturing POST Loop pin is set. Otherwise
	Loop or Display	display any messages (i.e., any non-fatal errors that were
	Messages	detected during POST) and enter Setup.
4F	Security Check	Ask password security (optional).
50	Write CMOS	Write all CMOS values back to RAM and clear screen.
51	Pre-boot Enable	Enable parity checker.
		Enable NMI, Enable cache before boot.
52	Initialize Option ROMs	Initialize any option ROMs present from C8000h to
		EFFFFh.
		When FSCAN option is enabled, will initialize from
		C8000h to F7FFFh.

53	Initialize Time Value	Initialize time value in 40h: BIOS area.
60	Setup Virus Protect	Setup virus protect according to Setup
61	Set Boot Speed	Set system speed for boot
62	Setup NumLock	Setup NumLock status according to Setup
63	Boot Attempt	Set low stack. Boot via INT 19h.
B0	Spurious	If interrupt occurs in protected mode.
B1	Unclaimed NMI	If unmasked NMI occurs, display Press F1 to disable NMI, F2 reboot.
E1-EF	Setup Pages	E1 - Page 1, E2 - Page 2, etc.
FF	Boot	

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APPENDIX C: BIOS DEFAULT DRIVE TABLE

Type	Size (MB)	Cylinders	Heads	Write/ Precomp	Land Zone	Sectors
1	10	306	4	128	305	17
2	21	615	4	300	615	17
3	32	615	6	300	615	17
4	65	940	8	512	940	17
5	49	940	6	512	940	17
6	21	615	4	65535	615	17
7	32	462	8	256	511	17
8	31	733	5	65535	733	17
9	117	900	15	65535	901	17
10	21	820	3	65535	820	17
11	37	855	5	65535	855	17
12	52	855	7	65535	855	17
13	21	306	8	128	319	17
14	44	733	7	65535	733	17
16	21	612	4	0	663	17
17	42	977	5	300	977	17
18	59	977	7	65535	977	17
19	62	1024	7	512	1023	17
20	31	733	5	300	732	17
21	44	733	7	300	732	17
22	31	733	5	300	732	17
23	10	306	4	0	336	17
24	42	977	5	65535	976	17
25	80	1024	9	65535	1023	17
26	74	1224	7	65535	1223	17
27	117	1224	11	65535	1223	17
28	159	1224	15	65535	1223	17
29	71	1024	8	65535	1023	17
30	98	1024	11	65535	1023	17
31	87	918	11	65535	1023	17
32	72	925	9	65535	926	17
33	89	1024	10	65535	1023	17
34	106	1024	12	65535	1023	17
35	115	1024	13	65535	1023	17
36	124	1024	14	65535	1023	17
37	17	1024	2	65535	1023	17
38	142	1024	16	65535	1023	17
39	119	918	15	65535	1023	17
40	42	820	6	65535	820	17
41	44	1024	5	65535	1023	17
42	68	1024	5	65535	1023	17
43	42	809	6	65535	852	17
44	64	809	6	65535	852	26
45	104	776	8	65535	775	33
User						1

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APPENDIX D: PROBLEM SHEET

1. Customer Data						
Name				Te	el. No.	
Address				Fa	ax. No.	
				P	urchase Date	
2. Mainboard Date						
Model NO.	GA-			Rev. N	No.	
Serial No.						
3. System Configur	ration					
CPU Type:						
CPU Brand:						
CPU Speed:						
DRAM Type:	□ 1	2	4	□ 8	🗖 16	🖬 32 MB
DRAM Speed:	B 80	7 0	🛛 60 ns			
DRAM Total Size:		MB				
DRAM Brand:						
SRAM Size:	🖬 64KB	🖬 128 KB	🖵 256 KB	□ 512	KB 🖬 1 MB	
SRAM Part No.	TAG:			DATA		
Video Card:						
Video Chip or Brand	l:					
Floppy Drive A Capa	acity & Brand:					
Floppy Drive B Capa	acity & Brand:					
Storage Controller T	уре	MFM	RLL	🗆 IDE	🗆 ESDI	SCSI
Hard Drive C Brand	& Type:					
Hard Drive D Brand & Type:						
LAN Controller Type:						
LAN Card Brand & M	Nodel:					
Serial / Parallel Chip Brand & Model:						
Mouse Brand & Model:						
O.S.	DOS	OS/2	NETWARE		UNIX / 3	KENIX Ver.:
4. AUTOEXEC.BAT & CONFIG.SYS File:						

5. Problem Description:

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