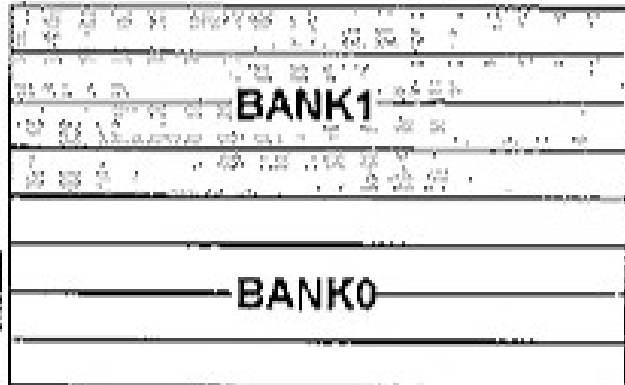


KEY-BOARD
CON-NECTOR

BAT-TERY



CONTAQ
82C596A

80486SX
PQFP

POWER
CONNECTOR

BIOS

P24TN675X
480X/092
PQFP Non-retail 486SX

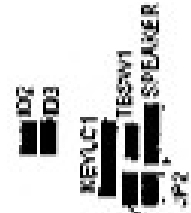


BATT1



82C206

80486
PGA



8042



U29 04K
 120K
 256K



U31 U43



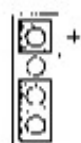
U32 U44



U33 U45

U34 U46

486CCLB LAYOUT

BATT1**External Battery Connector**

1. +VDD (External battery power input)
4. GND

JP2**Turbo LED**

- On :high speed
- Off :low speed

JP20

Short: disable PQFP CPU



Open: default

25**33****50**

25 Short: 25MHz CPU clock



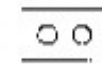
33 Short: 33MHz CPU clock



33,50 Short: 40MHz CPU clock



50 Short: 50MHz CPU clock

JP18**ID2****ID3****VESA high speed write index (for 486CCLB only)**

Short: For one wait state write

Open: For zero wait state write

VESA CPU speed index (for 486CCLB only)

Short: For 40,50MHz CPU clock

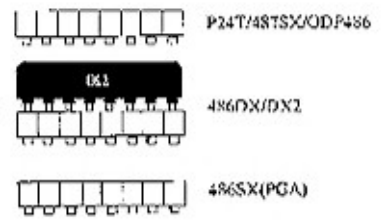
Open: For 25,33MHz CPU clock

CPU TYPE	CPU CLOCK
486SX-25	25MHz
486SX-33	33MHz
487SX-25	25MHz
487SX-33	33MHz
486DX-33	33MHz
486DX-40	40MHz
486DX-50	50MHz
486DX2-50	25MHz
486DX2-66	33MHz
486OVERDRIVE-25	25MHz
486OVERDRIVE-33	33MHz

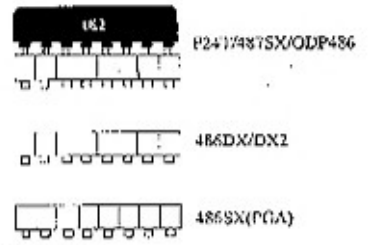
The 486CCLB/486CCIB can accept all 486 family CPU refer to following configuration. Use 0Ω network resistor inserting SIP socket for CPU type change.



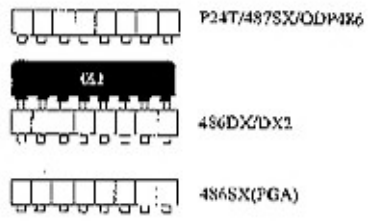
486DX CPU



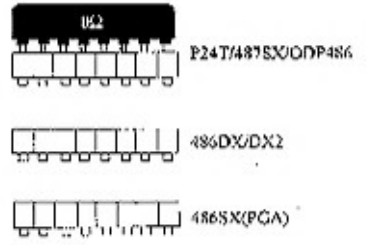
487SX CPU



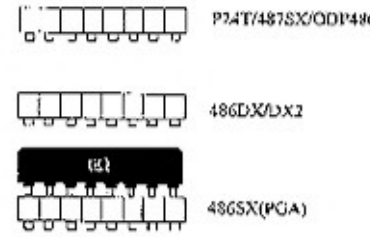
486DX2 CPU



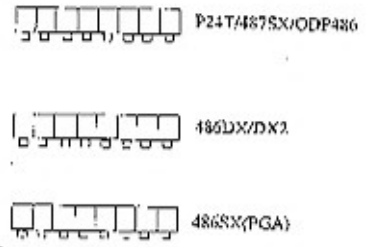
ODP486(OVERDRIVE) CPU



486SX PGA CPU



486SX PQFP CPU



When use 486SX PQFP CPU, don't insert 0Ω network resistor into SIP socket.