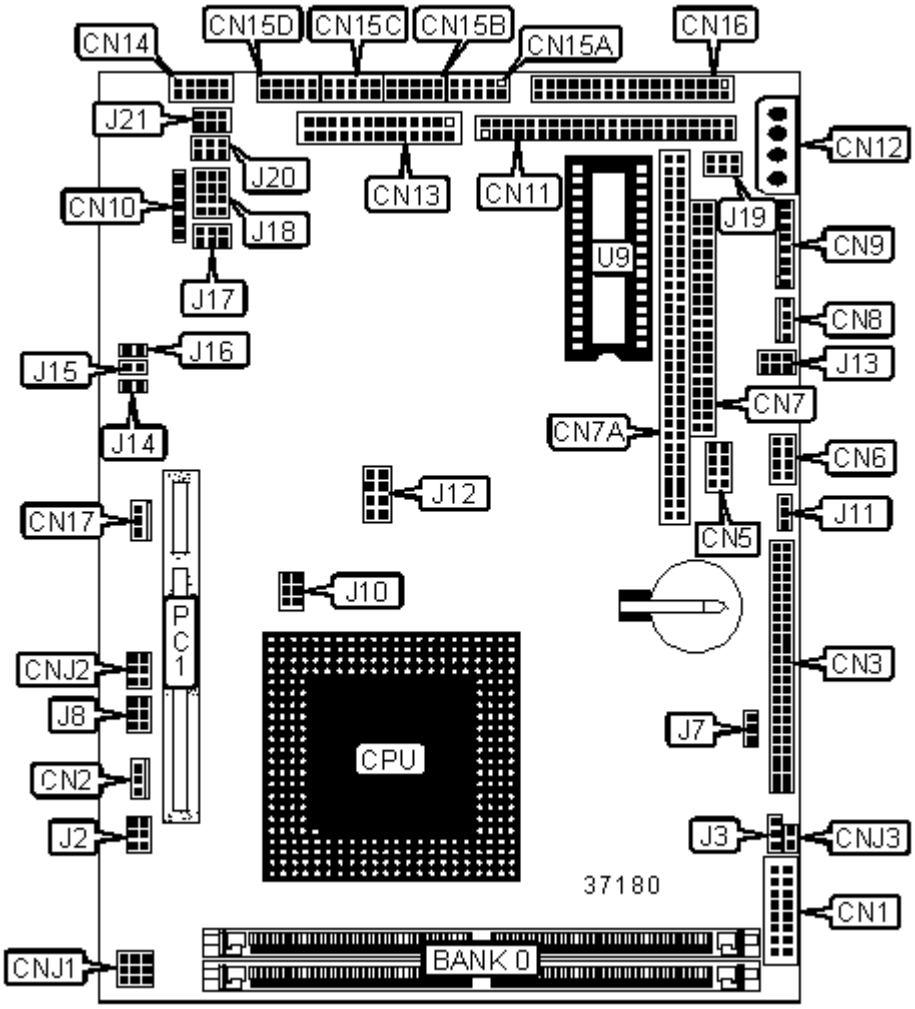
EMAC, INC.

PCM-5894, PCM-5894C

Device Type	Single Board Computer
Processor	CX 6X86L/CX 6X86MX/AM K5/AM K6/Pentium/Pentium MMX
Processor Speed	75/90/100/120/133/150/166/180/200/233MHz
Chip Set	SIS
Video Chip Set	Chips and Technology
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	2MB
Cache	512KB
BIOS	AMI
Dimensions	203mm x 146mm
I/O Options	32-bit PCI slot, Auxiliary power connector, Ethernet 10BaseT interface, Flat panel connector, Floppy drive interface, IDE interface, IR connector, Parallel interface, PC/104 connectors (2), Power connector, PS/2 mouse/AT keyboard interface, Serial interfaces (4), Solid-state flash disk socket, USB interface, VGA connector



CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
VGA connector	CN1	10BaseT Ethernet interface	CN14

CPU fan power A	CN2	Serial interface 1	CN15A
Flat panel connector	CN3	Serial interface 2	CN15B
USB interface	CN5	Serial interface 3	CN15C
IDE interface LED	CN6/Pins 1 & 2	Serial interface 4	CN15D
Speaker	CN6/Pins 3 - 5	Floppy drive interface	CN16
Reset switch	CN6/Pins 7 & 8	CPU fan power B	CN17
16-bit PC/104 connector	CN7	Unidentified	CNJ1
8-bit PC/104 connector	CN7A	Unidentified	CNJ2
Auxiliary power (-5V, -12V)	CN8	Unidentified	CNJ3
PS/2 mouse/AT keyboard interface	CN9	Ethernet Tx LED connector	J14
IR connector	CN10	Ethernet Rx LED connector	J15
IDE interface	CN11	Ethernet Link LED connector	J16
Power connector (+5V, +12V)	CN12	32-bit PCI slot	PC1
Parallel interface	CN13	Solid-state flash disk (DOC) socket	U9

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
»	CMOS memory normal operation	J7	Pins 1 & 2 closed
	CMOS memory clear	J7	Pins 2 & 3 closed
Note: Pin 1 location of J7 is unidentified.			

SIMM CONFIGURATION	
Size	**Bank 0**
8MB	(2) 1M x 32
16MB	(2) 2M x 32
32MB	(2) 4M x 32
64MB	(2) 8M x 32
128MB	(2) 16M x 32

Note: Board supports EDO memory.

CPU BUS SPEED SELECTION					
CPU		PCI	J2/Pins 1 & 2	J2/Pins 3 & 4	J2/Pins 5 & 6
	50MHz	25MHz	Open	Closed	Open
	50MHz	33.3MHz	Closed	Closed	Open
	55MHz	27.5MHz	Closed	Closed	Closed
	60MHz	30MHz	Closed	Open	Open
»	66MHz	33.3MHz	Open	Open	Open
	75MHz	32MHz	Open	Closed	Closed
Note: Pin 1 location of J2 is unidentified.					

CPU MULTIPLIER SELECTION				
Multiplier		J8/Pins 1 & 2	J8/Pins 3 & 4	J8/Pins 5 & 6
	1.5x/3.5x	Open	Open	Open
	2.0x	Closed	Open	Open
	2.5x	Closed	Closed	Open
»	3.0x	Open	Closed	Open
	4.0x	Closed	Open	Closed
	4.5x	Closed	Closed	Closed
	5.0x	Open	Closed	Closed
	5.5x	Open	Open	Closed
Note: Pin 1 location of J8 is unidentified.				

CPU VOLTAGE SELECTION (DUAL)						
Voltage		J10	J12/Pins 1 & 2	J12/Pins 3 & 4	J12/Pins 5 & 6	J12/Pins 7 & 8
	2.1V	Pins 3 & 5, 4 & 6	Open	Open	Open	Open
	2.2V	Pins 3 & 5, 4 & 6	Open	Open	Open	Closed

	2.3V	Pins 3 & 5, 4 & 6	Open	Open	Closed	Open
	2.4V	Pins 3 & 5, 4 & 6	Open	Open	Closed	Closed
	2.5V	Pins 3 & 5, 4 & 6	Open	Closed	Open	Open
	2.6V	Pins 3 & 5, 4 & 6	Open	Closed	Open	Closed
	2.7V	Pins 3 & 5, 4 & 6	Open	Closed	Closed	Open
»	2.8V	Pins 3 & 5, 4 & 6	Open	Closed	Closed	Closed
	2.9V	Pins 3 & 5, 4 & 6	Closed	Open	Open	Open
	3.0V	Pins 3 & 5, 4 & 6	Closed	Open	Open	Closed
	3.1V	Pins 3 & 5, 4 & 6	Closed	Open	Closed	Open
	3.2V	Pins 3 & 5, 4 & 6	Closed	Open	Closed	Closed
	3.4V	Pins 3 & 5, 4 & 6	Closed	Closed	Open	Closed
	3.6V	Pins 3 & 5, 4 & 6	Closed	Closed	Closed	Closed
Note: Pin 1 locations of J10 & J12 are unidentified. Designated pins should be in the closed position.						

CPU VOLTAGE SELECTION (SINGLE)					
Voltage	**J10**	**J12/Pins 1 & 2**	**J12/Pins 3 & 4**	**J12/Pins 5 & 6**	**J12/Pins 7 & 8**
3.3V	Pins 1 & 3, 2 & 4	Closed	Closed	Open	Open
3.5V	Pins 1 & 3, 2 & 4	Closed	Closed	Closed	Open
Note: Pin 1 locations of J10 & J12 are unidentified. Designated pins should be in the closed position.					

SERIAL INTERFACE 2 SELECTION			
Setting		**J17**	**J18**
»	RS-232	Pins 1 & 2 closed	Pins 1 & 2, 4 & 5, 7 & 8, 10 & 11 closed
	RS-422	Pins 3 & 4 closed	Pins 2 & 3, 5 & 6, 8 & 9, 11 & 12 closed
	RS-485	Pins 5 & 6 closed	Pins 2 & 3, 5 & 6, 8 & 9, 11 & 12 closed
Note: Pin 1 locations of J17 & J18 are unidentified. Pins should be open unless designated as closed.			

SERIAL INTERFACES 3 & 4 IRQ SELECTION

Setting		J19
»	COM3 (IRQ5), COM4 (IRQ10)	Pins 2 & 4, 3 & 5 closed
	COM3 (IRQ10), COM4 (IRQ5)	Pins 1 & 3, 4 & 6 closed
Note: Pin 1 location of J19 is unidentified.		

SERIAL INTERFACE 3 VOLTAGE SELECTION				
Setting		J20/Pins 1 & 2	J20/Pins 3 & 4	J20/Pins 5 & 6
»	RI	Open	Open	Closed
	+5V	Open	Closed	Open
	+12V	Closed	Open	Open
Note: Pin 1 location of J20 is unidentified.				

SERIAL INTERFACE 4 VOLTAGE SELECTION				
Setting		J21/Pins 1 & 2	J21/Pins 3 & 4	J21/Pins 5 & 6
»	RI	Open	Open	Closed
	+5V	Open	Closed	Open
	+12V	Closed	Open	Open
Note: Pin 1 location of J21 is unidentified.				

SOLID-STATE DISK (DOC) ADDRESS SELECTION				
Address		J13/Pins 1 & 2	J13/Pins 3 & 4	J13/Pins 5 & 6
	CC00	Open	Open	Open
	D000	Open	Open	Closed
	D400	Open	Closed	Open
	D800	Open	Closed	Closed
»	DC00	Closed	Open	Open
	Disabled	Closed	Closed	Closed
Note: Pin 1 location of J13 is unidentified.				

FLAT PANEL CONNECTOR VOLTAGE SELECTION			
Voltage		**J3**	**J11**
»	5V	Pins 1 & 2 closed	Pins 1 & 2 closed
	3.3V	Pins 2 & 3 closed	Pins 2 & 3 closed
Note: Pin 1 locations of J3 & J11 are unidentified.			

MISCELLANEOUS TECHNICAL NOTES
Solid-state flash disk socket supports (DOC 1000 & 2000) devices from 2MB to 72MB.