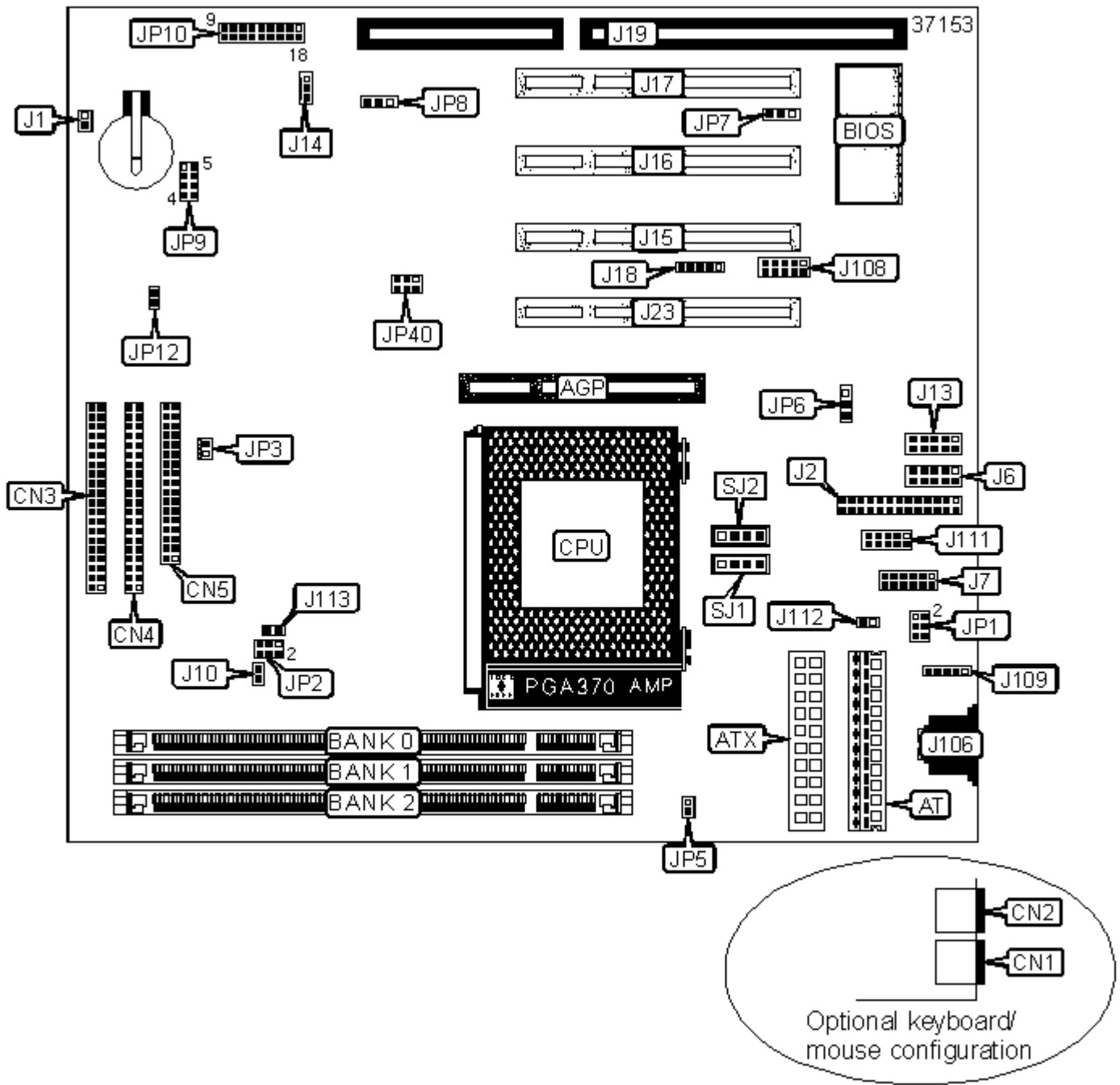


SHUTTLE COMPUTER INTERNATIONAL, INC.

HOT-685V

Device Type	Mainboard
Processor	Celeron
Processor Speed	300/333/350/366/400/433/450/466/500MHz
Chip Set	VIA
Audio Chip Set	Forte Media
Maximum Onboard Memory	768MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128 (located on the Celeron CPU)
BIOS	Award
Dimensions	220mm x 230mm
I/O Options	16-bit ISA slot, 32-bit PCI slots (4), AT power connector, ATX power connector, audio in - CD ROM, audio interface, floppy drive interface, game/MIDI interface, IDE interfaces (2), IR connector, keyboard port, parallel port, PS/2 keyboard port (optional), PS/2 mouse interface, PS/2 mouse port (optional), serial interfaces (2), SB-Link connector, TAD connector, USB interface, Wake-on-LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
AT power connector	AT	32-bit PCI slot	J23
ATX power connector	ATX	Keyboard port	J106
PS/2 keyboard port (optional)	CN1	USB interface	J108
PS/2 mouse port (optional)	CN2	PS/2 mouse interface	J109
IDE interface 1	CN3	Audio interface	J111
IDE interface 2	CN4	System fan power	JP3

Floppy drive interface	CN5	CPU fan power	JP5
AGP fan power	J1	IDE interface LED	JP10/Pins 6 & 7
Parallel port	J2	EPMI connector	JP10/Pins 12 & 13
Serial interface 1	J6	Green PC LED connector	JP10/Pins 10 & 11
Game/MIDI interface	J7	Power switch	JP10/Pins 8 & 9
Serial interface 2	J13	Speaker	JP10/Pins 1 - 4
Wake-on-LAN connector	J14	Power LED	JP10/Pins 14 - 16
32-bit PCI slot	J15	Reset switch	JP10/Pins 17 & 18
32-bit PCI slot	J16	SB-Link connector	JP40
32-bit PCI slot	J17	TAD connector	SJ2
IR connector	J18	Audio in - CD ROM	SJ1
16-bit ISA slot	J19		

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	66MHz based processor normal bus speed	J10	Closed
	66MHz based processor overclocked to 100MHz	J10	Open
	AT power connector enabled	J112	Closed
	ATX power connector enabled	J112	Open
»	Onboard sound enabled	JP6	Pins 1 & 2 closed
	Onboard sound disabled	JP6	Pins 2 & 3 closed
	Flash EEPROM BIOS chip is 12V	JP7	Pins 2 & 3 closed
	Flash EEPROM BIOS chip is 5V	JP7	Pins 1 & 2 closed
»	CMOS memory normal operation	JP8	Pins 1 & 2 closed
	CMOS memory clear	JP8	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
------	--------	--------	--------

8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports SDRAM memory.

CACHE CONFIGURATION

Note: 128KB cache is located on Celeron 300A and greater CPUs.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP2	JP9	JP113
300MHz	66MHz	4.5x	Pins 5 & 6	Pins 2 & 6, 3 & 7	Closed
333MHz	66MHz	5.0x	Pins 5 & 6	Pins 3 & 7, 4 & 8	Closed
350MHz	100MHz	3.5x	Open	Pins 1 & 5, 3 & 7	Closed
366MHz	66MHz	5.5x	Pins 5 & 6	Pins 3 & 7	Closed
400MHz	66MHz	6.0x	Pins 5 & 6	Pins 1 & 5, 2 & 6, 4 & 8	Closed
400MHz	100MHz	4.0x	Open	Pins 2 & 6, 3 & 7, 4 & 8	Closed
433MHz	66MHz	6.5x	Pins 5 & 6	Pins 1 & 5, 2 & 6	Closed
450MHz	100MHz	4.5x	Open	Pins 2 & 6, 3 & 7	Closed
466MHz	66MHz	7.0x	Pins 5 & 6	Pins 1 & 5, 4 & 8	Closed
500MHz	66MHz	7.5x	Pins 5 & 6	Pins 1 & 5	Closed
500MHz	100MHz	5.0x	Open	Pins 3 & 7, 4 & 8	Closed

Note: Designated pins should be in the closed position.

POWER-ON BY KEYBOARD/MOUSE SELECTION

Function	JP1
Power-on by keyboard/mouse disabled	Pins 3 & 5, 4 & 6 closed
Power-on by keyboard/mouse enabled	Pins 1 & 3, 2 & 4 closed
Power-on by mouse enabled	Pins 2 & 4, 3 & 5 closed

Power-on by keyboard enabled

Pins 1 & 3, 4 & 6 closed