ADVANTECH CO., LTD.

PCM-5862EL

Device Type Single Board Computer

Processor CX 686MX/AM K5/AM K6/Pentium/Pentium MMX

Processor Speed 75/90/100/120/133/150/166/200/233MHz

Chip Set SIS

Video Chip Set Chips & Technology

Maximum Onboard Memory 128MB (EDO supported)

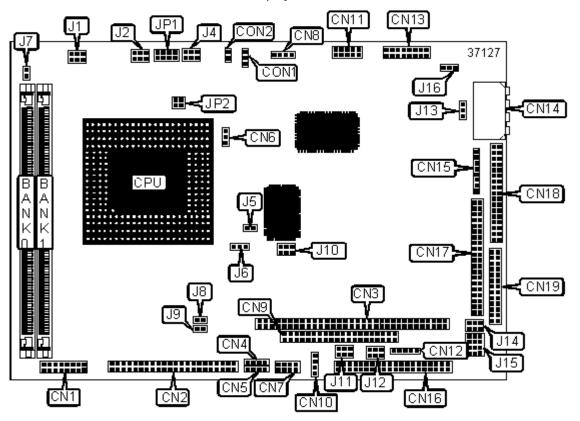
Maximum Video Memory2MBCache512KBBIOSAward

Dimensions 203mm x 146mm

I/O Options Ethernet 10/100BaseT interface, floppy drive interface, IDE interface, parallel

interface, PS/2 mouse/keyboard interface, serial interfaces (4), IR connector, USB interfaces (2), ATX feature connector, audio in - CD-ROM, PC/104 connectors (2), Watchdog timer connector, Front panel connector, Flat panel

display connector



	CONNE	CTIONS	
Purpose	Location	Purpose	Location
CRT display connector	CN1	IR connector	CN12
Flat Panel display connector	CN2	Audio in	CN13
PC/104 connector (16-bit)	CN3	Main power	CN14
USB interface 1	CN4	PS/2 mouse/keyboard interface	CN15

USB interface 2	CN5	Serial interface	CN16
Fan power	CN6	IDE interface	CN17
Flat Panel connector	CN7	Floppy drive interface	CN18
Audio in - CD-ROM	CN8	Parallel interface	CN19
PC/104 connector (8-bit)	CN9	ATX feature connector	CON1
Peripheral power connector	CN10	Soft off power switch	CON2
Ethernet 10/100BaseT interface	CN11		

	USER CONFIGURABLE SE	TTINGS	
	Function	Label	Position
»	Cache disabled	J5	Open
	Cache enabled	J5	Closed
»	CMOS memory normal operation	J6	Pins 2 & 3 closed
	CMOS memory clear	J6	Pins 1 & 2 closed
»	Buzzer enabled	J7	Closed
	Buzzer disabled	J7	Open
»	Factory configured - do not alter	Ј8	Unidentified
»	Factory configured - do not alter	J9	Unidentified
»	LCD 5V power	J10	Pins 3 & 5, 4 & 6 closed
	LCD 3.3V power	J10	Pins 1 & 3, 2 & 4 closed
»	Watchdog timer activates system reset upon CPU halt	J13	Off
	Watchdog timer generates interrupt on IRQ15 upon CPU halt	J13	On
»	+5V audio power selected	J16	Pins 1 & 2 closed
	+12V audio power selected	J16	Pins 2 & 3 closed
»	Pentium MMX disabled	JP2	Pins 1 & 2, 3 & 4 open
	Pentium MMX enabled	JP2	Pins 1 & 2, 3 & 4 closed

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

	CPU SP	EED SELECTION (CX 6	SX86MX)	
CPU speed	Clock speed	Multiplier	J1	J4
166MHz	60MHz	2.5x	3 & 4, 5 & 6	3 & 4, 5 & 6
200MHz	66MHz	2.5x	3 & 4	3 & 4, 5 & 6
233MHz	75MHz	2.5x	1 & 2	3 & 4, 5 & 6
Note: Pins designated	should be in the closed	position.	1	1

	CPU S	SPEED SELECTION (A	M K5)	
CPU speed	Clock speed	Multiplier	J1	J4
75MHz	50MHz	1.5x	1 & 2, 5 & 6	Open
90MHz	60MHz	1.5x	1 & 2, 3 & 4	Open
100MHz	66MHz	1.5x	3 & 4	Open
120MHz	66MHz	2x	3 & 4	5 & 6

133MHz	66MHz	2x	3 & 4	5 & 6
150MHz	60MHz	2.5x	1 & 2, 3 & 4	3 & 4, 5 & 6
166MHz	66MHz	2.5x	3 & 4	3 & 4, 5 & 6
Note: Pins designated should be in the closed position.				

	CPU :	SPEED SELECTION (A	м К6)	
CPU speed	Clock speed	Multiplier	J1	J4
166MHz	66MHz	2.5x	3 & 4	3 & 4, 5 & 6
200MHz	66MHz	3x	3 & 4	5 & 6
233MHz	66MHz	3.5x	3 & 4	Open
Note: Pins designated	should be in the closed	position.		

	CPU SI	PEED SELECTION (PE	NTIUM)	
CPU speed	Clock speed	Multiplier	J1	J4
75MHz	50MHz	1.5x	1 & 2, 5 & 6	Open
90MHz	60MHz	1.5x	1 & 2, 3 & 4	Open
100MHz	66MHz	1.5x	3 & 4	Open
120MHz	66MHz	2x	3 & 4	3 & 4
133MHz	66MHz	2x	3 & 4	3 & 4
150MHz	60MHz	2.5x	1 & 2, 3 & 4	3 & 4, 5 & 6
166MHz	66MHz	2.5x	3 & 4	3 & 4, 5 & 6
180MHz	66MHz	3x	3 & 4	5 & 6
200MHz	66MHz	3x	3 & 4	5 & 6
Note: Pins designated	should be in the closed	position.		1

	CPU SPE	ED SELECTION (PENT	IUM MMX)	
CPU speed	Clock speed	Multiplier	J1	J4
166MHz	66MHz	2.5x	3 & 4	3 & 4, 5 & 6
200MHz	66MHz	3x	3 & 4	5 & 6

233MHz	66MHz	3.5x	3 & 4	Open
Note: Pins designated	should be in the closed	position.		

CI speed	J2
25MHz	Pins 3 & 4 closed
30MHz	Pins 1 & 2 closed
32MHz	Pins 3 & 4, 5 & 6 closed
33.3MHz	Pins 1 & 2, 3 & 4 closed
33.3MHz	Open
	25MHz 30MHz 32MHz 33.3MHz

Note: Default PCI speed is used with system at 66MHz bus, instead of 50MHz.

	CPU VOLTAGE SELECTION (DUAL)			
Voltage	JP1/Pins 1 & 2	JP1/Pins 3 & 4	JP1/Pins 5 & 6	JP1/Pins 7 & 8
0V	Open	Open	Open	Open
2.1V	Closed	Open	Open	Open
2.2V	Open	Closed	Open	Open
2.3V	Closed	Closed	Open	Open
2.4V	Open	Open	Closed	Open
2.5V	Closed	Open	Closed	Open
2.6V	Open	Closed	Closed	Open
2.7V	Closed	Closed	Closed	Open
2.8V	Open	Open	Open	Closed
2.9V	Closed	Open	Open	Closed
3V	Open	Closed	Open	Closed
3.1V	Closed	Closed	Open	Closed
3.2V	Open	Open	Closed	Closed
3.4V	Open	Closed	Closed	Closed

CPU VOLTAGE SELECTION (SINGLE)				
Voltage	JP1/Pins 1 & 2	JP1/Pins 3 & 4	JP1/Pins 5 & 6	JP1/Pins 7 & 8
3.3V	Closed	Open	Closed	Closed
3.5V	Closed	Closed	Closed	Closed

SERIAL INTERFACE 2 ADDRESS SELECTION			
	Setting J14 J15		
»	RS-232	1 & 2	1 & 2, 4 & 5, 7 & 8, 10 & 11
	RS-422	3 & 4	2 & 3, 5 & 6, 8 & 9, 11 & 12
	RS-485	5 & 6	2 & 3, 5 & 6, 8 & 9, 11 & 12
Note: Pins designated should be in the closed position			

	COM 3 VOLTAGE SELECTION		
Setting		J12	
»	RI	Pins 5 & 6 closed	
	+5V	Pins 3 & 4 closed	
	+12V	Pins 1 & 2 closed	

	COM 4 VOLTAGE SELECTION		
Setting		J11	
»	RI	Pins 5 & 6 closed	
	+5V	Pins 3 & 4 closed	
	+12V	Pins 1 & 2 closed	

MISCELLANEOUS TECHNICAL NOTE

Note: All pin one locations for connectors and jumpers are unidentified.