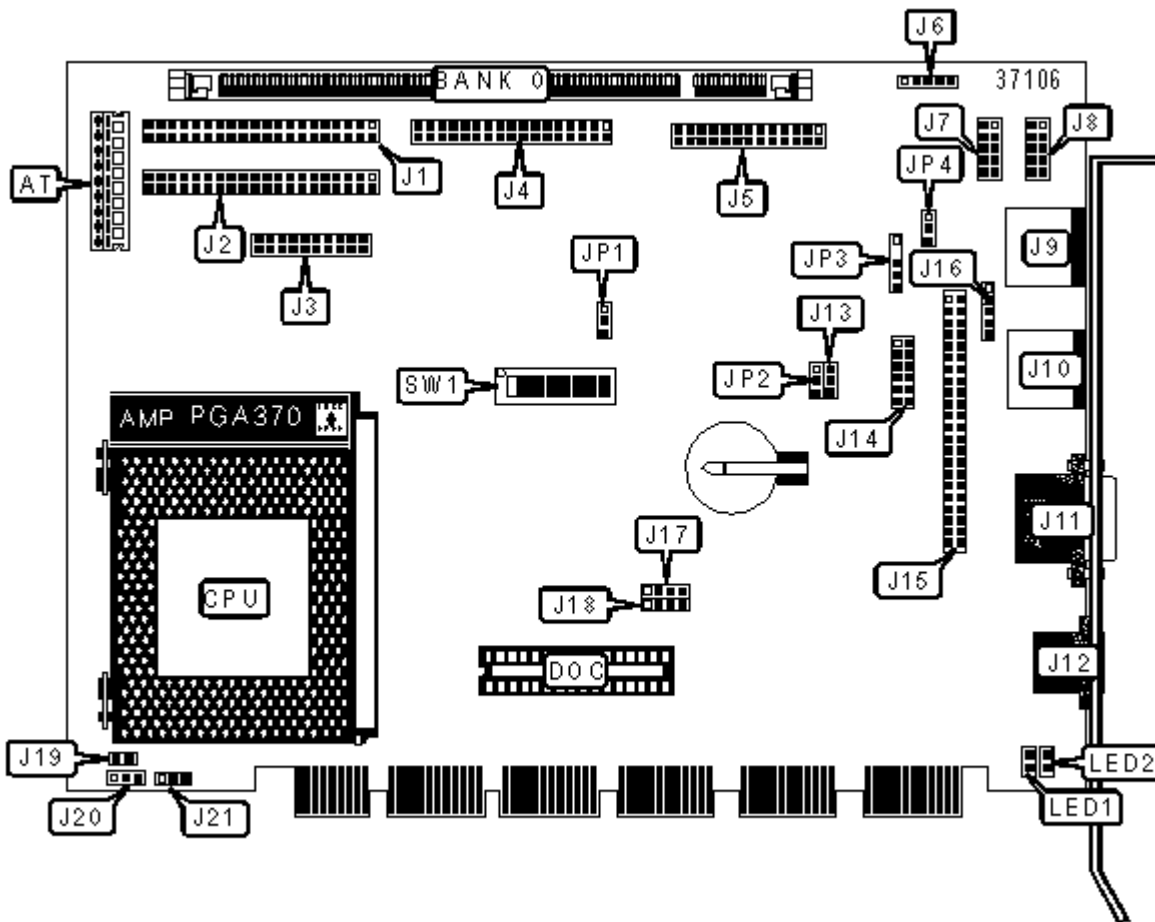


TMC RESEARCH CORPORATION

CI7BP (VER. 1.0)

Device Type	Single Board Computer
Processor	Celeron
Processor Speed	300/333/366/400/433/466/500Mhz
Chip Set	Intel 440BX
Video Chip Set	Chips and Technology
Maximum Onboard Memory	256MB (SDRAM supported)
Maximum Video Memory	2MB (SDRAM supported)
Cache	128KB
BIOS	Award
Dimensions	185mm x 129mm
I/O Options	Ethernet 10BaseT connector, Flat panel display connectors (2), Floppy drive interface, IDE interfaces (2), IR connector, Parallel port, PS/2 keyboard interface, PS/2 mouse interface, Serial ports (2), USB ports (2), VGA port



CONNECTIONS			
Purpose	Location	Purpose	Location
AT power connector	AT	PS/2 keyboard port	J9
DiskOnChip socket	DOC	PS/2 mouse port	J10
IDE interface 1	J1	VGA port	J11

IDE interface 2	J2	Ethernet RJ45 connector	J12
Speaker	J3/Pins 1 - 4	ATX power interface	J13
Green PC switch	J3/Pins 6 & 16	Flat panel display connector 1	J14
ATX power switch	J3/Pins 7 & 17	Flat panel display connector 2	J15
Turbo LED	J3/Pins 8 & 18	Unidentified	J16
Reset switch	J3/Pins 9 & 19	USB interface 1	J17
IDE interface LED	J3/Pins 10 & 20	USB interface 2	J18
Power LED & keylock	J3/Pins 11 - 15	Unidentified	J19
Floppy drive interface	J4	CPU fan power	J20
Parallel interface	J5	Chassis fan power	J21
PS/2 keyboard interface	J6	IR connector	JP3
Serial port 1	J7	LED connector 1	LED1
Serial port 2	J8	LED connector 2	LED2

USER CONFIGURABLE SETTINGS

	Function	Label	Position
»	DiskOnChip BIOS address D8000-DFFFF select	JP1	Pins 2 & 3 closed
	DiskOnChip BIOS address D0000-D7FFF select	JP1	Pins 1 & 2 closed
»	CMOS memory normal operation	JP2	Pins 1 & 2 closed
	CMOS memory clear	JP2	Pins 2 & 3 closed
»	LCD power setting 5v	JP4	Pins 1 & 2 closed
	LCD power setting 3.3v	JP4	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0
8MB	(1) 1M x 64
16MB	(1) 2M x 64
32MB	(1) 4M x 64
64MB	(1) 8M x 64

128MB	(1) 16M x 64
256MB	(1) 32M x 64

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	SW1/ 1	SW1/ 2	SW1/ 3	SW1/ 4	SW1/ 5	SW1/ 6	SW1/ 7	SW1/ 8
300MHz	66MHz	4.5x	Off	Off	On	Off	Off	On	Off	On
333MHz	66MHz	5x	Off	Off	On	Off	Off	Off	On	On
366MHz	66MHz	5.5x	Off	Off	On	Off	Off	Off	Off	On
400MHz	66MHz	6x	Off	Off	On	Off	On	On	On	Off
433MHz	66MHz	6.5x	Off	Off	On	Off	On	On	Off	Off
466MHz	66MHz	7x	Off	Off	On	Off	On	Off	On	Off
500MHz	66MHz	7.5x	Off	Off	On	Off	On	Off	Off	Off