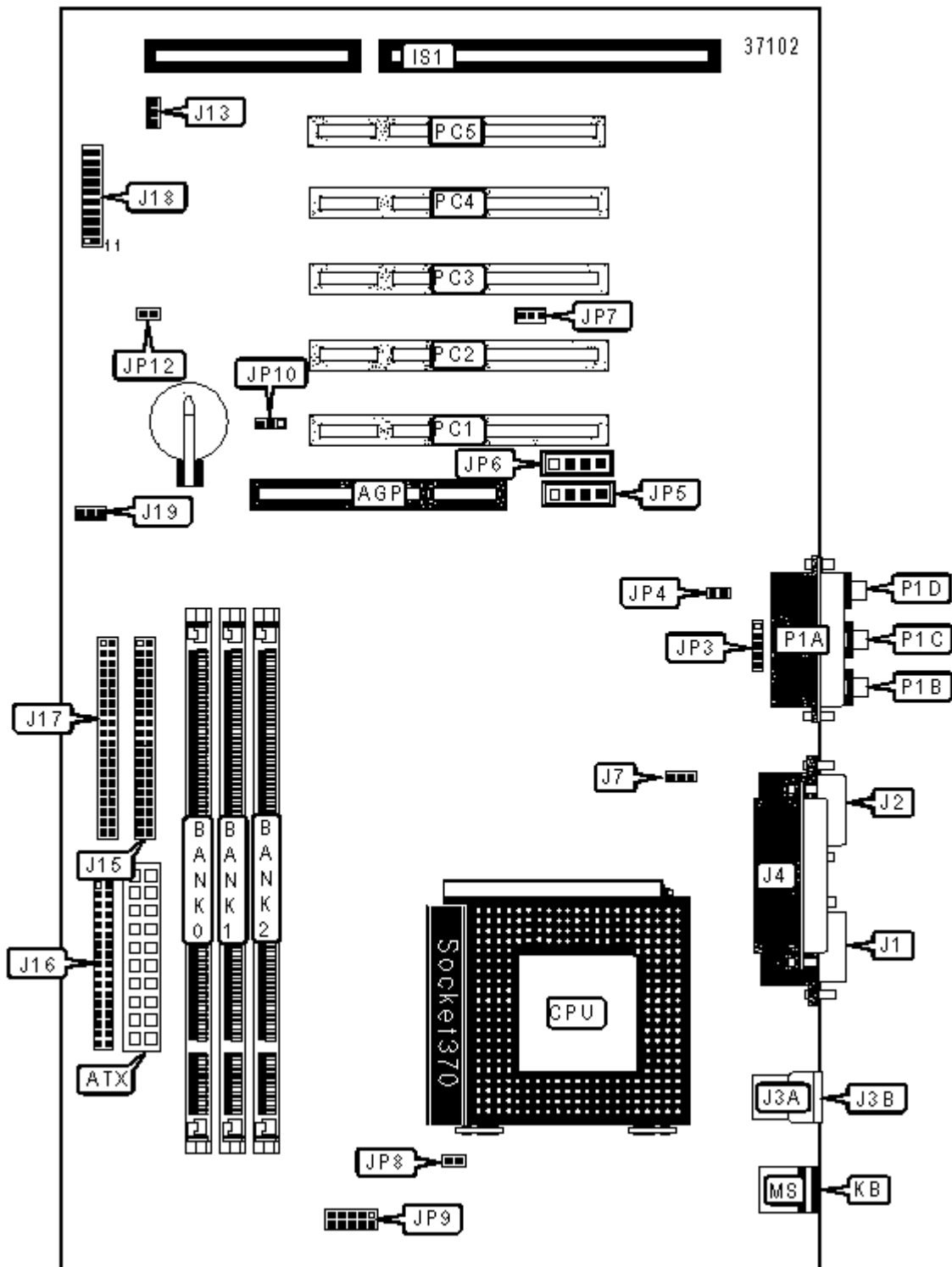


## TMC RESEARCH CORPORATION

# TI7NBF (VIA) (VER. 1.0)

<b>Device Type</b>	Mainboard
<b>Processor</b>	Celeron
<b>Processor Speed</b>	300/333/366/400/433/466/500/533MHz
<b>Chip Set</b>	VIA
<b>Audio Chip Set</b>	Creative Technology Ltd.
<b>Maximum Onboard Memory</b>	384MB (SDRAM supported)
<b>Maximum Audio Memory</b>	Unidentified
<b>Cache</b>	0/128KB (located on the Celeron CPU)
<b>BIOS</b>	Award
<b>Dimensions</b>	306mm x 180mm
<b>I/O Options</b>	16-bit ISA slot (1), 32-bit PCI slots (5), AGP slot, audio in - CD-ROMs (2), ATX power connector, floppy drive interface, game port, IDE interfaces (2), IR connector, parallel port, PS/2 mouse port, PS/2 keyboard port, serial ports (2), USB port, line in, line out, microphone in, Wake-on-LAN connector



### CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	Turbo LED connector	J18/Pins 8 & 18
ATX power connector	ATX	Reset switch	J18/Pins 9 & 19
16-bit ISA slot	IS1	IDE interface LED	J18/Pins 10 & 20
Serial port 1	J1	AGP Fan Power	J19

Serial port 2	J2	IR connector	JP3
USB port 1	J3A	Unidentified	JP4
USB port 2	J3B	Audio in - CD-ROM 1	JP5
Parallel port	J4	Audio in - CD-ROM 2	JP6
CPU fan power	J7	Wake-on-LAN connector	JP7
Chassis fan power	J13	PS/2 keyboard port	KB
IDE interface 2	J15	PS/2 mouse port	MS
Floppy drive interface	J16	Game port	P1A
IDE interface 1	J17	Line out	P1B
Speaker	J18/Pins 1-4	Line in	P1C
Green PC Connector	J18/Pins 6 & 16	Microphone in	P1D
ATX power on switch	J18/Pins 7 & 17	32-bit PCI slots	PC1 - PC5
Power LED & keylock	J18/Pins 11-15		

#### USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Bus speed select auto detect	JP8	Closed
	Bus speed select 100 MHz	JP8	Open
»	CMOS memory normal operation	JP10	Pins 1 & 2 closed
	CMOS memory clear	JP10	Pins 2 & 3 closed

#### DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None

32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board supports SDRAM memory.

### CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.