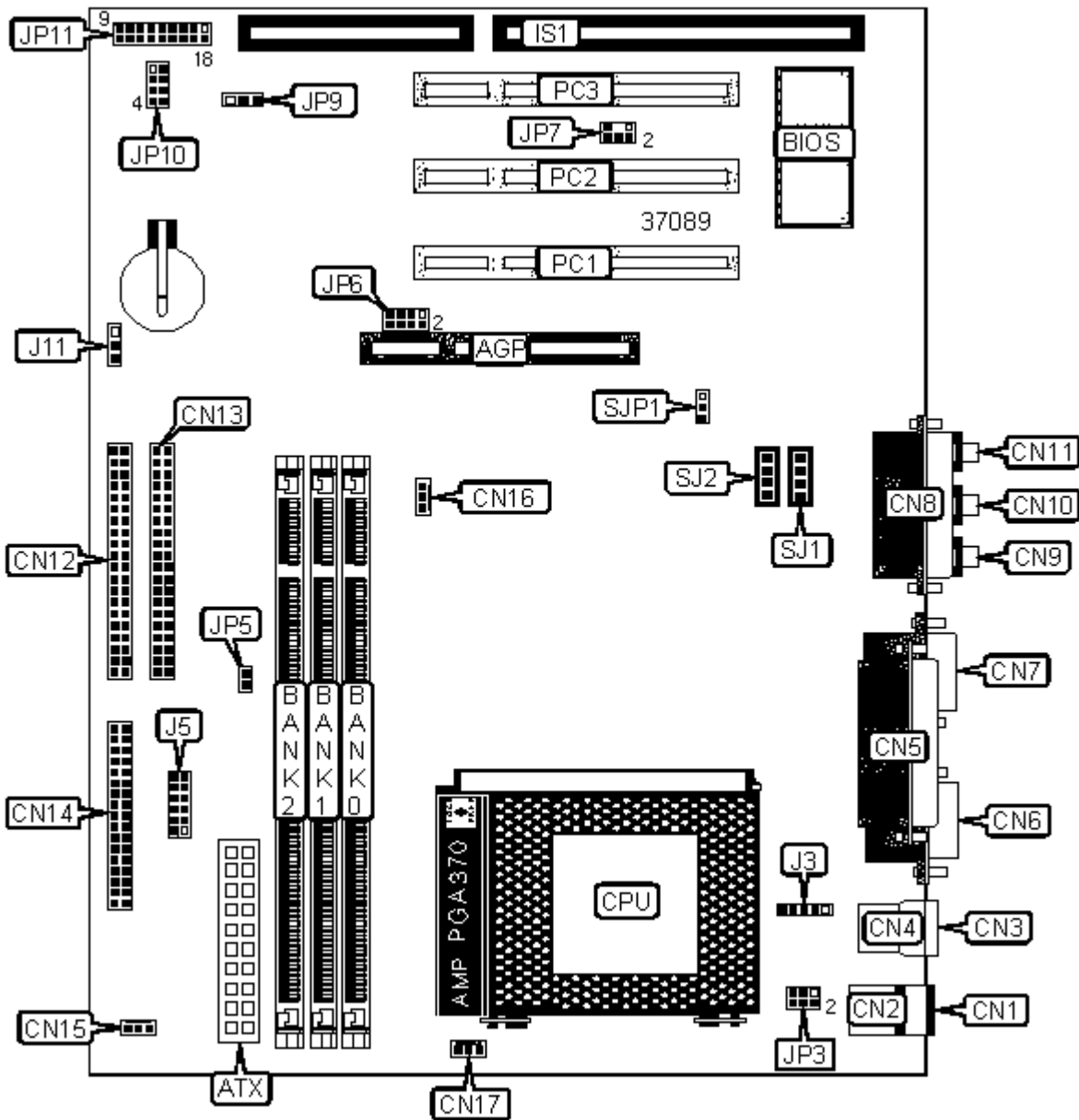


# SHUTTLE COMPUTER INTERNATIONAL, INC.

HOT-687

<b>Device Type</b>	Mainboard
<b>Processor</b>	Celeron
<b>Processor Speed</b>	300/333/350/366/400/433/450/466/500MHz
<b>Chip Set</b>	Intel 440BX
<b>Audio Chip Set</b>	Forte Media
<b>Maximum Onboard Memory</b>	768MB (SDRAM supported)
<b>Maximum Audio Memory</b>	Unidentified
<b>Cache</b>	0/128 (located on the Celeron CPU)
<b>BIOS</b>	Award
<b>Dimensions</b>	244mm x 190mm
<b>I/O Options</b>	16-bit ISA slot, 32-bit PCI slots (3), ATX power connector, audio in - CD ROM, floppy drive interface, game/MIDI port, IDE interfaces (2), IR connector, line in, line out, microphone in, parallel port, PS/2 keyboard port, PS/2 mouse port, serial ports (2), SB-Link connector, TAD connector, USB ports (2), Wake-on-LAN connector



## CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	AGP fan power	CN16
PS/2 keyboard port	CN1	CPU fan power	CN17
PS/2 mouse port	CN2	16-bit ISA slot	IS1
USB port 1	CN3	IR connector	J3
USB port 2	CN4	Wake-on-LAN connector	J11
Parallel port	CN5	IDE interface LED	JP11/Pins 2 & 3
Serial port 2	CN6	Green PC connector	JP11/Pins 4 & 5
Serial port 1	CN7	Green PC LED connector	JP11/Pins 6 & 7
Game/MIDI port	CN8	Power switch	JP11/Pins 8 & 9
Line out	CN9	Speaker	JP11/Pins 10 - 13
Line in	CN10	Power LED	JP11/Pins 14 - 16
Microphone in	CN11	Reset switch	JP11/Pins 17 & 18
IDE interface 1	CN12	SB-Link connector	JP7
IDE interface 2	CN13	32-bit PCI slots	PC1 - PC3
Floppy drive interface	CN14	TAD connector	SJ1
System fan power	CN15	Audio in - CD ROM	SJ2

## USER CONFIGURABLE SETTINGS

Function		Label	Position
»	66MHz based processor normal bus speed	JP5	Closed
	66MHz based processor overclocked to 100MHz	JP5	Open
»	CMOS memory normal operation	JP9	Pins 1 & 2 closed
	CMOS memory clear	JP9	Pins 2 & 3 closed
	Onboard sound enabled	SJP1	Pins 1 & 2 closed
	Onboard sound disabled	SJP1	Pins 2 & 3 closed

## DIMM CONFIGURATION

<b>Size</b>	<b>Bank 0</b>	<b>Bank 1</b>	<b>Bank 2</b>
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64

384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board SDRAM memory.

### CACHE CONFIGURATION

Note: 128KB cache is located on Celeron 300A and greater CPUs.

### CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP6	JP10
300MHz	66MHz	4.5x	Pins 5 & 6, 7 & 8	Pins 2 & 6, 3 & 7
300MHz	100MHz	3.0x	Pins 7 & 8	Pins 1 & 5, 3 & 7, 4 & 8
333MHz	66MHz	5.0x	Pins 5 & 6, 7 & 8	Pins 3 & 7, 4 & 8
350MHz	100MHz	3.5x	Pins 7 & 8	Pins 1 & 5, 3 & 7
366MHz	66MHz	5.5x	Pins 5 & 6, 7 & 8	Pins 3 & 7
400MHz	66MHz	6.0x	Pins 5 & 6, 7 & 8	Pins 1 & 5, 2 & 6, 4 & 8
400MHz	100MHz	4.0x	Pins 7 & 8	Pins 2 & 6, 3 & 7, 4 & 8
433MHz	66MHz	6.5x	Pins 5 & 6, 7 & 8	Pins 1 & 5, 2 & 6
450MHz	100MHz	4.5x	Pins 7 & 8	Pins 2 & 6, 3 & 7
466MHz	66MHz	7.0x	Pins 5 & 6, 7 & 8	Pins 1 & 5, 4 & 8
500MHz	66MHz	7.5x	Pins 5 & 6, 7 & 8	Pins 1 & 5
500MHz	100MHz	5.0x	Pins 7 & 8	Pins 3 & 7, 4 & 8

Note: Designated pins should be in the closed position.

### POWER-ON BY KEYBOARD/MOUSE SELECTION

Function	JP3
Power-on by keyboard/mouse disabled	Pins 3 & 5, 4 & 6 closed

Power-on by keyboard/mouse enabled	Pins 1 & 3, 4 & 6 closed
Power-on by mouse enabled	Pins 2 & 4, 3 & 5 closed