## SUPERPOWER COMPUTER CO., LTD.

SP-6XV, SP-6XV-A66, SP-6XV-133

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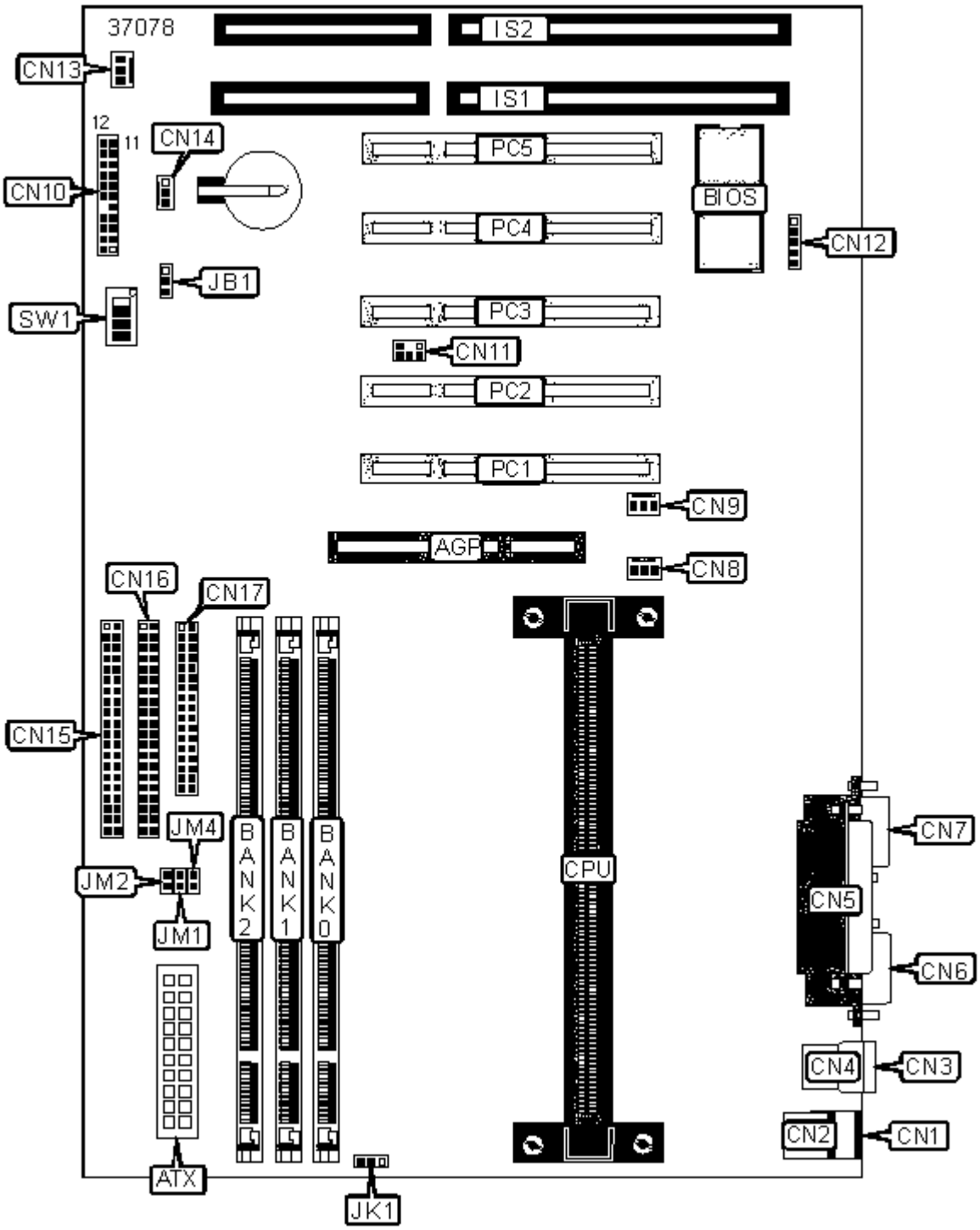
|---|---|
| **Device Type** | Mainboard |
| **Processor** | Celeron/Pentium II/Pentium III |
| **Processor Speed** | 233/266/300/333/350/366/400/433/450/500/<br>533/550/600/650/667/700/733MHz |
| **Chip Set** | VIA |
| **Maximum Onboard Memory** | 1.2GB (EDO & SDRAM supported) |
| **Cache** | 0/128/256/512KB (located on the CPU) |
| **BIOS** | Award |
| **Dimensions** | 305mm x 190mm |
| **I/O Options** | 16-bit ISA slots (2), 32-bit PCI slots (5), AGP slot, ATX power connector, floppy drive interface, green PC switch, IDE interfaces (2), IR connector, parallel port, PS/2 keyboard port, PS/2 mouse port, serial ports (2), USB ports (2), Wake-on-LAN connector |




| CONNECTIONS | | | |
|---|---|---|---|
| **Purpose** | **Location** | **Purpose** | **Location** |
| AGP slot | AGP | Green PC switch | CN10/Pins 7 & 16 |
| ATX power connector | ATX | Reset switch | CN10/Pins 8 & 15 |
| PS/2 keyboard port | CN1 | Green PC LED | CN10/Pins 10 & 13 |
| PS/2 mouse port | CN2 | PS-ON | CN10/Pins 11 & 12 |
| USB port 2 | CN3 | Speaker | CN10/Pins 19 - 22 |

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|---|---|---|---|
| USB port 1 | CN4 | SB-Link connector | CN11 |
| Parallel port | CN5 | IR connector | CN12 |
| Serial port 1 | CN6 | System fan power | CN13 |
| Serial port 2 | CN7 | Wake-on-LAN connector | CN14 |
| CPU fan power | CN8 | IDE interface 1 | CN15 |
| AGP fan power | CN9 | IDE interface 2 | CN16 |
| Power LED | CN10/Pins 1 - 3 | Floppy drive interface | CN17 |
| Keylock | CN10/Pins 4 & 5 | 16-bit ISA slots | IS1 - IS2 |
| IDE interface LED | CN10/Pins 6 & 17 | 32-bit PCI slots | PC1 - PC5 |

| USER CONFIGURABLE SETTINGS | | | |
|---|---|---|---|
| **Function** | | **Label** | **Position** |
| » | CMOS memory normal operation | JB1 | Pins 1 & 2 closed |
| | CMOS memory clear | JB1 | Pins 2 & 3 closed |
| » | Power-on by keyboard disabled | JK1 | Pins 1 & 2 closed |
| | Power-on by keyboard enabled | JK1 | Pins 2 & 3 closed |
| | Auto-detection of CPU bus speed enabled (66MHz/100MHz only) | JM1 | Closed |
| | Auto-detection of CPU bus speed disabled | JM1 | Open |
| Note: JM1 setting of auto-detect bus speed (closed) will override bus speed set by JM2 and JM4. | | | |

| DIMM CONFIGURATION | | | |
|---|---|---|---|
| **Size** | **Bank 0** | **Bank 1** | **Bank 2** |
| 16MB | (1) 2M x 64 | None | None |
| 32MB | (1) 2M x 64 | (1) 2M x 64 | None |
| 32MB | (1) 4M x 64 | None | None |
| 48MB | (1) 2M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 64MB | (1) 4M x 64 | (1) 4M x 64 | None |
| 64MB | (1) 8M x 64 | None | None |

| | | | |
|---|---|---|---|
| 64MB | (1) 4M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 96MB | (1) 4M x 64 | (1) 4M x 64 | (1) 4M x 64 |
| 96MB | (1) 8M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 128MB | (1) 8M x 64 | (1) 8M x 64 | None |
| 128MB | (1) 16M x 64 | None | None |
| 128MB | (1) 8M x 64 | (1) 4M x 64 | (1) 4M x 64 |
| 160MB | (1) 16M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 192MB | (1) 8M x 64 | (1) 8M x 64 | (1) 8M x 64 |
| 192MB | (1) 16M x 64 | (1) 4M x 64 | (1) 4M x 64 |
| 256MB | (1) 16M x 64 | (1) 16M x 64 | None |
| 256MB | (1) 32M x 64 | None | None |
| 256MB | (1) 16M x 64 | (1) 8M x 64 | (1) 8M x 64 |
| 288MB | (1) 32M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 320MB | (1) 32M x 64 | (1) 4M x 64 | (1) 4M x 64 |
| 384MB | (1) 16M x 64 | (1) 16M x 64 | (1) 16M x 64 |
| 384MB | (1) 32M x 64 | (1) 8M x 64 | (1) 8M x 64 |
| 512MB | (1) 32M x 64 | (1) 32M x 64 | None |
| 512MB | (1) 32M x 64 | (1) 16M x 64 | (1) 16M x 64 |
| 512MB | (1) 64M x 64 | None | None |
| 768MB | (1) 32M x 64 | (1) 32M x 64 | (1) 32M x 64 |
| 1024MB | (1) 64M x 64 | (1) 64M x 64 | None |
| 1040MB | (1) 64M x 64 | (1) 64M x 64 | (1) 2M x 64 |
| 1056MB | (1) 64M x 64 | (1) 64M x 64 | (1) 4M x 64 |
| 1088MB | (1) 64M x 64 | (1) 64M x 64 | (1) 8M x 64 |
| 1152MB | (1) 64M x 64 | (1) 64M x 64 | (1) 16M x 64 |
| 1280MB | (1) 64M x 64 | (1) 64M x 64 | (1) 32M x 64 |

Note: Board supports EDO & SDRAM memory.
Note: PC-133 modules are required in banks 1 and 2 to achieve settings above 768MB.

| CACHE CONFIGURATION |
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| Note: 128KB cache is located on Celeron 300A and greater CPUs. 256/512KB cache is located on the Pentium II and Pentium III CPUs. |

| CPU SPEED SELECTION (CELERON) | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| **CPU Speed** | **Clock Speed** | **Multiplier** | **SW1/1** | **SW1/2** | **SW1/3** | **SW1/4** | **JM2** | **JM4** |
| 266MHz | 66MHz | 4.0x | On | On | On | Off | Closed | Closed |
| 300MHz | 66MHz | 4.5x | On | Off | On | Off | Closed | Closed |
| 333MHz | 66MHz | 5.0x | On | On | Off | Off | Closed | Closed |
| 366MHz | 66MHz | 5.5x | On | Off | Off | Off | Closed | Closed |
| 400MHz | 66MHz | 6.0x | Off | On | On | On | Closed | Closed |
| 433MHz | 66MHz | 6.5x | Off | Off | On | On | Closed | Closed |
| Note: The bus speed set by jumpers JM2 and JM4 will be overridden by the auto-detect setting, if jumper JM1 is set to the closed position. | | | | | | | | |

| CPU SPEED SELECTION (PENTIUM II) | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| **CPU Speed** | **Clock Speed** | **Multiplier** | **SW1/1** | **SW1/2** | **SW1/3** | **SW1/4** | **JM2** | **JM4** |
| 233MHz | 66MHz | 3.5x | On | Off | Off | On | Closed | Closed |
| 266MHz | 66MHz | 4.0x | On | On | On | Off | Closed | Closed |
| 300MHz | 66MHz | 4.5x | On | Off | On | Off | Closed | Closed |
| 333MHz | 66MHz | 5.0x | On | On | Off | Off | Closed | Closed |
| 350MHz | 100MHz | 3.5x | On | Off | Off | On | Open | Closed |
| 400MHz | 100MHz | 4.0x | On | On | On | Off | Open | Closed |
| 450MHz | 100MHz | 4.5x | On | Off | On | Off | Open | Closed |
| Note: The bus speed set by jumpers JM2 and JM4 will be overridden by the auto-detect setting, if jumper JM1 is set to the closed position. | | | | | | | | |

| CPU SPEED SELECTION (PENTIUM III) | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| **CPU Speed** | **Clock Speed** | **Multiplier** | **SW1/1** | **SW1/2** | **SW1/3** | **SW1/4** | **JM2** | **JM4** |

| | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| 450MHz | 100MHz | 4.5x | On | Off | On | Off | Open | Closed |
| 500MHz | 100MHz | 5.0x | On | On | Off | Off | Open | Closed |
| 533MHz | 133MHz | 4.0x | On | On | On | Off | Open | Open |
| 550MHz | 100MHz | 5.5x | On | Off | Off | Off | Open | Closed |
| 600MHz | 100MHz | 6.0x | Off | On | On | On | Open | Closed |
| 600MHz | 133MHz | 4.5x | On | Off | On | Off | Open | Open |
| 650MHz | 100MHz | 6.5x | Off | Off | On | On | Open | Closed |
| 667MHz | 133MHz | 5.0x | On | On | Off | Off | Open | Open |
| 700MHz | 100MHz | 7.0x | Off | On | Off | On | Open | Closed |
| 733MHz | 133MHz | 5.5x | On | Off | Off | Off | Open | Open |

Note: The bus speed set by jumpers JM2 and JM4 will be overridden by the auto-detect setting, if jumper JM1 is set to the closed position.
Note: Auto-detect setting can not be used to detect 133MHz bus speed.