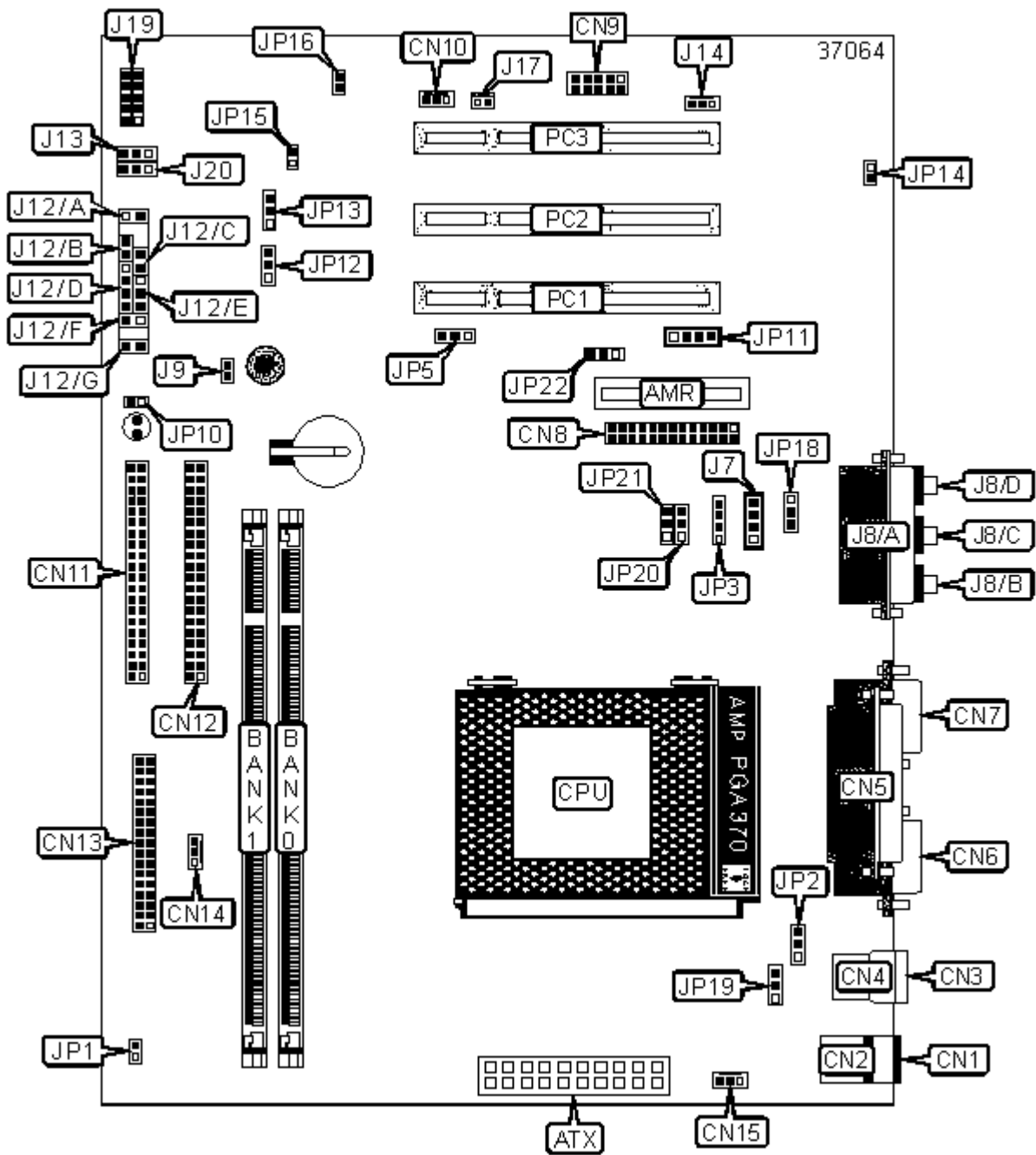


GIGA-BYTE TECHNOLOGY CO., LTD.

GA-6WMMC7-1 (REV. 1.3), GA-6WMMC7-2 (REV. 1.3)

Device Type	Mainboard
Processor	Celeron
Processor Speed	200/233/266/300/333/366/400/433/466/500/533MHz
Chip Set	Intel 810
Video Chip Set	Unidentified
Maximum Onboard Memory	512MB (SDRAM supported)
Maximum Video Memory	Unidentified
Cache	0/128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	243mm x 190mm
I/O Options	32-bit PCI slots (3), Audio/Modem riser slot, ATX power connector, audio in - CD-ROM, auxiliary in, digital flat panel connector, floppy drive interface, front panel USB interface, game/MIDI port, green PC connector, IDE interfaces (2), IR connector, line in, line out, microphone in, parallel port, PS/2 keyboard port, PS/2 mouse port, serial port, SPDIF connector, STR LED connector, USB ports (2), VGA port, voice modem connector, Wake-on-LAN connector, Wake-on-modem connector



CONNECTIONS

Purpose	Location	Purpose	Location
Audio/modem riser slot	AMR	Line out	J8/B
ATX power connector	ATX	Line in	J8/C
PS/2 keyboard port	CN1	Microphone in	J8/D
PS/2 mouse port	CN2	Green LED	J12/A
USB port 1	CN3	Reset switch	J12/B
USB port 2	CN4	Power switch	J12/C

Parallel port	CN5	Speaker	J12/D
Serial port 1	CN6	Power LED	J12/E
VGA port	CN7	IDE interface LED	J12/F
Digital flat panel connector	CN8	Green PC switch	J12/G
IR connector	CN9	Wake-on-LAN connector	J14
System fan power	CN10	Wake-on-modem connector	J17
IDE interface 1	CN11	Front panel USB interface	J19
IDE interface 2	CN12	Voice modem connector	JP3
Floppy drive interface	CN13	STR LED connector	JP10
Power supply fan power	CN14	Auxiliary in	JP11
CPU fan power	CN15	Chassis intrusion connector	JP14
Audio in - CD-ROM	J7	SPDIF connector	JP18
Game/MIDI port	J8/A	32-bit PCI slots	PC1 - PC3

USER CONFIGURABLE SETTINGS

Function		Label	Position
	Buzzer enabled	J9	Closed
	Buzzer disabled	J9	Open
	STR disabled	JP1	Open
	STR enabled	JP1	Closed
»	Power-on by keyboard disabled	JP2	Pins 2 & 3 closed
	Power-on by keyboard enabled	JP2	Pins 1 & 2 closed
»	CMOS memory normal operation	JP12	Pins 2 & 3 closed
	CMOS memory clear	JP12	Pins 1 & 2 closed
	Reboot on timeout	JP15	Open
	Do not reboot on timeout	JP15	Closed
»	Table is unlocked	JP16	Closed
	Table is locked	JP16	Open

»	Wake-on-USB keyboard disabled	JP19	Pins 1 & 2 closed
	Wake-on-USB keyboard enabled	JP19	Pins 2 & 3 closed

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
16MB	(1) 2M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
256MB	(1) 32M x 64	None
264MB	(1) 32M x 64	(1) 1M x 64
272MB	(1) 32M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64

320MB	(1) 32M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64
<p>Note: Board supports SDRAM memory. Note: PC100 modules must be used.</p>		

CACHE CONFIGURATION

Note: 128KB cache is located on Celeron 300A and greater CPUs.

SYSTEM BUS FORCED SPEED SELECTION

Setting	JP5
Auto	Pins 1 & 2 closed
Force 66MHz	Pins 2 & 3 closed
Force 100MHz	Open

USB PORT SELECTION

Setting	JP13	JP20
Front panel USB selected	Pins 1 & 2 closed	Pins 1 & 2 closed
Back panel USB selected	Pins 2 & 3 closed	Pins 2 & 3 closed

SYSTEM BOOT SELECTION

Setting	JP13
Normal	Pins 1 & 2 closed
Safe mode	Pins 2 & 3 closed
Recovery	Open

QUAD SPEAKER SELECTION

Setting	JP20	JP21
Line in	Pins 1 & 2 closed	Pins 1 & 2 closed
Quad out	Pins 2 & 3 closed	Pins 2 & 3 closed

AMR SELECTION

AMR Card	Onboard CDOEC	JP22
Secondary	Primary	Pins 1 & 2 closed
Primary	Disabled	Pins 2 & 3 closed