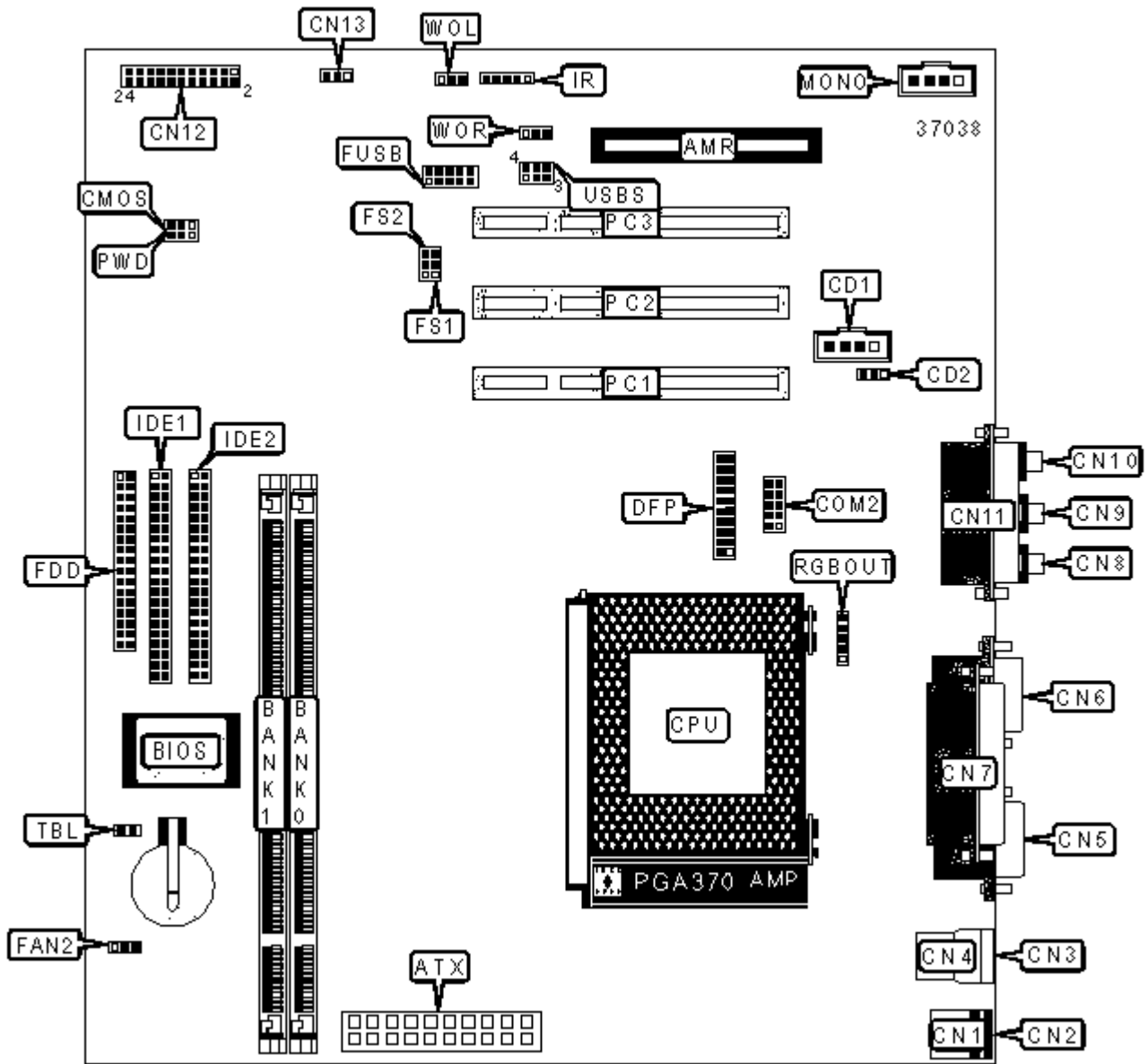


FIRST INTERNATIONAL COMPUTER, INC.

CW33

Device Type	Mainboard
Processor	Celeron
Processor Speed	333/366/400/433/466/500MHz
Chip Set	Intel 810
Video Chip Set	Intel
Audio Chip Set	ESS
Maximum Onboard Memory	512MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	244mm x 211mm
I/O Options	32-bit PCI slots (3), floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial port, serial interface, VGA port, IR connector, USB interface, USB ports (2), ATX power connector, Audio/Modem Riser slot, line in, line out, microphone in, audio in - CD-ROMs (2), Modem audio connector, Wake-on-LAN connector, Wake-on-Ring connector, TV-out connector, Flat Panel display connector



CONNECTIONS

Purpose	Location	Purpose	Location
Audio/Modem Riser slot	AMR	Reset switch	CN12/Pins 15 & 17
ATX power connector	ATX	Green PC switch	CN12/Pins 21 & 23
Audio in - CD-ROM 1	CD1	Message LED	CN12/Pins 22 & 24
Audio in - CD-ROM 2	CD2	Chassis intrusion connector	CN13
PS/2 mouse port	CN1	Serial interface	COM2
PS/2 keyboard port	CN2	Digital Flat Panel connector	DFP
USB port 1	CN3	CPU fan power	FAN1
USB port 2	CN4	Chassis fan power	FAN2
Serial port	CN5	Floppy drive interface	FDD

VGA port	CN6	USB interface	FUSB
Parallel port	CN7	IDE interface 1	IDE1
Line out	CN8	IDE interface 2	IDE2
Line in	CN9	IR connector	IR
Microphone in	CN10	Audio in - Modem	MONO
Game/MIDI port	CN11	32-bit PCI slots	PC1 - PC3
IDE interface LED	CN12/Pins 1 & 2	TV-out connector	RGBOUT
Speaker	CN12/Pins 7, 9, 11 & 13	Wake-on-LAN connector	WOL
Power LED	CN12/Pins 8, 10 & 12	Wake-on-Ring connector	WOR
Soft off power supply	CN12/Pins 14 & 16		

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	CMOS	Pins 1 & 2 closed
	CMOS memory clear	CMOS	Pins 2 & 3 closed
»	Back USB connector selected	USBS	Pins 2 & 3, 5 & 6 closed
	Front USB connector selected	USBS	Pins 1 & 2, 4 & 5 closed
»	BIOS top block locked	TBL	Open
	BIOS top block unlocked	TBL	Closed
»	Password normal operation	PWD	Pins 1 & 2 closed
	Password clear	PWD	Pins 2 & 3 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None

32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
256MB	(2) 32M x 36	None
264MB	(2) 32M x 36	(2) 1M x 36
272MB	(2) 32M x 36	(2) 2M x 36
288MB	(2) 32M x 36	(2) 4M x 36
320MB	(2) 32M x 36	(2) 8M x 36
384MB	(2) 32M x 36	(2) 16M x 36
512MB	(2) 32M x 36	(2) 32M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.

BUS SIZE SELECTION

Setting	FS1	FS2
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Autodetect	Pins 2 & 3 closed	Pins 2 & 3 closed
100MHz	Pins 2 & 3 closed	Pins 1 & 2 closed
133MHz	Pins 1 & 2 closed	Pins 1 & 2 closed