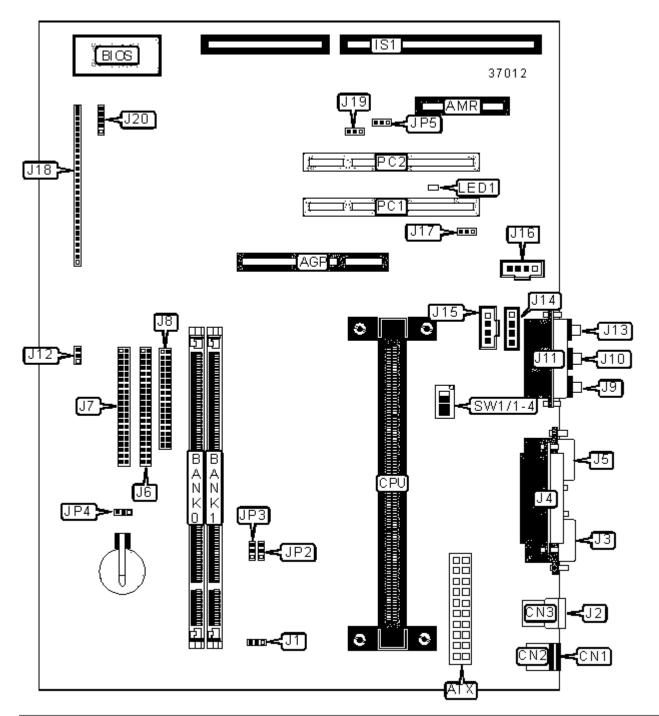
## DIAMOND FLOWER, INC.

PA33 (REV. A+)

Device Type	Mainboard
Processor	Celeron/Pentium II/Pentium III
Processor Speed	233/266/300/333/350/366/400/433/450/500/533/550/600MHz
Chip Set	VIA
Maximum Onboard Memory	512MB (SDRAM supported)
Cache	0/128/256/512KB (located on the CPU)
BIOS	Award
Dimensions	243mm x 220mm
I/O Options	16-bit ISA slot (1), 32-bit PCI slots (2), floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial ports (2), Audio Mocem Riser slot, IR connector, USB ports (2), ATX power connector, AGP slot, line in, line out, microphone in, audio in - CD-ROMs (3), Wake-on-LAN connector



CONNECTIONS					
Purpose	Location	Purpose	Location		
AGP slot	AGP	Chassis fan power	J12		
Audio Modem Riser slot	AMR	Microphone in	J13		
ATX power connector	ATX	Audio in - CD-ROM (Sony)	J14		
PS/2 keyboard port	CN1	Audio in - CD-ROM (Sony)	J15		
PS/2 mouse port	CN2	Audio in - CD-ROM (Mitsumi)	J16		
USB port 1	CN3	AGP fan power	J17		

16-bit ISA slot	IS1	ATX LED	J18/Pins 1 & 2
CPU fan power	J1	IDE interface LED	J18/Pins 4 & 5
USB port 2	J2	Green PC LED	J18/Pins 7 & 8
Serial port 1	J3	Power switch	J18/Pins 10 & 11
Parallel port	J4	Green PC connector	J18/Pins 13 & 14
Serial port 2	J5	Reset switch	J18/Pins 16 & 17
IDE interface 1	J6	Speaker	J18/Pins 19 - 22
IDE interface 2	J7	Power LED	J18/Pins 24 - 28
Floppy drive interface	J8	Wake-on-LAN connector	J19
Line out	J9	IR connector	J20
Line in	J10	32-bit PCI slots	PC1 - PC2
Game/MIDI port	J11		

	USER CONFIGURABLE SETTINGS					
	Function Label Position					
»	CMOS memory normal operation	JP4	Pins1 & 2 closed			
	CMOS memory clear	JP4	Pins 2 & 3 closed			
»	On-board audio CODEC primary	JP5	Pins 1 & 2 closed			
	AMR audio CODEC primary	JP5	Pins 2 & 3 closed			

DIMM CONFIGURATION					
Size	Bank 0	Bank 1			
16MB	(1) 2M x 64	None			
32MB	(1) 2M x 64	(1) 2M x 64			
32MB	(1) 4M × 64	None			
48MB	(1) 4M × 64	(1) 2M x 64			
64MB	(1) 4M × 64	(1) 4M x 64			
64MB	(1) 8M × 64	None			
80MB	(1) 8M x 64	(1) 2M x 64			

96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M × 64	(1) 8M x 64
128MB	(1) 16M x 64	None
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M × 64
256MB	(1) 32M x 64	None
272MB	(1) 32M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64
320MB	(1) 32M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M × 64
512MB	(1) 32M x 64	(1) 32M x 64

## CACHE CONFIGURATION

Note: 512KB cache is located on the Pentium III CPUs. 256KB/512KB cache is located on the Pentium II CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.

CPU SPEED SELECTION (CELERON)						
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
266MHz	66MHz	4x	On	On	Off	On
300MHz	66MHz	4.5x	Off	On	Off	On
333MHz	66MHz	5x	On	Off	Off	On
366MHz	66MHz	5.5x	Off	Off	Off	On
400MHz	66MHz	6x	On	On	On	Off
433MHz	66MHz	6.5x	Off	On	On	Off

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
233MHz	66MHz	3.5x	Off	Off	On	On
266MHz	66MHz	4x	On	On	Off	On
300MHz	66MHz	4.5x	Off	On	Off	On
333MHz	66MHz	5x	On	Off	Off	On
350MHz	100MHz	3.5x	Off	Off	On	On
400MHz	100MHz	4x	On	On	Off	On
450MHz	100MHz	4.5x	Off	On	Off	On
Note: Pins des	ignated should be	e in the closed po	sition.		1	1

	CPU SPEED SELECTION (PENTIUM III)						
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	
450MHz	100MHz	4.5x	Off	On	Off	On	
500MHz	100MHz	5x	On	Off	Off	On	
533MHz	133MHz	4x	On	On	Off	On	
550MHz	100MHz	5.5x	Off	Off	Off	On	
600MHz	100MHz	6x	On	On	On	Off	
600MHz	133MHz	4.5	Off	On	Off	On	
Note: Pins des	ignated should be	e in the closed po	sition.				

	EXTERNAL CLOCK SPEED SELECTION						
	Setting JP2 JP3						
»	Auto detect	Pins 1 & 2 closed	Pins1 & 2 closed				
	66MHz	Pins 2 & 3 closed	Pins 2 & 3 closed				
	100MHz	Open Pins 2 & 3 closed					
	133MHz	Pins 1 & 2 closed	Open				

DIAGNOSTIC LED(S)					
LED	Color	Status	Condition		

LED1	Unidentified	On	System is in the power on, soft off, or suspend modes
LED1	Unidentified	Off	System is not in the power on, soft off, or suspend modes