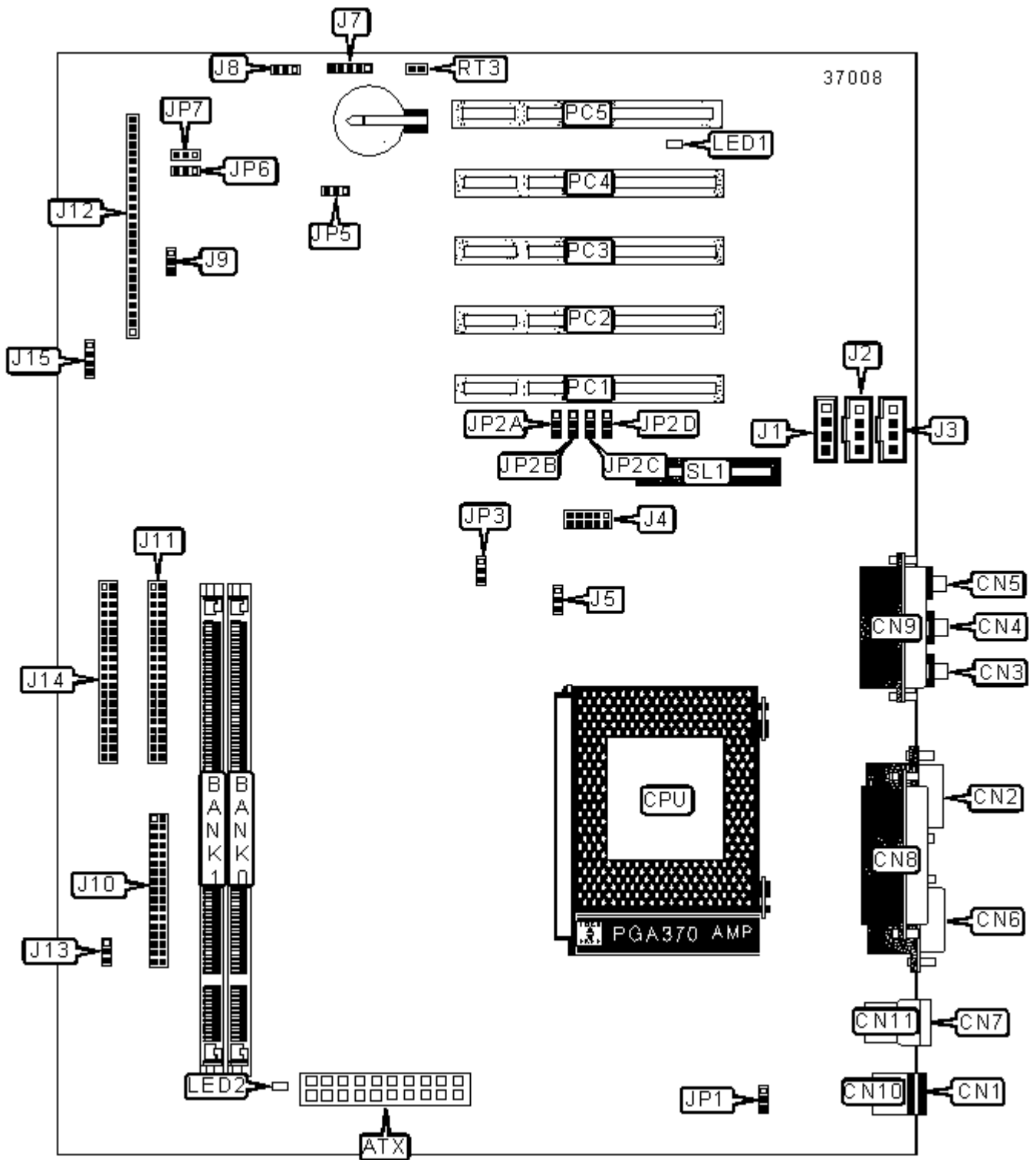


DIAMOND FLOWER, INC.

CW65-L (REV. A+), CW65-S (REV. A+)

Device Type	Mainboard
Processor	Celeron
Processor Speed	300/333/366/400/433/466/500MHz
Chip Set	Intel 810-L/810
Video Chip Set	Unidentified
Audio Chip Set	Unidentified
Maximum Onboard Memory	512MB (SDRAM supported)
Maximum Video Memory	1MB (shared from system memory)
Maximum Audio Memory	Unidentified
Cache	0/128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	305mm x 180mm
I/O Options	32-bit PCI slots (5), floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial port, serial interface, VGA port, Audio Modem Riser slot, IR connector, USB ports (2), ATX power connector, line in, line out, microphone in, audio in - CD-ROMs (3), Wake-on-Lan connector,



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Chassis fan power 1	J8
PS/2 keyboard port	CN1	Wake-on-LAN connector	J9
VGA port	CN2	Floppy drive interface	J10
Line out	CN3	IDE interface 2	J11

Line in	CN4	IDE interface LED	J12/Pins 1 & 2
Microphone in	CN5	Green PC LED	J12/Pins 4 & 5
Serial port	CN6	Power switch	J12/Pins 7 & 8
USB port 1	CN7	Green PC connector	J12/Pins 10 & 11
Parallel port	CN8	Reset switch	J12/Pins 13 & 14
Game/MIDI port	CN9	Speaker	J12/Pins 16-19
PS/2 mouse port	CN10	Power LED & keylock	J12/Pins 21-25
USB port 2	CN11	Chassis fan power 2	J13
Audio in - CD-ROM (Sony)	J1	IDE interface 1	J14
Audio in - CD-ROM (Sony)	J2	Chasis intrusion connector	J15
Audio in - CD-ROM (Mitsumi)	J3	32-bit PCI slots	PC1 - PC5
Serial interface	J4	Thermal connector	RT3
CPU fan power	J5	Audio Modem Riser slot	SL1
IR connector	J7		

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Keyboard/mouse power on disabled	JP1	Pins 1 & 2 closed
	Keyboard/mouse power on enabled	JP1	Pins 2 & 3 closed
»	CMOS memory normal operation	JP5	Pins 1 & 2 closed
	CMOS memory clear	JP5	Pins 2 & 3 closed
»	Factory configured - do not alter	JP6	Pins 1 & 2 closed
»	Beep message sent to PC speaker	JP7	Pins 2 & 3 closed
	Beep message sent to external speaker	JP7	Pins 1 & 2 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1
16MB	(1) 2M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64

32MB	(1) 4M x 64	None
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
256MB	(1) 32M x 64	None
272MB	(1) 32M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64
320MB	(1) 32M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64
Note: Board supports SDRAM memory.		

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.

EXTERNAL CLOCK SPEED SELECTION

	Speed	JP3
»	Auto-detect	Pins 1 & 2 closed
	66MHz	Pins 2 & 3 closed
	100MHz	Open

AUDIO MODEM RISER SETTINGS

Setting		JP2A	JP2B	JP2C	JP2D
»	Modem Riser only/CODEC enabled	1 & 2	1 & 2	1 & 2	1 & 2
	Audio Modem Riser/CODEC disabled	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

DIAGNOSTIC LED(S)

LED	Color	Status	Condition
LED1	Unidentified	On	PCI standby power is on
LED1	Unidentified	Off	PCI standby power is off
LED2	Unidentified	On	DIMM standby power is on
LED2	Unidentified	Off	DIMM standby power is off