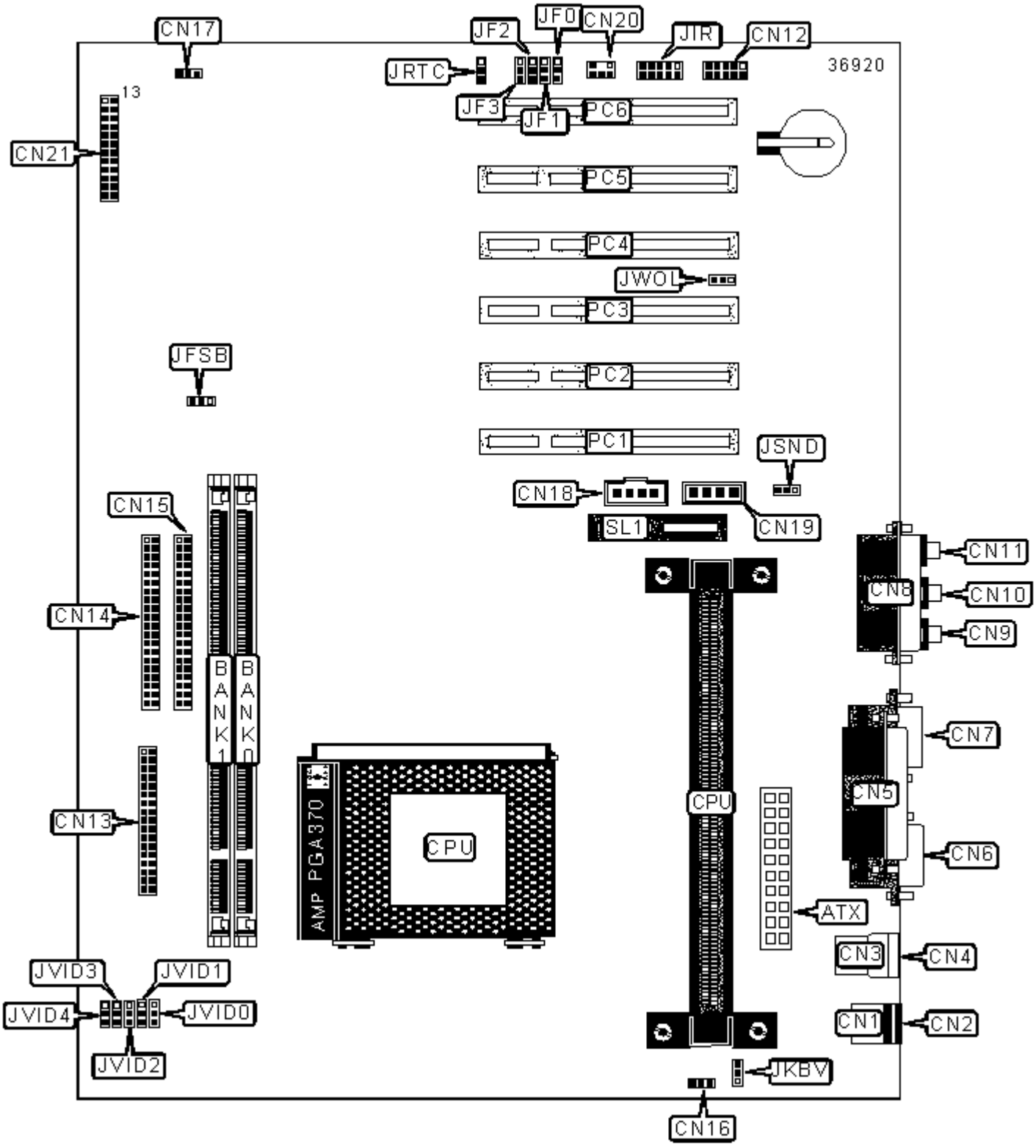
ACER, INC.

P3WA

Device Type	Mainboard
Processor	Celeron/Pentium II/Pentium III
Processor Speed	233/266/300/333/350/366/400/433/450/466/500MHz
Chip Set	Intel 810
Video Chip Set	Unidentified
Audio Chip Set	ADI
Maximum Onboard Memory	512MB (SDRAM supported)
Maximum Video Memory	4MB
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	Award
Dimensions	305mm x 180mm
I/O Options	32-bit PCI slots (6), floppy drive interface, game port, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial port, serial interface, VGA port, Audio Modem Riser slot, IR connector, USB ports (2), ATX power connector, AGP slot, line in, line out, microphone in, audio in - CD-ROMs (2), Wake-on-LAN connector, SB-Link connector



CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
ATX power connector	ATX	CPU fan power	CN16
PS/2 mouse port	CN1	Chassis fan power	CN17
PS/2 keyboard port	CN2	Audio in - CD-ROM (Sony)	CN18

USB port 1	CN3	Audio in - CD-ROM (Mitsumi)	CN19
USB port 2	CN4	SB-Link connector	CN20
Parallel port	CN5	Green PC LED	CN21/Pins 1 & 2
Serial port	CN6	Reset switch	CN21/Pins 3 & 4
VGA port	CN7	IDE interface LED	CN21/Pins 6 & 7
Game port	CN8	Power switch	CN21/Pins 9 & 10
Line out	CN9	Power LED & keylock	CN21/Pins 14-18
Line in	CN10	Speaker	CN21/Pins 20-23
Microphone in	CN11	IR connector	JIR
Serial interface	CN12	Wake-on-LAN connector	JWOL
Floppy drive interface	CN13	32-bit PCI slots	PC1 - PC6
IDE interface 1	CN14	Audio Modem Riser slot	SL1
IDE interface 2	CN15		

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
»	Keyboard voltage selection 5V	JKBV	Pins 1 & 2 closed
	Keyboard voltage selection 5V stand by	JKBV	Pins 2 & 3 closed
»	CMOS memory normal operation	JRTC	Pins 2 & 3 closed
	CMOS memory clear	JRTC	Pins 1 & 2 closed
»	Audio CODEC enabled	JSND	Pins 1 & 2 closed
	Audio CODEC disabled	JSND	Pins 2 & 3 closed

DIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
16MB	(1) 2M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
48MB	(1) 4M x 64	(1) 2M x 64

64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
256MB	(1) 32M x 64	None
272MB	(1) 32M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64
320MB	(1) 32M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64
Note: Board supports SDRAM memory.		

CACHE CONFIGURATION
Note: 512KB cache is located on the Pentium III CPUs. 256KB/512KB cache is located on the Pentium II CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.

CPU SPEED SELECTION (CELERON)							
CPU speed	**Clock speed**	**Multiplier**	**JF0**	**JF1**	**JF2**	**JF3**	**JFSB**
266MHz*	66MHz	4x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
300MHz	66MHz	4.5x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
333MHz	66MHz	5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3
366MHz*	66MHz	5.5x	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3
400MHz	66MHz	6x	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3

433MHZ	66MHz	6.5x	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
466MHz*	66MHz	7x	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
500MHz*	66MHz	7.5x	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3

Note: Designated pins should be in the closed position.
Note: Clock speed may be set to auto-detect by leaving JFSB open.
*Note: Available for Socket 370 CPU's only.

CPU SPEED SELECTION (PENTIUM II)							
CPU speed	**Clock speed**	**Multiplier**	**JF0**	**JF1**	**JF2**	**JF3**	**JFSB**
233MHz	66MHz	3.5x	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
266MHz	66MHz	4x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
300MHz	66MHz	4.5x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
333MHz	66MHz	5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3
350MHz	100MHz	3.5x	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
400MHz	100MHz	4x	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
450MHZ	100MHz	4.5x	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Designated pins should be in the closed position.
Note: Clock speed may be set to auto-detect by leaving JFSB open.

CPU SPEED SELECTION (PENTIUM III)							
CPU speed	**Clock speed**	**Multiplier**	**JF0**	**JF1**	**JF2**	**JF3**	**JFSB**
450MHZ	100MHz	4.5x	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
500MHz	100MHz	5x	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2

Note: Designated pins should be in the closed position.
Note: Clock speed may be set to auto-detect by leaving JFSB open.

CPU VOLTAGE SELECTION					
Voltage	**JVID0**	**JVID1**	**JVID2**	**JVID3**	**JVID4**
auto-detect	Open	Open	Open	Open	Open
1.8v	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3

1.85v	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
1.9v	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
1.95v	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
2v	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
2.05v	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2.1v	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
2.2v	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
Note: Designated pins should be in the closed position.					