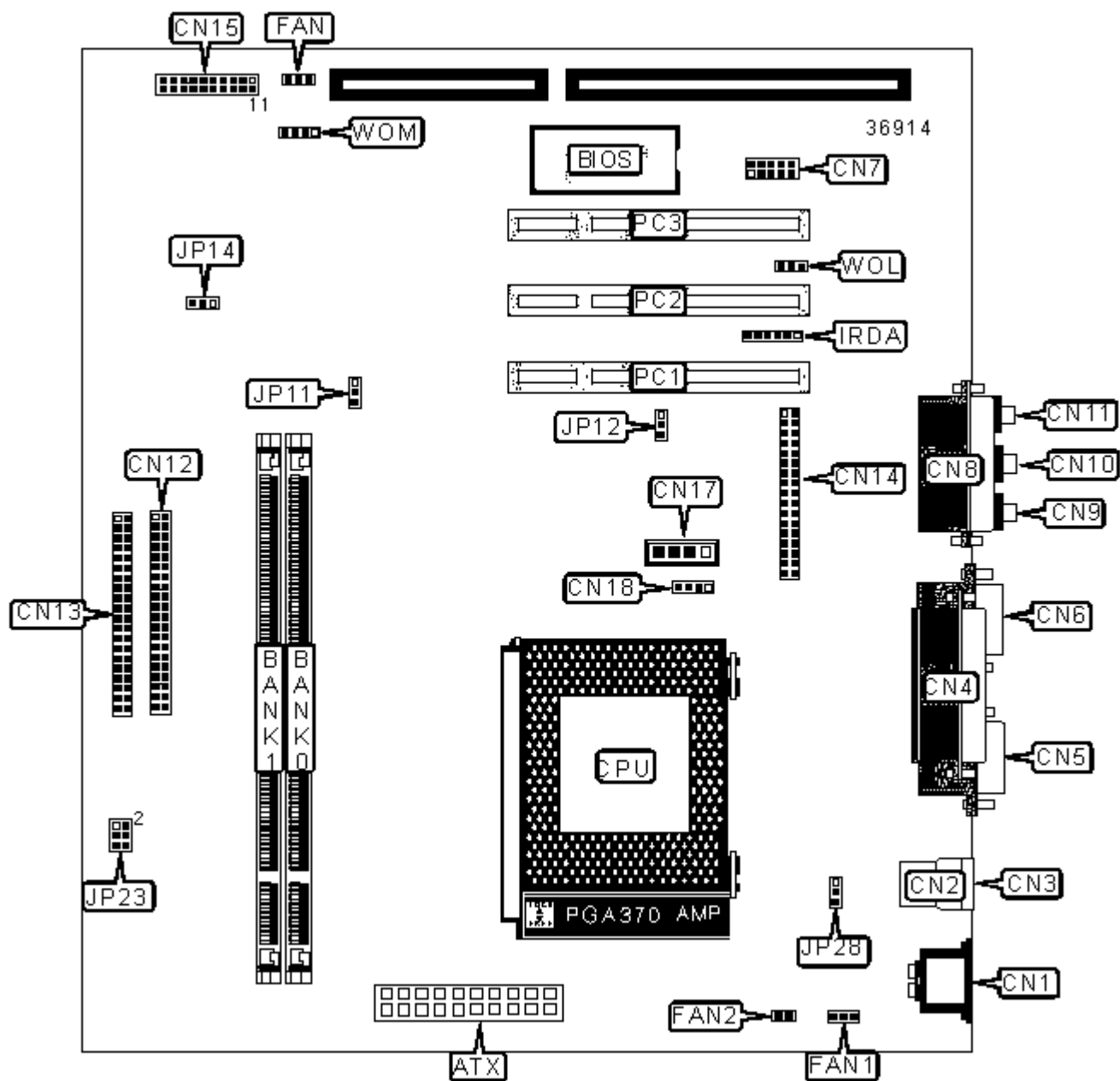


ACER, INC.

MX3ZA

Device Type	Mainboard
Processor	Celeron
Processor Speed	300/333/350/366/400/433MHz
Chip Set	Intel 440ZX
Video Chip Set	ATI
Audio Chip Set	ESS
Maximum Onboard Memory	256MB (SDRAM supported)
Maximum Video Memory	Unidentified
Maximum Audio Memory	Unidentified
Cache	0/128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	244mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, game port, IDE interfaces (2), parallel port, PS/2 mouse port, serial port, serial interface, VGA port, IR connector, USB ports (2), ATX power connector, line in, line out, microphone in, audio in - CD-ROM, Wake-on-LAN connector, Wake-on-Modem connector, Modem/voice connector



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Keylock	CN15/Pins 1 & 2
PS/2 mouse port	CN1	IDE interface LED	CN15/Pins 3-6
USB port 1	CN2	Speaker	CN15/Pins 7-10
USB port 2	CN3	Power switch	CN15/Pins 11 & 12
Parallel port	CN4	ACPI & Power LED	CN15/Pins13-15
Serial port	CN5	Reset switch	CN15/Pins 19 & 20
VGA port	CN6	Audio in - CD-ROM	CN17
Serial interface	CN7	Modem/voice connector	CN18

Game port	CN8	Chassis fan power	FAN
Line out	CN9	CPU fan power 1	FAN1
Line in	CN10	CPU fan power 2	FAN2
Microphone in	CN11	IR connector	IRDA
IDE interface 2	CN12	32-bit PCI slots	PC1 - PC3
IDE interface 1	CN13	Wake-on-LAN connector	WOL
Floppy drive interface	CN14	Wake-on-Modem connector	WOM

USER CONFIGURABLE SETTINGS			
Function		Label	Position
»	On-board Video enabled	JP11	Pins 1 & 2 closed
	On-board Video disabled	JP11	Pins 2 & 3 closed
»	On-board Audio enabled	JP12	Pins 1 & 2 closed
	On-board Audio disabled	JP12	Pins 2 & 3 closed
»	CMOS memory normal operation	JP14	Pins 1 & 2 closed
	CMOS memory clear	JP14	Pins 2 & 3 closed
	Wake on mouse/keyboard disabled	JP28	Pins 1 & 2 closed
	Wake on mouse/keyboard enabled	JP28	Pins 2 & 3 closed

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
16MB	(1) 2M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64

48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board supports SDRAM memory. Note: To reach maximum of 256MB, double sided DIMM's must be used.		

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPU's.

AGP CLOCK SPEED SELECTION

	Setting	JP23
»	Auto	Pins 1 & 2 closed
	AGP speed equal to CPU speed	Pins 3 & 4 closed
	AGP speed 3/2 of CPU speed	Pins 5 & 6 closed