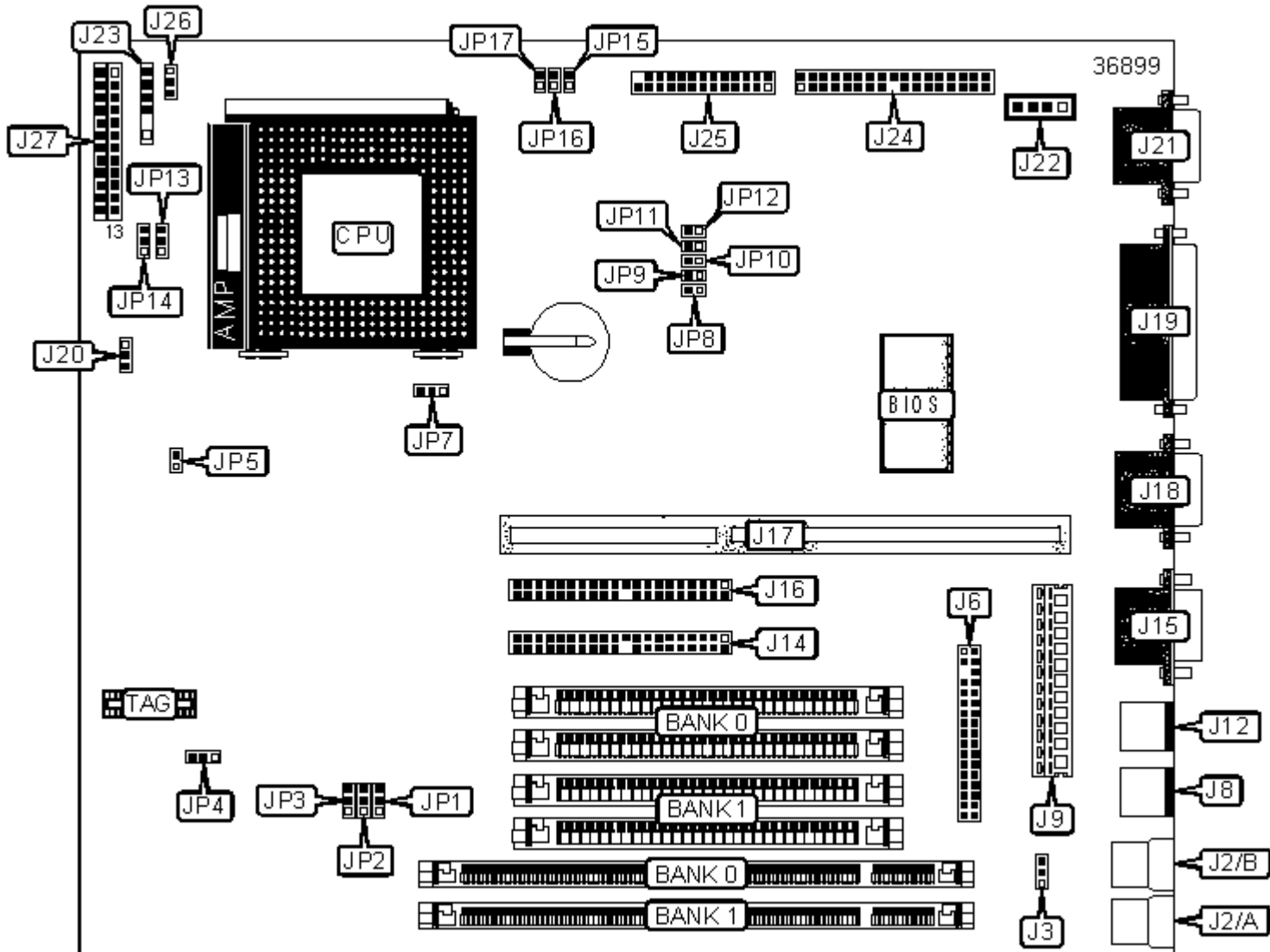


BCM ADVANCED RESEARCH, INC.

SL5598

Device Type	Mainboard
Processor	CX 6x86/CX 6x86L/CX 6x86MX/AM K5/AM K6/Pentium/Pentium MMX/IDT C6
Processor Speed	75/90/100/120/133/150/166/200/233/266/300MHz
Chip Set	SiS
Video Chip Set	SiS
Audio Chip Set	ESS
Maximum Onboard Memory	256MB (EDO & SDRAM supported)
Maximum Video Memory	4MB
Maximum Audio Memory	Unidentified
Cache	0/256/512KB
BIOS	Award
Dimensions	229mm x 280mm
I/O Options	AT power connector, audio in - CD-ROM, EISA riser slot, feature connector, floppy drive interface, green PC connector, IDE interfaces (2), IR connector, MIDI/audio interface, parallel port, PS/2 keyboard port, PS/2 mouse port, serial ports (2), USB ports (2), VGA port, Wake-on-LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
USB port 1	J2/A	VGA port	J21
USB port 2	J2/B	Audio in - CD-ROM	J22
Wake-on-LAN connector	J3	IR connector	J23
Floppy drive interface	J6	MIDI/audio interface	J24
PS/2 keyboard port	J8	Feature connector	J25
AT power connector	J9	CPU fan power	J26
PS/2 mouse port	J12	Power LED & keylock	J27/Pins 1 - 5
IDE interface 1	J14	Speaker	J27/Pins 7 - 10
Serial port 1	J15	Power switch	J27/Pins 13 & 14
IDE interface 2	J16	Reset switch	J27/Pins 15 & 16
EISA riser slot	J17	Green PC switch	J27/Pins 17 & 18
Serial port 2	J18	IDE interface LED	J27/Pins 19 - 22
Parallel port	J19	4 second power switch	J27/Pins 23 & 24
System fan power	J20		

USER CONFIGURABLE SETTINGS

Function		Label	Position
	Onboard video enabled	JP4	Pins 2 & 3 closed
	Onboard video disabled	JP4	Pins 1 & 2 closed
	Linear Burst selected	JP5	Closed
	Interleaved Burst selected	JP5	Open
»	CMOS memory normal operation	JP7	Pins 1 & 2 closed
	CMOS memory clear	JP7	Pins 2 & 3 closed

Note: Use Linear Burst mode for Cyrix CPUs. Use Interleaved Burst mode for other processors.

SIMM CONFIGURATION

Size	Bank 0	Bank 1

8MB	(2) 1M x 32	None
16MB	(2) 1M x 32	(2) 1M x 32
16MB	(2) 2M x 32	None
24MB	(2) 2M x 32	(2) 1M x 32
32MB	(2) 2M x 32	(2) 2M x 32
32MB	(2) 4M x 32	None
40MB	(2) 4M x 32	(2) 1M x 32
48MB	(2) 4M x 32	(2) 2M x 32
64MB	(2) 4M x 32	(2) 4M x 32
64MB	(2) 8M x 32	None
72MB	(2) 8M x 32	(2) 1M x 32
80MB	(2) 8M x 32	(2) 2M x 32
96MB	(2) 8M x 32	(2) 4M x 32
128MB	(2) 8M x 32	(2) 8M x 32
128MB	(2) 16M x 32	None
136MB	(2) 16M x 32	(2) 1M x 32
144MB	(2) 16M x 32	(2) 2M x 32
160MB	(2) 16M x 32	(2) 4M x 32
192MB	(2) 16M x 32	(2) 8M x 32
256MB	(2) 16M x 32	(2) 16M x 32

Note: Board supports EDO SIMM memory.

Note: Single-sided SIMMs: 4MB, 8MB, 16MB; Double-sided SIMMs: 8MB, 32MB, 64MB.

DIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
16MB	(1) 2M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64

32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board supports EDO & SDRAM memory.		

CPU I/O VOLTAGE SELECTION		
CPU Type	JP13	JP14
CX 6x86, AM K5, Pentium	Pins 1 & 2 closed	Pins 1 & 2 closed
CX 6x86L, CX 6x86MX, AM K6, Pentium MMX	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU EXTERNAL CLOCK SELECTION			
Clock Speed	JP1	JP2	JP3
50MHz	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
55MHz	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
60MHz	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
66MHz	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2

Note: Pins designated should be in the closed position.

CPU MULTIPLIER SELECTION

Multiplier	JP15	JP16	JP17
1.5x	Open	Open	Open
2.0x	Closed	Open	Open
2.5x	Closed	Closed	Open
3.0x	Open	Closed	Open
3.5x	Open	Open	Open
4.0x	Closed	Open	Closed

VCORE SELECTION

Setting	JP8	JP9	JP10	JP11	JP12
2.2V	Open	Open	Open	Open	Open
2.8V	Closed	Open	Open	Open	Open
2.9V	Open	Closed	Open	Open	Open
3.2V	Open	Open	Closed	Open	Open
3.38V	Open	Open	Open	Closed	Open
3.52V	Open	Open	Open	Open	Closed