FAMOUS TECHNOLOGY CO., LTD.

MP-6VIP-C (REV. 1.00)

Device Type	Mainboard
Processor	Celeron
Processor Speed	300/333/366/400MHz
Chip Set	VIA
Audio Chip Set	AC97 Codec
Maximum Onboard Memory	768MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128 (located on the Celeron CPU)
BIOS	Award
Dimensions	305mm x 170mm
I/O Options	32-bit PCI slots (4), AGP slot, ATX power connector, audio in - CD-ROMs (2), audio modem riser slot, floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), IR connector, line in, line out, microphone in, parallel port, PS/2 mouse port, serial ports (2), USB ports (2), Wake-on-LAN connector

36882

J1 J2
JP10
CN12
CN13
CN14
JBAT1
CN15
CN16
PC1
PC2
PC3
PC4
SW1
6
AGP
BIOS
JPA1 JP2
AMR
CN11
CN7 CN10
CN9
CN8
ATX
J7
CN6
CN4
CN5
BANK0 BANK1 BANK2
AMP PGA370
CPU
CN2 CN3
CN1
CN17
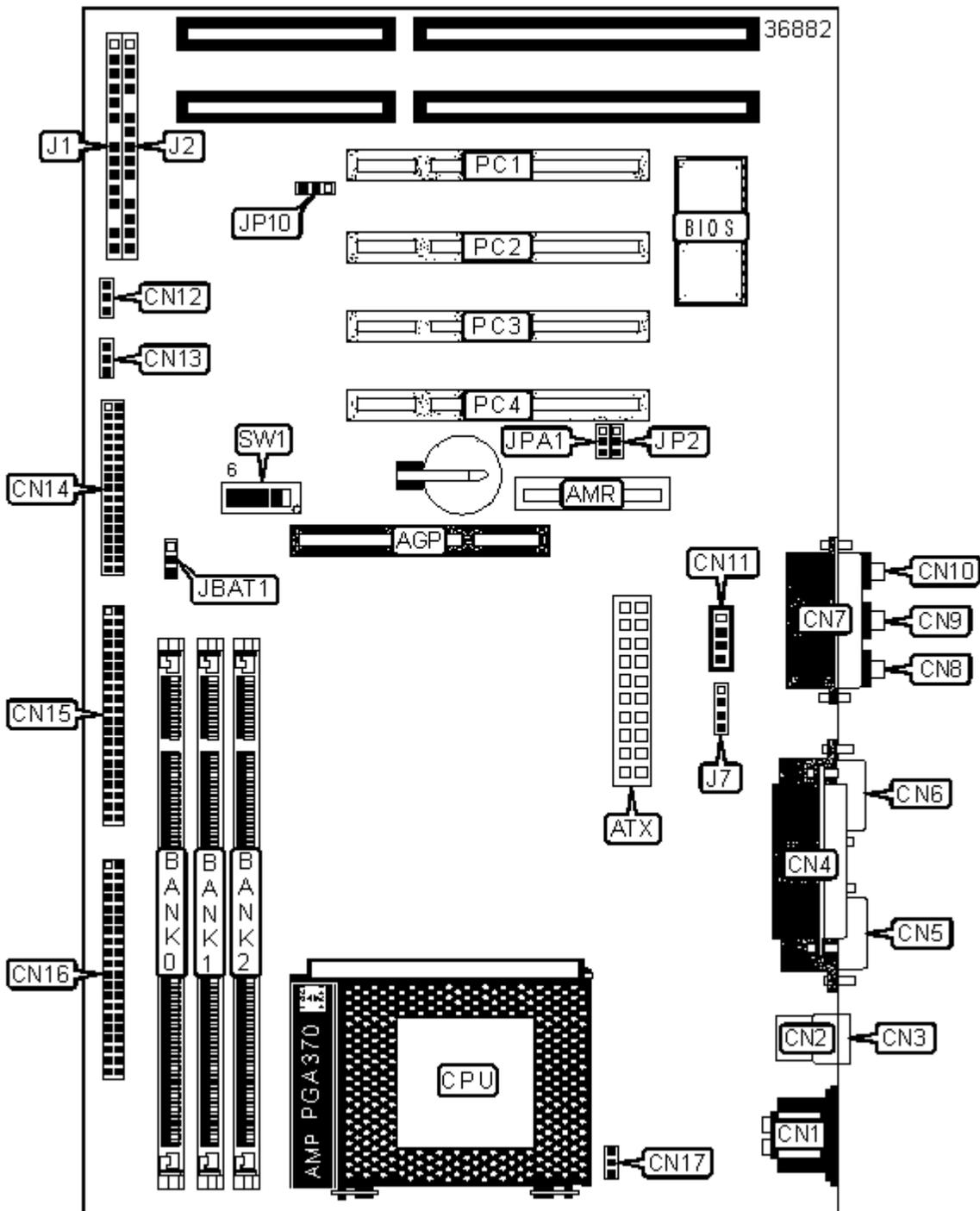
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
AGP slot	AGP	Wake-on-LAN connector	CN13
Audio modem riser slot	AMR	Floppy drive interface	CN14
ATX power connector	ATX	IDE interface 1	CN15
PS/2 mouse port	CN1	IDE interface 2	CN16
USB port 1	CN2	CPU fan power	CN17

USB port 2	CN3	Speaker	J1/Pins 1 - 4
Parallel port	CN4	Reset switch	J1/Pins 5 & 6
Serial port 2	CN5	Power LED & keylock	J1/Pins 8 - 12
Serial port 1	CN6	Green PC LED	J1/Pins 14 & 15
Game/MIDI port	CN7	IDE interface LED	J2/Pins 1 - 4
Line out	CN8	IR connector	J2/Pins 6 - 10
Line in	CN9	Power switch	J2/Pins 12 & 13
Microphone in	CN10	Green PC connector	J2/Pins 14 & 15
Audio in - CD-ROM	CN11	Audio in - CD-ROM	J7
System fan power	CN12	32-bit PCI slots	PC1-PC4

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
» CMOS memory normal operation	JBAT1	Pins 1 & 2 closed
CMOS memory clear	JBAT1	Pins 2 & 3 closed
» Use power switch on the mainboard	JP10	Pins 1 & 2 closed
Use ATX power supply to control on/off switch	JP10	Pins 2 & 3 closed

DIMM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64

64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports SDRAM memory.

CACHE CONFIGURATION

Note: 128KB cache is located on Celeron 300A and greater CPUs.

ONBOARD SOUND SELECTION		
Setting	JP2	JPA1
» Onboard sound enabled	Pins 2 & 3 closed	Pins 2 & 3 closed
Onboard sound disabled	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU SPEED SELECTION								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
300MHz	66MHz	4.5x	On	Off	On	Off	On	Off
333MHz	66MHz	5.0x	Off	Off	On	On	On	Off
366MHz	66MHz	5.5x	Off	Off	Off	On	On	Off
400MHz	66MHz	6.0x	On	On	On	Off	On	Off
Note: To use the auto-detect (default) bus clock setting, SW1/5 is off, and SW1/6 is on.								