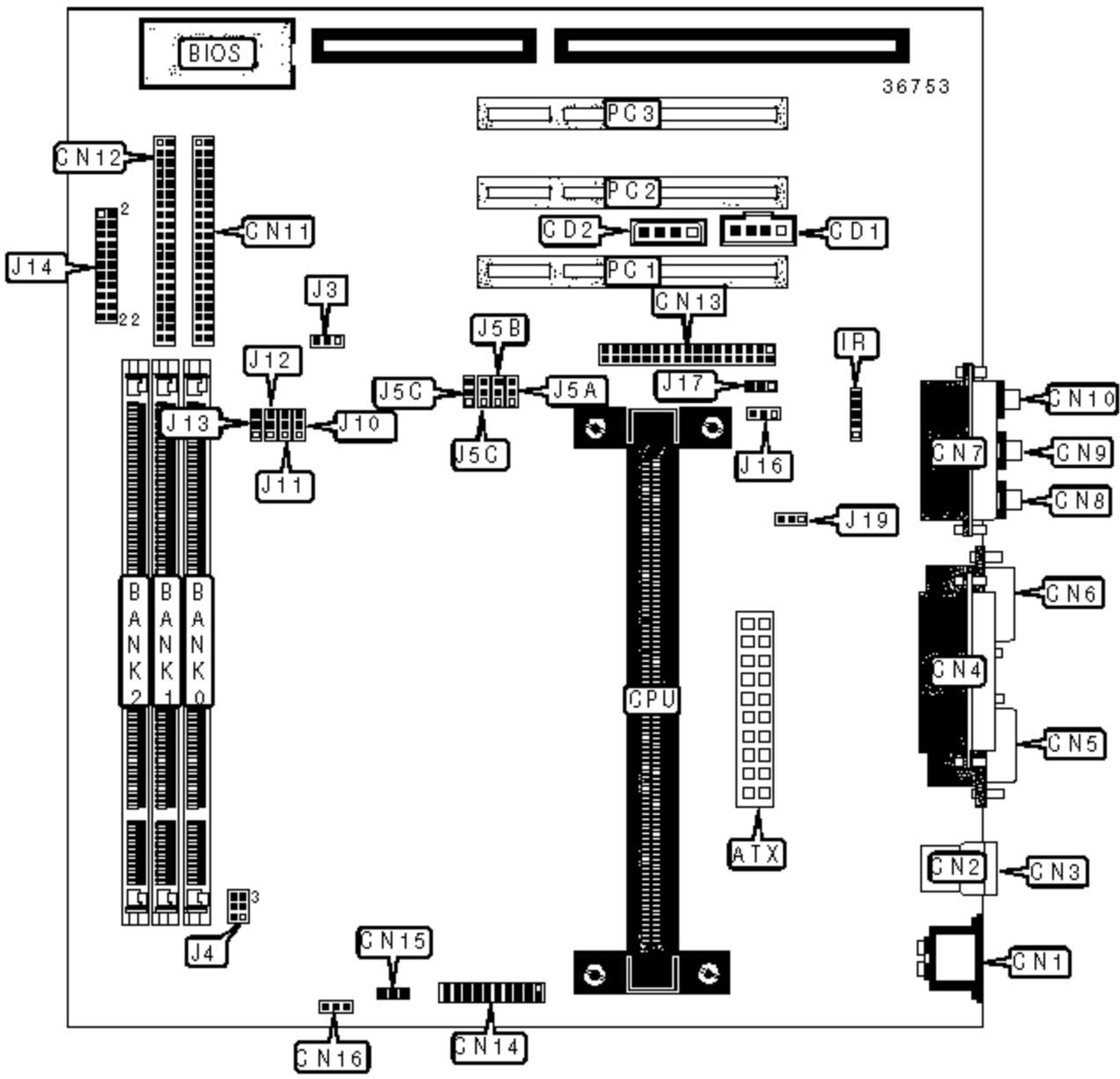


EURONE

EM-7130S

Device Type	Mainboard
Processor	Celeron/Pentium II
Processor Speed	233/266/300/333/350/400/450/500MHz
Chip Set	SIS
Video Chip Set	Unidentified
Audio Chip Set	Unidentified
Maximum Onboard Memory	768MB (EDO, SDRAM, & DDR supported)
Maximum Video Memory	8MB
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	AMI
Dimensions	Unidentified
I/O Options	32-bit PCI slots (3), floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VGA feature connector, IR connector, USB connectors (2), ATX power connector, line in, line out, microphone in, audio in - CD-ROMs (2), Wake-on-LAN connector, Digital audio in, Digital audio out



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Floppy drive interface	CN13
Audio in - CD-ROM (Sony)	CD1	VGA feature connector	CN14
Audio in - CD-ROM (Panasonic)	CD2	Chassis fan power	CN15
PS/2 mouse port	CN1	CPU fan power	CN16
USB port 1	CN2	IR connector	IR
USB port 2	CN3	Power LED & keylock	J14/Pins 2, 4, 6, 8 & 10
Parallel port	CN4	Speaker	J14/Pins 1, 3, 5 & 7
Serial port 1	CN5	IDE interface LED	J14/Pins 15 & 16
Serial port 2	CN6	Reset switch	J14/Pins 17 & 18
Game/MIDI port	CN7	Green PC connector	J14/Pins 21 & 22
Line in	CN8	Digital audio in	J16
Microphone in	CN9	Digital audio out	J17
Line out	CN10	Wake-on-LAN connector	J19
IDE interface 1	CN11	32-bit PCI slots	PC1-PC3
IDE interface 2	CN12		

USER CONFIGURABLE SETTINGS

Function	Label	Position
CMOS memory normal operation	J3	Pins 1 & 2 closed
CMOS memory clear	J3	Pins 2 & 3 closed
» DIMM voltage 3.3V	J4	Pins 2 & 3, 5 & 6 closed
DIMM voltage 5V	J4	Pins 1 & 2, 4 & 5 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2

8MB	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None
40MB	(1) 2M x 64	(1) 2M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None
72MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64
80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
264MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64
272MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
520MB	(1) 32M x 64	(1) 32M x 64	(1) 1M x 64
528MB	(1) 32M x 64	(1) 32M x 64	(1) 2M x 64

544MB	(1) 32M x 64	(1) 32M x 64	(1) 4M x 64
576MB	(1) 32M x 64	(1) 32M x 64	(1) 8M x 64
640MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports EDO, SDRAM, & DDR memory.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.

CPU MULTIPLIER SELECTION

Multiplier	J5A	J5B	J5C	J5D
2x	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
2.5x	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
3x	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
3.5x	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
» 4x	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
4.5x	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
5x	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
5.5x	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed

CPU FREQUENCY SELECTION

Frequency	J10	J11	J12	J13
» 66MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
100MHz (SDRAM 66MHz)	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
100MHz (SDRAM 100MHz)	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed