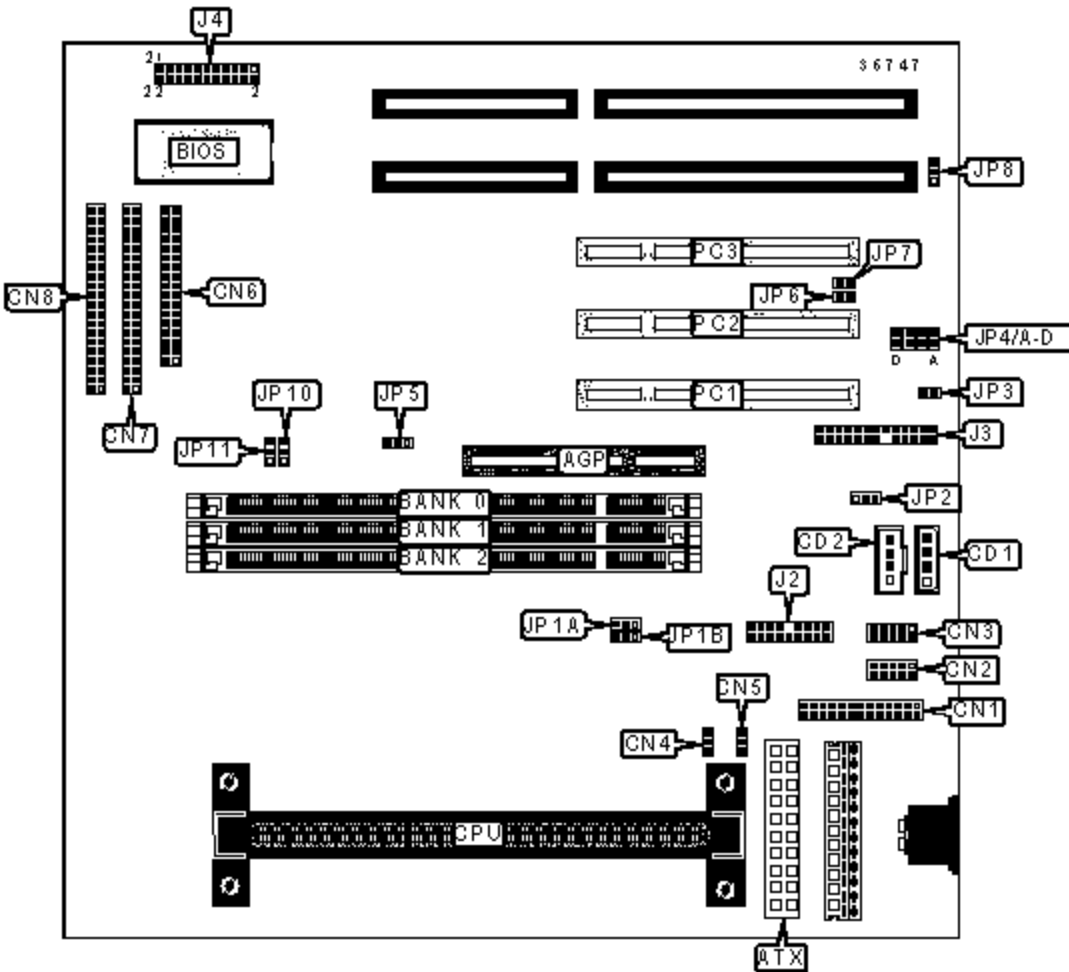


EURONE

EM-7226S

Device Type	Mainboard
Processor	Celeron/Pentium II
Processor Speed	233/266/300/333/350/400/450MHz
Chip Set	BXcel
Audio Chip Set	ALI
Maximum Onboard Memory	768MB (EDO, FPM, & SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, sound/game interface, green PC connector, IDE interfaces (2), parallel interface, serial interfaces (2), ATX power connector, AGP slot, microphone in interface, audio in - CD-ROMs (2), Digital audio in, Digital audio out, ATX form card connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	ATX form card connector	J2
ATX power connector	ATX	Sound & game interface	J3
Audio in - CD-ROM (Panasonic)	CD1	Power LED & keylock	J4/Pins 2, 4, 6, 8 & 10

Audio in - CD-ROM (Sony)	CD2	Speaker	J4/Pins 1, 3, 5 & 7
Parallel interface	CN1	Turbo LED	J4/Pins 13 & 14
Serial interface 2	CN2	IDE interface LED	J4/Pins 15 & 16
Serial interface 1	CN3	Reset switch	J4/Pins 17 & 18
Chassis fan power	CN4	Green PC LED	J4/Pins 19 & 20
CPU fan power	CN5	Green PC connector	J4/Pins 21 & 22
Floppy drive interface	CN6	Digital audio in	JP6
IDE interface 1	CN7	Digital audio out	JP7
IDE interface 2	CN8	32-bit PCI slots	PC1-PC3

USER CONFIGURABLE SETTINGS

Function		Label	Position
	CMOS memory normal operation	JP2	Pins 1 & 2 Closed
	CMOS memory clear	JP2	Pins2 & 3 Closed
»	Microphone type standard mode	JP3	Open
	Microphone type special mode	JP3	Closed
»	On-board sound enabled	JP8	Pins 2 & 3 Closed
	On-board sound disabled	JP8	Pins 1 & 2 Closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None

40MB	(1) 2M x 64	(1) 2M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None
72MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64
80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
264MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64
272MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
520MB	(1) 32M x 64	(1) 32M x 64	(1) 1M x 64
528MB	(1) 32M x 64	(1) 32M x 64	(1) 2M x 64
544MB	(1) 32M x 64	(1) 32M x 64	(1) 4M x 64
576MB	(1) 32M x 64	(1) 32M x 64	(1) 8M x 64
640MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports EDO, FPM, & SDRAM memory.
Note: EDO & FPM memory is not recommended with 100MHz CPU frequency.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.

CPU FREQUENCY SELECTION

Frequency	JP5	JP10	JP11
66MHz	Pins 2 & 3 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed
100MHz	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed
112MHz	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
133MHz	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed

Note: 112MHz and 133MHz settings are for internal testing only.

CPU MULTIPLIER SELECTION

Multiplier	JP4A	JP4B	JP4C	JP4D
2x	Closed	Closed	Closed	Closed
2.5x	Open	Closed	Closed	Closed
3x	Closed	Closed	Open	Closed
3.5x	Open	Closed	Open	Closed
4x	Closed	Closed	Closed	Open
4.5x	Open	Closed	Closed	Open
5x	Closed	Closed	Open	Open
5.5x	Open	Closed	Open	Open
6x	Closed	Open	Closed	Closed
6.5x	Open	Open	Closed	Closed
7x	Closed	Open	Open	Closed
7.5x	Open	Open	Open	Closed
8x	Closed	Open	Closed	Open

DIMM VOLTAGE SELECTION

Voltage	JP1A	JP1B
3.3V	Pins 1 & 2 Closed	Pins 1 & 2 Closed
5V	Pins 2 & 3 Closed	Pins 2 & 3 Closed