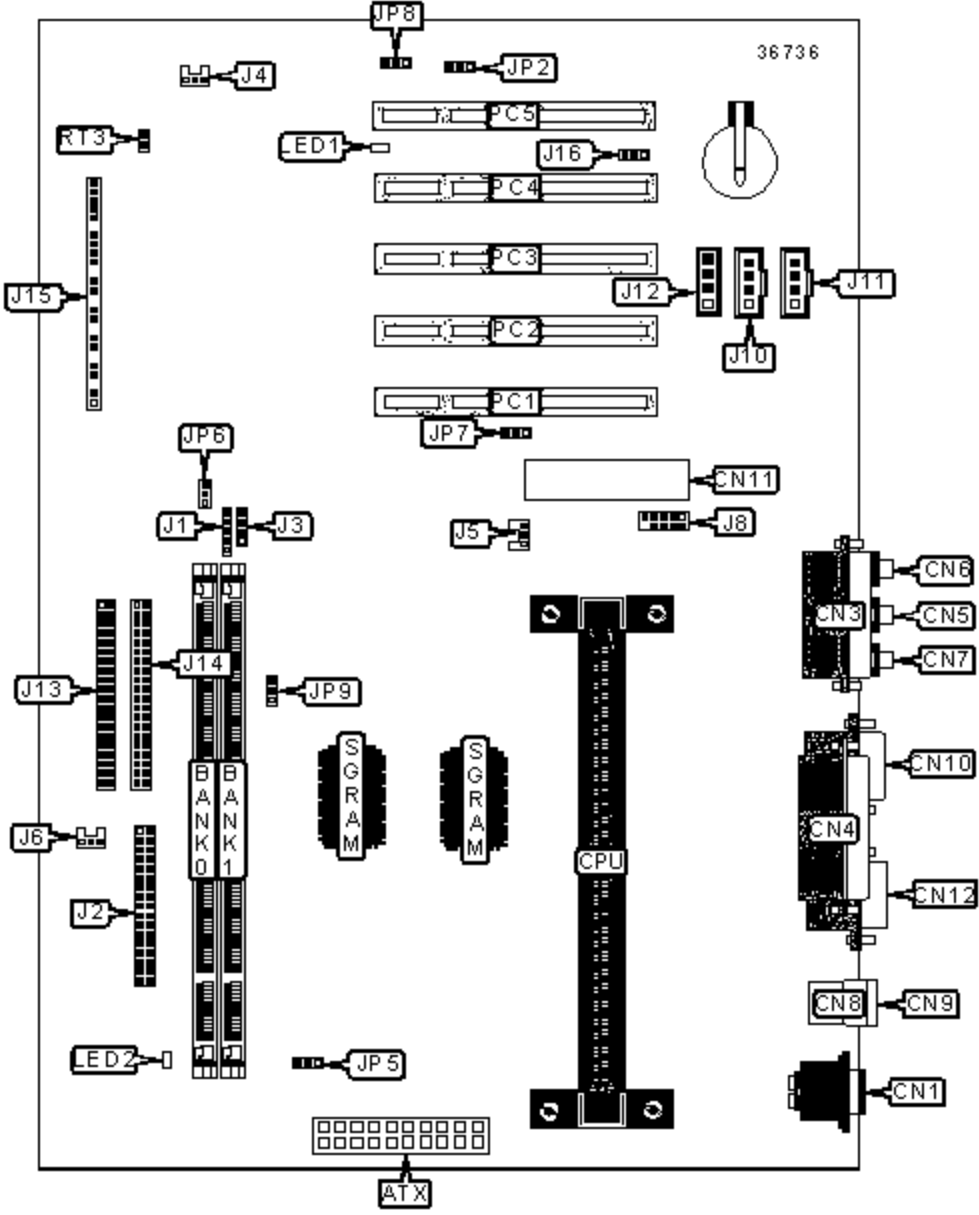


DIAMOND FLOWER, INC.

PW65-E (REV. A+)

Device Type	Mainboard
Processor	Celeron/Pentium II/Pentium III
Processor Speed	233/266/300/333/350/366/400/433/450/500/533/550MHz
Chip Set	Intel 810-E
Video Chip Set	Unidentified
Audio Chip Set	Unidentified
Maximum Onboard Memory	512MB (SDRAM supported)
Maximum Video Memory	4MB
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	Award
Dimensions	304mm x 170mm
I/O Options	32-bit PCI slots (5), floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial port, serial interface, VGA port, riser slot, IR connector, USB ports (2), ATX power connector, line in, line out, microphone in, audio in - CD-ROMs (3), Wake on LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Power fan	J6
PS/2 mouse port	CN1	Serial interface	J8
Game/MIDI port	CN3	Audio in - CD-ROM (Sony)	J10
Parallel port	CN4	Audio in - CD-ROM (Mitsumi)	J11
Line in	CN5	Audio in - CD-ROM (Sony)	J12
Microphone in	CN6	IDE interface 1	J13
Line out	CN7	IDE interface 2	J14
USB connector 1	CN8	IDE interface LED	J15/Pins 1 & 2
USB connector 2	CN9	Green PC LED	J15/Pins 4 & 5
VGA port	CN10	Power switch	J15/Pins 7 & 8
AMR riser slot	CN11	Green PC connector	J15/Pins 10 & 11
Serial port	CN12	Reset switch	J15/Pins 13 & 14
IR connector	J1	Speaker	J15/Pins 16-19
Floppy drive interface	J2	Power LED & keylock	J15/Pins 21-25
Chassis open connector	J3	Wake on LAN connector	J16
Chassis fan power	J4	32-bit PCI slots	PC1 - PC5
CPU fan power	J5	Thermal connector	RT3

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	JP2	Pins 1 & 2 closed
	CMOS memory clear	JP2	Pins 2 & 3 closed
»	Keyboard/mouse power on disabled	JP5	Pins 1 & 2 closed
	Keyboard/mouse power on enabled	JP5	Pins 2 & 3 closed
»	Factory configured - do not alter	JP6	Pins 1 & 2 closed
»	Audio CODEC enabled	JP7	Pins 2 & 3 closed

	Audio CODEC disabled	JP7	Pins 1 & 2 closed
»	PC speaker enabled for beep message	JP8	Pins 2 & 3 closed
	External speaker enabled for beep message	JP8	Pins 1 & 2 closed

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
16MB	(1) 2M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
256MB	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 32M x 64
Note: Board supports SDRAM memory.		

CACHE CONFIGURATION
Note: 256KB/512KB cache is located on the Pentium II/Pentium III CPU. 128KB cache is located on the Celeron 300A and greater CPU.

BUS SPEED SELECTION

Setting		JP9
»	Auto-detect	Pins 2 & 3 closed
	66MHz	Pins 1 & 2 closed
	100MHz	Open

DIAGNOSTIC LED(S)

LED	Color	Status	Condition
LED1	Unidentified	On	System is in power-on, soft-off, or suspend mode
LED1	Unidentified	Off	System is off
LED2	Unidentified	On	System is in power-on, or suspend mode
LED2	Unidentified	Off	System is in Soft-off state or Off