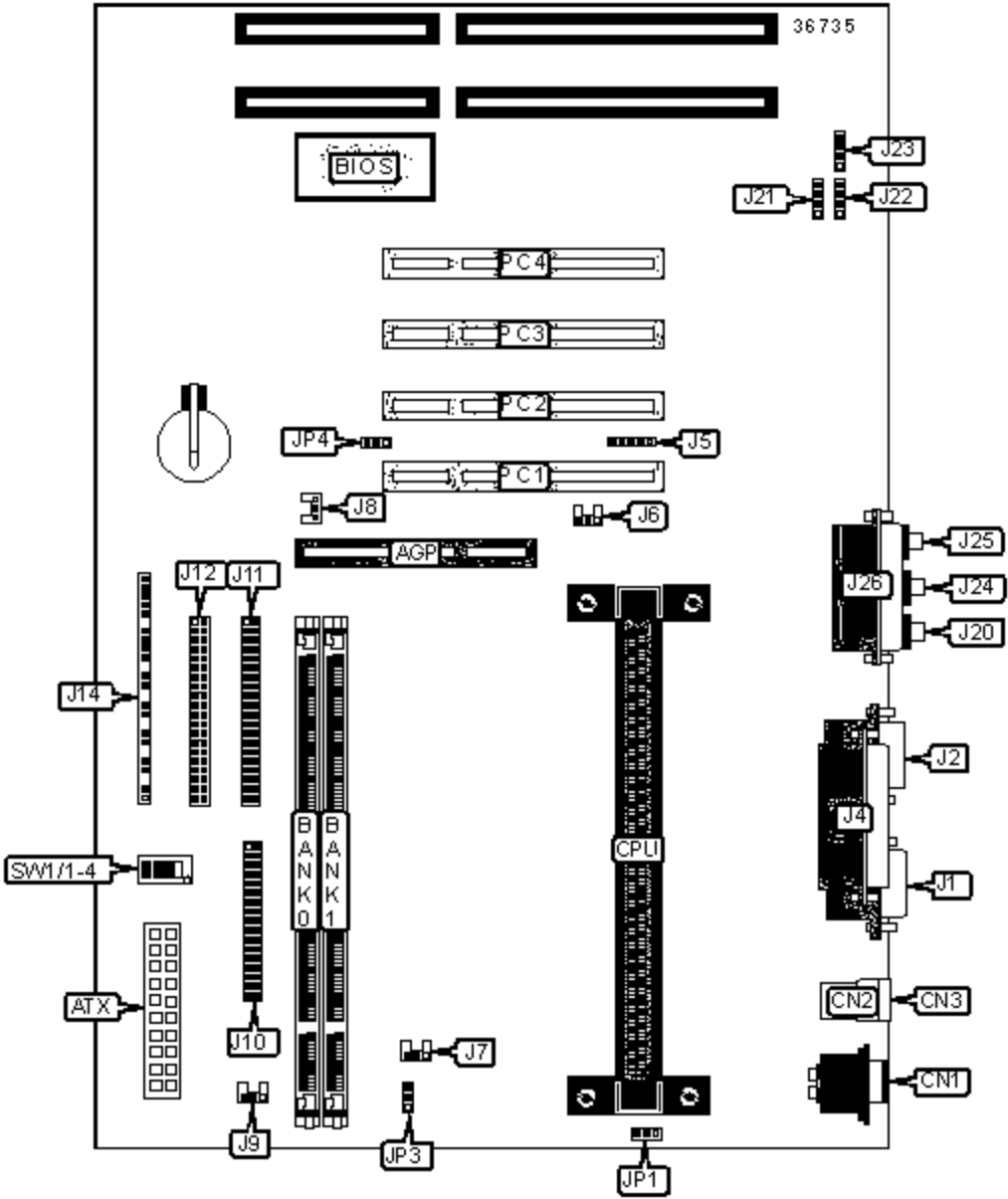


# DIAMOND FLOWER, INC.

## PB64-ZX (REV. A+)

Device Type	Mainboard
Processor	Celeron/Pentium II/Pentium III
Processor Speed	233/266/300/333/350/366/400/433/450/500MHz
Chip Set	Intel 440ZX
Audio Chip Set	ESS
Maximum Onboard Memory	256MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	Award
Dimensions	304mm x 170mm
I/O Options	32-bit PCI slots (4), floppy drive interface, game/MIDI port, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, AGP slot, line in, line out, microphone in, audio in - CD-ROMs (3), Wake-on-LAN connector



### CONNECTIONS

Location	Location
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Purpose		Purpose	
AGP slot	AGP	IDE interface LED	J14/Pins 1 & 2
ATX power connector	ATX	Green PC LED	J14/Pins 4 & 5
PS/2 mouse port	CN1	Power switch	J14/Pins 7 & 8
USB port 1	CN2	Green PC connector	J14/Pins 10 & 11
USB port 2	CN3	Reset switch	J14/Pins 13 & 14
Serial port 1	J1	Speaker	J14/Pins 16-19
Serial port 2	J2	Power LED & keylock	J14/Pins 21-25
Parallel port	J4	Line out	J20
IR connector	J5	Audio in - CD-ROM (Sony)	J21
AGP fan power	J6	Audio in - CD-ROM (Sony)	J22
CPU fan power	J7	Audio in - CD-ROM (Mitsumi)	J23
Wake-on-LAN connector	J8	Line in	J24
Chassis fan power	J9	Microphone in	J25
Floppy drive interface	J10	Game/MIDI port	J26
IDE interface 1	J11	32-bit PCI slots	PC1 - PC4
IDE interface 2	J12		

### USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Keyboard/mouse power on disabled	JP1	Pins 1 & 2 closed
	Keyboard/mouse power on enabled	JP1	Pins 2 & 3 closed
»	CMOS memory normal operation	JP4	Pins 1 & 2 closed
	CMOS memory clear	JP4	Pins 2 & 3 closed

### DIMM CONFIGURATION

Size	Bank 0	Bank 1
16MB	(1) 2M x 64	None

32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board supports SDRAM memory.		

### CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II/Pentium III CPU. 128KB cache is located on the Celeron 300A and greater CPUs.

### CPU SPEED SELECTION (CELERON)

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
266MHz	66MHz	4x	On	On	Off	On
300MHz	66MHz	4.5x	Off	On	Off	On
333MHz	66MHz	5x	On	Off	Off	On
366MHz	66MHz	5.5x	Off	Off	Off	On
400MHz	66MHz	6x	On	On	On	Off
433MHz	66MHz	6.5x	Off	On	On	Off

**CPU SPEED SELECTION (PENTIUM II)**

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
233MHz	66MHz	3.5x	Off	Off	On	On
266MHz	66MHz	4x	On	On	Off	On
300MHz	66MHz	4.5x	Off	On	Off	On
333MHz	66MHz	5x	On	Off	Off	On
350MHz	100MHz	3.5x	Off	Off	On	On
400MHz	100MHz	4x	On	On	Off	On
450MHz	100MHz	4.5x	Off	On	Off	On

**CPU SPEED SELECTION (PENTIUM III)**

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
450MHz	100MHz	4.5x	Off	On	Off	On
500MHz	100MHz	5x	On	Off	Off	On

**BUS SPEED SELECTION**

Setting		JP3
»	Auto-detect	Pins 1 & 2 closed
	66MHz	Pins 2 & 3 closed
	100MHz	Open