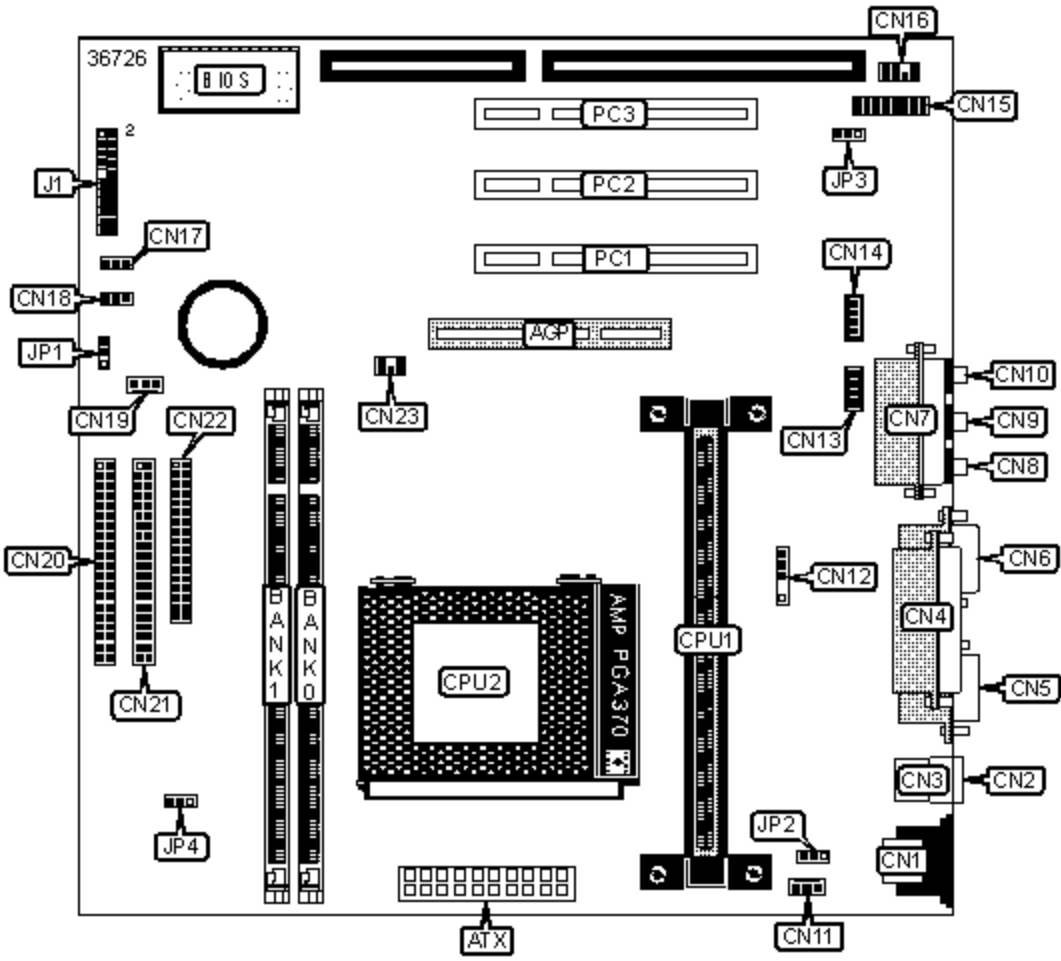


ELITEGROUP COMPUTER SYSTEMS, INC.

P6ZXT-ME

Device Type	Mainboard
Processor	Celeron/Pentium II/Pentium III
Processor Speed	233/266/300/333/350/366/400/433/450/466/500/550MHz
Chip Set	Intel 440ZX
Audio Chip Set	Elite
Maximum Onboard Memory	256MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	Award
Dimensions	245mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, Wake-on LAN connector, Wake-on modem connector, auxiliary/audio in - CD-ROMs (2), game/MIDI port, line-out, line-in, microphone-in, SPDIF connector, SB-Link connector, fax/modem adapter card connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	SPDIF in/out connector	CN16
ATX power connector	ATX	Wake-on-modem connector	CN17
PS/2 mouse port	CN1	Wake-on-LAN connector	CN18

USB port 1	CN2	Case fan power connector	CN19
USB port 2	CN3	IDE interface 1	CN20
Parallel port	CN4	IDE interface 2	CN21
Serial port 1	CN5	Floppy drive interface	CN22
Serial port 2	CN6	SB-Link connector	CN23
Game/MIDI port	CN7	Speaker	J1/Pins 1, 3, 5 & 7
Line out	CN8	Power LED	J1/Pins 2, 4 & 6
Line in	CN9	Keylock connector	J1/Pins 8 & 10
Microphone in	CN10	Green PC LED connector	J1/Pins 13 & 14
CPU fan power	CN11	IDE interface LED	J1/Pins 15 & 16
IR connector	CN12	Reset switch	J1/Pins 17 & 18
Audio in - CD-ROM	CN13	SMI connector	J1/Pins 19 & 20
Auxiliary audio in connector	CN14	Power switch	J1/Pins 21 & 22
Fax/modem adapter card connector	CN15	32-bit PCI slots	PC1 - PC3

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	JP1	Pins 2 & 3 closed
	CMOS memory clear	JP1	Pins 1 & 2 closed
	Keyboard power on enabled	JP2	Pins 1 & 2 closed
	Keyboard power on disabled	JP2	Pins 2 & 3 closed
	Onboard audio enabled	JP3	Pins 1 & 2 closed
	Onboard audio disabled	JP3	Pins 2 & 3 closed
	Bus frequency-automatic selection	JP4	Pins 1 & 2 closed
	Bus frequency forced to 100 MHz	JP4	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1
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8MB	(1) 1M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
16MB	(1) 2M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64

Note: Board supports SDRAM memory. If installed processor runs over a 100MHz system bus, then PC-100 modules are required

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II & Pentium III CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.