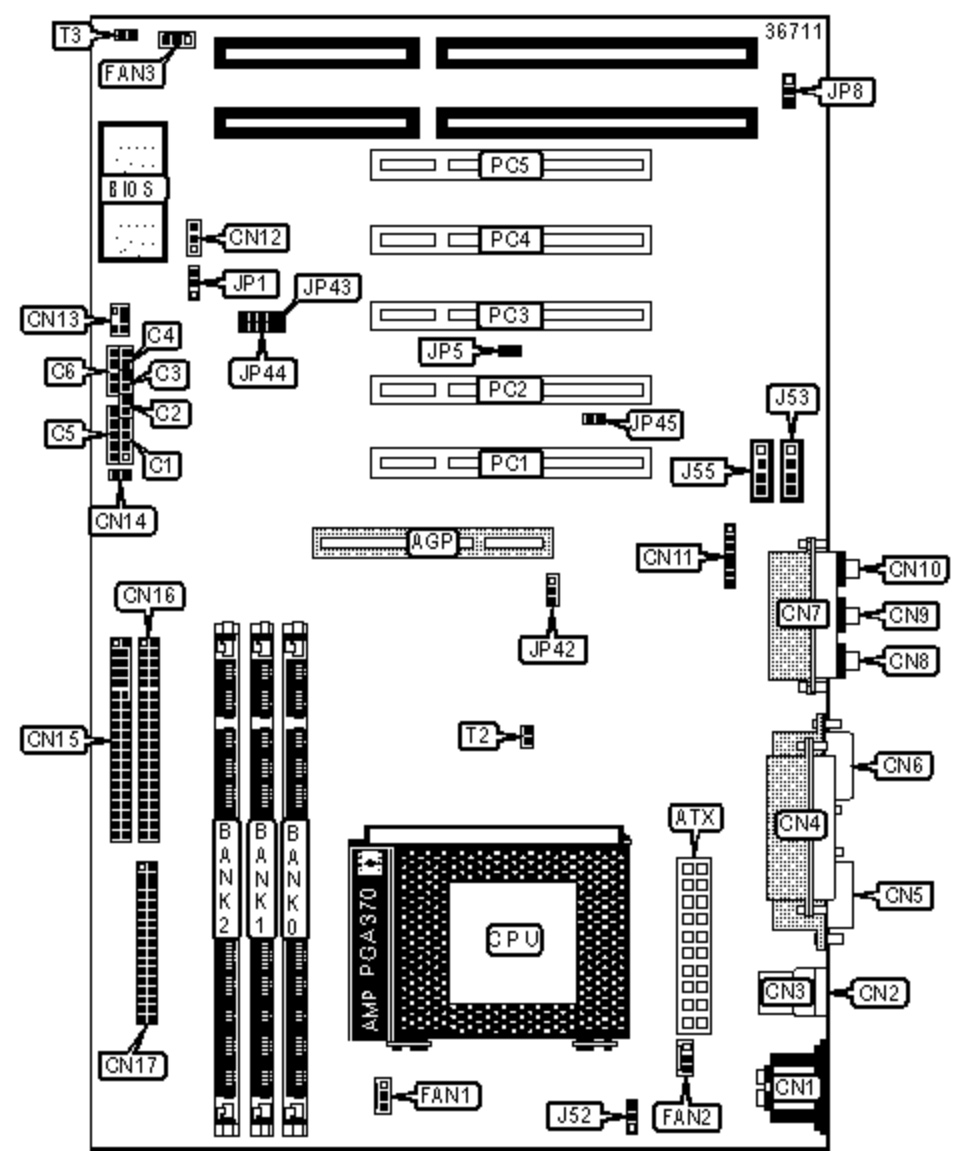


IWILL CORPORATION

LE370

Device Type	Mainboard
Processor	Celeron
Processor Speed	233/266/300/333/366/400/433/466/500/533MHz
Chip Set	Intel 440LX
Audio Chip Set	Aurel
Maximum Onboard Memory	768MB (EDO & SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	189mm x 305mm
I/O Options	32-bit PCI slots (5), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, AGP slot, Wake-on LAN connector, SB-Link connector, audio in - CD-ROM, AUX audio-in, line out, line in, microphone in, game/MIDI port, SMBus connector, Wake-on modem connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	IR connector	CN11

ATX power connector	ATX	System fan power	FAN3
ACPI LED	C1	Wake-on LAN connector	CN12
IDE interface LED	C2	SB-Link connector	CN13
Unidentified	C3	Power switch	CN14
Reset switch	C4	IDE interface 1	CN15
Power LED & keylock	C5	IDE interface 2	CN16
Speaker	C6	Floppy drive interface	CN17
PS/2 mouse port	CN1	CPU fan power	FAN1
USB port 1	CN2	System fan power	FAN2
USB port 2	CN3	Auxiliary in	J53
Parallel port	CN4	Audio in - CD-ROM	J55
Serial port 1	CN5	SMBus connector	JP43
Serial port 2	CN6	Wake-on modem connector	JP44
Game/MIDI port	CN7	Chassis intrusion connector	JP45
Line out	CN8	32-bit PCI slots	PC1 - PC5
Line in	CN9	Temperature sensor connector	T2
Microphone in	CN10	Temperature sensor connector	T3

USER CONFIGURABLE SETTINGS

Function		Label	Position
	Keyboard power-on disabled	J52	Unidentified
	Keyboard power-on enabled	J52	Unidentified
»	CMOS memory normal operation	JP1	Pins 1 & 2 closed
	CMOS memory clear	JP1	Pins 2 & 3 closed
	Factory configured - do not alter	JP5	Unidentified
	On board audio enabled	JP8	Pins 1 & 2 closed
	On board audio disabled	JP8	Pins 2 & 3 closed

	Keyboard power-on password enabled	JP42	Pins 1 & 2 closed
	Clear keyboard power-on password	JP42	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None

256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports EDO & SDRAM memory. Maximum EDO is 384MB. Maximum SDRAM is 768 MB.

Note: EDO & SDRAM can not be mixed.

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.