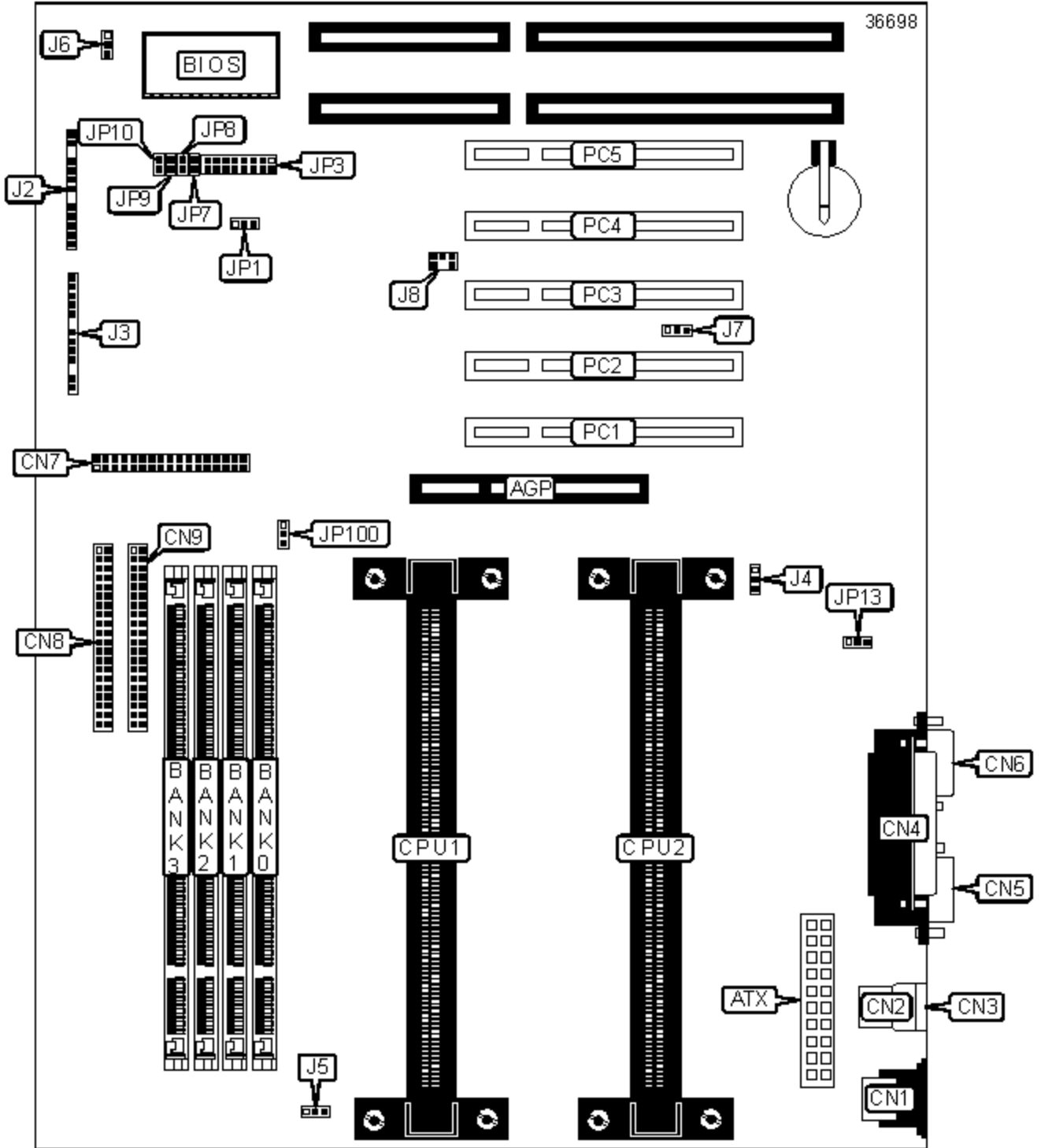


EPOX COMPUTER CO., LTD.

KP6-BS (SMP)

Device Type	Mainboard
Processor	Pentium II (2)
Processor Speed	233/266/300/333/350/400/450MHz
Chip Set	Intel 440BX
Maximum Onboard Memory	1GB (EDO & SDRAM supported)
Cache	256/512KB (located on the Pentium II CPU)
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (5), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, AGP slot, SB-link connector, Wake-on-LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	PC speaker	J2/Pins 4 - 7
ATX power connector	ATX	Power LED & keylock	J2/Pins 9 - 13
PS/2 mouse port	CN1	IR connector	J3/Pins 1 - 5
USB port 2	CN2	IDE interface LED	J3/Pins 7 & 8
USB port 1	CN3	Turbo LED	J3/Pins 9 & 10
Parallel port	CN4	Power switch	J3/Pins 12 & 13
Serial port 1	CN5	CPU 1 fan power	J4
Serial port 2	CN6	CPU 2 fan power	J5
Floppy drive interface	CN7	Chassis fan power	J6
IDE interface 1	CN8	Wake-on-LAN connector	J7
IDE interface 2	CN9	SB-link connector	J8
Reset switch	J2/Pins 1 & 2	32-bit PCI slots	PC1 - PC5

USER CONFIGURABLE SETTINGS

	Function	Label	Position
»	CMOS memory normal operation	JP1	Pins 1 & 2 closed
	CMOS memory clear	JP1	Pins 2 & 3 closed
»	Factory configured - do not alter	JP3/Pins 13 & 14	Unidentified
»	ESSJ (JP3) selection of CPU speed enabled	JP3/Pins 15 & 16	Open
	ESSJ (JP3) selection of CPU speed disabled	JP3/Pins 15 & 16	Closed
»	Factory configured - do not alter	JP7	Unidentified
»	Factory configured - do not alter	JP8	Unidentified
»	Factory configured - do not alter	JP9	Unidentified
»	Factory configured - do not alter	JP10	Unidentified
»	Power-on keyboard disabled	JP13	Pins 2 & 3 closed
	Power-on keyboard enabled	JP13	Pins 1 & 2 closed

»	SDRAM clock speed set automatically	JP100	Pins 1 & 2 closed
	SDRAM clock speed set to 100MHz	JP100	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	(1) 8M x 64	None	None	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 16M x 64	None	None	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None	None

256MB	(1) 32M x 64	None	None	None
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None	None
528MB	(1) 32M x 64	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
544MB	(1) 32M x 64	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
576MB	(1) 32M x 64	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
640MB	(1) 32M x 64	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
768MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
1024MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports EDO & SDRAM memory. If using the 100MHz FSB, 100MHz SDRAM should be used.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPUs.

CPU SPEED SELECTION

CPU speed	Clock speed	JP3
200MHz	66MHz	Pins 1 & 2 closed
233Mhz	66MHz	Pins 3 & 4 closed
» 266MHz	66MHz	Pins 5 & 6 closed
300MHz	66MHz	Pins 7 & 8 closed
300MHz	100MHz	Pins 1 & 2 closed

	333MHz	66MHz	Pins 9 & 10 closed
	350MHz	100MHz	Pins 3 & 4 closed
	366MHz	66MHz	Pins 11 & 12 closed
	400MHz	100MHz	Pins 5 & 6 closed
	450MHz	100MHz	Pins 7 & 8 closed
	500MHz	100MHz	Pins 9 & 10 closed
	550MHz	100MHz	Pins 11 & 12 closed
Automatic CPU speed selection			Pins 13 & 14 closed

Note: Designated pins should be in the closed position