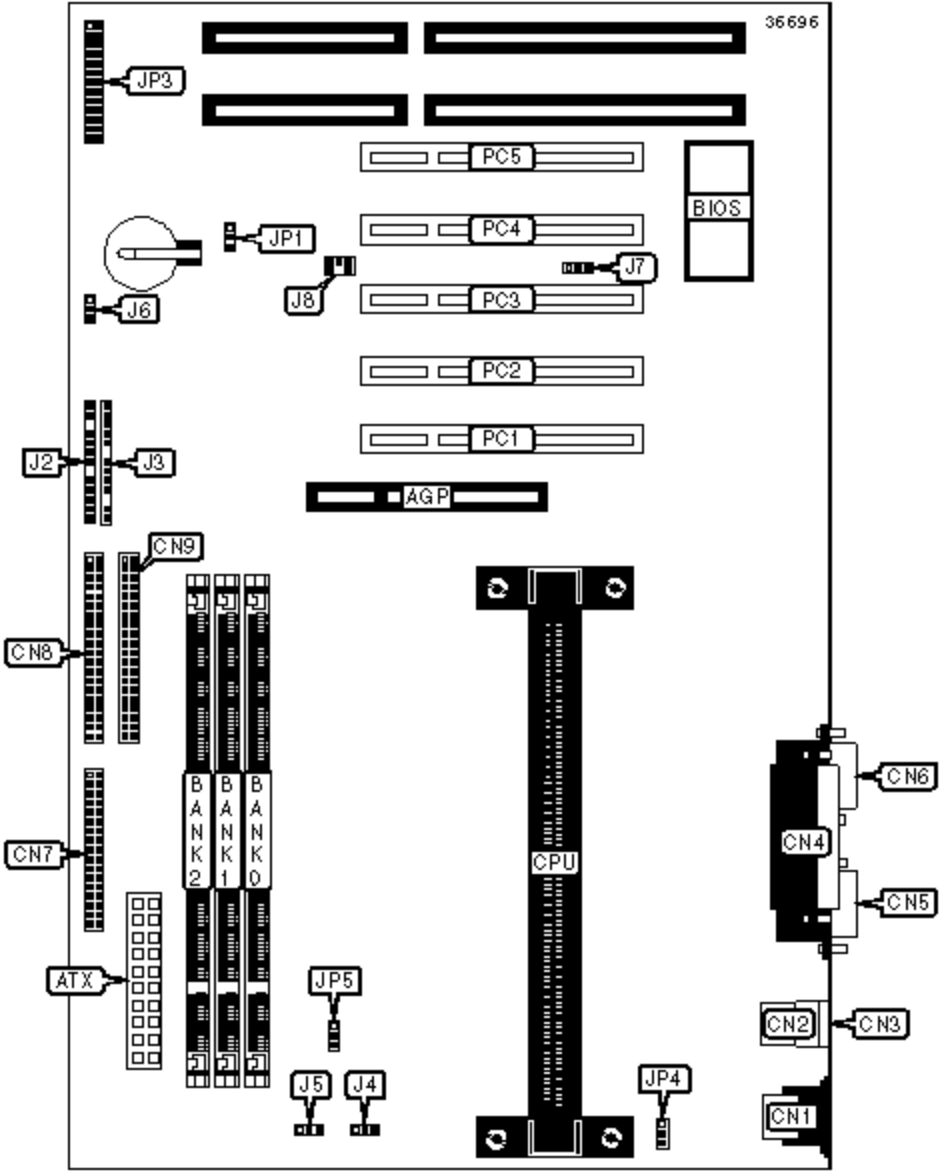


# EPOX COMPUTER CO., LTD.

## P2-112A

<b>Device Type</b>	Mainboard
<b>Processor</b>	Pentium II
<b>Processor Speed</b>	200/233/266/300/333/400/450/500MHz
<b>Chip Set</b>	VIA
<b>Maximum Onboard Memory</b>	384MB (EDO & SDRAM supported)
<b>Cache</b>	256/512KB (located on the Pentium II CPU)
<b>BIOS</b>	Award
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit PCI slots (5), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, AGP slot, SB-link connector, Wake-on-LAN connector



### CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	PC speaker	J2/Pins 4 - 7
ATX power connector	ATX	Power LED & keylock	J2/Pins 9 - 13

PS/2 mouse port	CN1	IR connector	J3/Pins 1 - 5
USB port 1	CN2	IDE interface LED	J3/Pins 7 & 8
USB port 2	CN3	Turbo LED	J3/Pins 9 & 10
Parallel port	CN4	Power switch	J3/Pins 12 & 13
Serial port 1	CN5	CPU fan power	J4
Serial port 2	CN6	Power supply fan	J5
Floppy drive interface	CN7	Chassis fan power	J6
IDE interface 1	CN8	Wake-on-LAN connector	J7
IDE interface 2	CN9	SB-link connector	J8
Reset switch	J2/Pins 1 & 2	32-bit PCI slots	PC1 - PC5

### USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	JP1	Pins 1 & 2 closed
	CMOS memory clear	JP1	Pins 2 & 3 closed
»	Power-on keyboard disabled	JP4	Pins 1 & 2 closed
	Power-on keyboard enabled	JP4	Pins 2 & 3 closed
»	SDRAM clock speed set to match CPU clock	JP5	Pins 1 & 2 closed
	SDRAM clock speed set to match AGP clock	JP5	Pins 2 & 3 closed

### DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None

32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board supports EDO & SDRAM memory.

### CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPUs.

### CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP3
200MHz	66MHz	3x	1 & 2, 15 & 16

233MHz	66MHz	3.5x	3 & 4, 15 & 16
266MHz	66MHz	4x	5 & 6, 15 & 16
300MHz	66MHz	4.5x	7 & 8, 15 & 16
300MHz	100MHz	3x	1 & 2, 21 & 22
333MHz	66MHz	5X	9 & 10, 15 & 16
350MHz	100MHz	3.5x	3 & 4, 21 & 22
400MHz	100MHz	4x	5 & 6, 21 & 22
450MHz	100MHz	4.5x	7 & 8, 21 & 22
500MHz	100MHz	5x	9 & 10, 21 & 22
Automatic CPU speed selection			13 & 14
Note: Designated pins should be in the closed position			