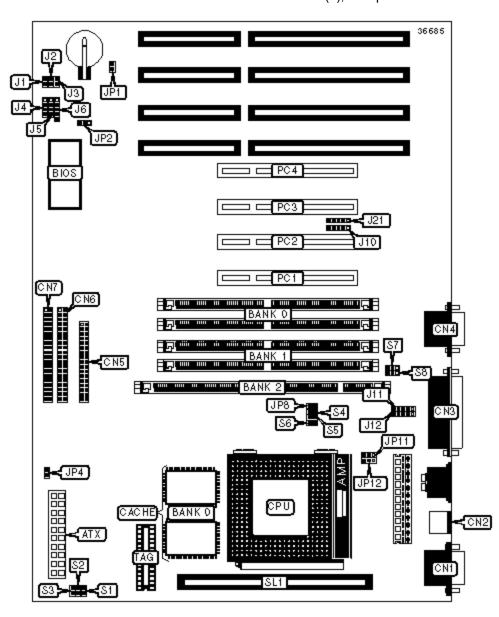
DTK COMPUTER, INC.

PAM-0070I (VER. 1.02)

Device Type Processor Processor Speed Chip Set Maximum Onboard Memory Cache BIOS Dimensions I/O Options Mainboard CX 6X86/AM K5/Pentium/Pentium MMX 75/90/100/110/120/133/150/166/200MHz Intel 430VX 128MB (EDO & SDRAM supported) 512KB Award/AMI 305mm x 210mm 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, Fast IR connector, USB interfaces (2), ATX power connector



CONNECTIONS					
Purpose Location Purpose Location					
ATX power connector ATX PC speaker			J4		
Serial port 1	CN1	IDE interface LED	J5		

PS/2 mouse port	CN2	Power LED & Keylock	J6
Parallel port	CN3	IR connector	J10
Serial port 2	CN4	USB interface 1	J11
Floppy drive interface	CN5	USB interface 2	J12
IDE interface 1	CN6	Fast IR connector	J21
IDE interface 2	CN7	Power switch	JP4
Reset switch	J1	32-bit PCI slots	PC1 - PC4
Green PC switch	J2	Cache slot	SL1
Green PC LED	J3		

	USER CONFIGURABLE SETTINGS					
	Function Label Position					
»	CMOS memory normal operation	JP1	Open			
	CMOS memory clear	JP1	Closed			
	5V Flash EPROM enabled	JP2	Pins 2 & 3 closed			
	12V Flash EPROM enabled	JP2	Pins 1 & 2 closed			

SIMM CONFIGURATION					
Size Bank 0		Bank 1			
8MB	(2) 1M x 36	None			
16MB	(2) 2M x 36	None			
16MB	(2) 1M x 36	(2) 1M x 36			
24MB	(2) 2M x 36 (2) 1M x 3				
32MB	(2) 4M x 36	None			
32MB	(2) 2M x 36	(2) 2M x 36			
40MB	(2) 4M x 36	(2) 1M x 36			
48MB	(2) 4M x 36	(2) 2M x 36			

64MB	(2) 8M x 36	None			
64MB	(2) 4M x 36	(2) 4M x 36			
72MB	(2) 8M x 36	(2) 1M x 36			
80MB	(2) 8M x 36	(2) 2M x 36			
96MB	(2) 8M x 36	(2) 4M x 36			
128MB (2) 8M x 36 (2) 8M x 36					
Note: Board supports EDO memory. Note: If any memory is placed in the DIMM socket, SIMM Bank 0 must be left empty.					

DIMM CONFIGURATION			
Size	Bank 0		
8MB	(1) 1M x 64		
16MB	(1) 2M x 64		
32MB	(1) 4M x 64		
64MB	(1) 8M x 64		
128MB	(1) 16M x 64		
256MB (1) 32M x 64			
Note: Board supports SDRAM memory. Note: If any memory is placed in the DIMM socket, SIMM Bank 0 must be left empty.			

CACHE CONFIGURATION				
Size SL1 TAG				
256KB	Not Installed	8KB x 8, 16KB x 8 or 32KB x 8		
512KB	256KB SRAM	8KB x 8, 16KB x 8 or 32KB x 8		

CPU SPEED SELECTION (PENTIUM)							
Speed JP11 JP12 S1 S2 S3							
75MHz	75MHz 1 & 2 1 & 2 Closed Closed Open						

90MHz	1 & 2	1 & 2	Closed	Open	Open	
100MHz	1 & 2	1 & 2	Open	Open	Open	
120MHz	2 & 3	1 & 2	Closed	Open	Open	
133MHz	2 & 3	1 & 2	Open	Open	Open	
150MHz	2 & 3	2 & 3	Closed	Open	Open	
166MHz	2 & 3	2 & 3	Open	Open	Open	
200MHz 1 & 2 2 & 3 Open Open Open						
Note: Designated pins should be in the closed position.						

CPU SPEED SELECTION (AM K5)						
Speed	speed JP11 JP12 S1 S2 S					
75MHz	1 & 2	1 & 2	Closed	Closed	Open	
90MHz	1 & 2	1 & 2	Closed	Open	Open	
100MHz	1 & 2	1 & 2	Open	Open	Open	
120MHz	1 & 2	1 & 2	Closed	Open	Open	
133MHz	1 & 2	1 & 2	Open	Open	Open	
150MHz	1 & 2	1 & 2	Open	Open	Open	
166MHz	2 & 3	2 & 3	Open	Open	Open	
Note: Desi	Note: Designated pins should be in the closed position.					

CPU SPEED SELECTION (CX 6X86)								
Speed	Speed JP11 JP12 S1 S2 S3							
120MHz	2 & 3	Closed	Open					
133Mhz	2 & 3	1 & 2	Open	Closed	Open			
150MHz	2 & 3	1 & 2	Closed	Open	Open			
166MHz 2 & 3 1 & 2 Open Open Open								
200MHz	2 & 3	1 & 2	Open	Open	Closed			
Note: Desig	qnated pins s	should be in t	he closed po:	sition.				

Note: Designated pins should be in the closed position.

CPU VOLTAGE SELECTION (SINGLE)							
Voltage JP8 S4 S5 S6							
3.4V	Closed	Closed	Closed	Closed			
3.5V	3.5V Open Closed Closed Closed						

CPU VOLTAGE SELECTION (DUAL)					
Voltage	JP8	S4	S5	S6	
3.4V/2.8V	Closed	Open	Open	Open	

DRAM VOLTAGE SELECTION				
Voltage	S7	S8		
5V	Pins 1 & 2 closed	Pins 1 & 2 closed		
3.3V	Pins 2 & 3 closed	Pins 2 & 3 closed		