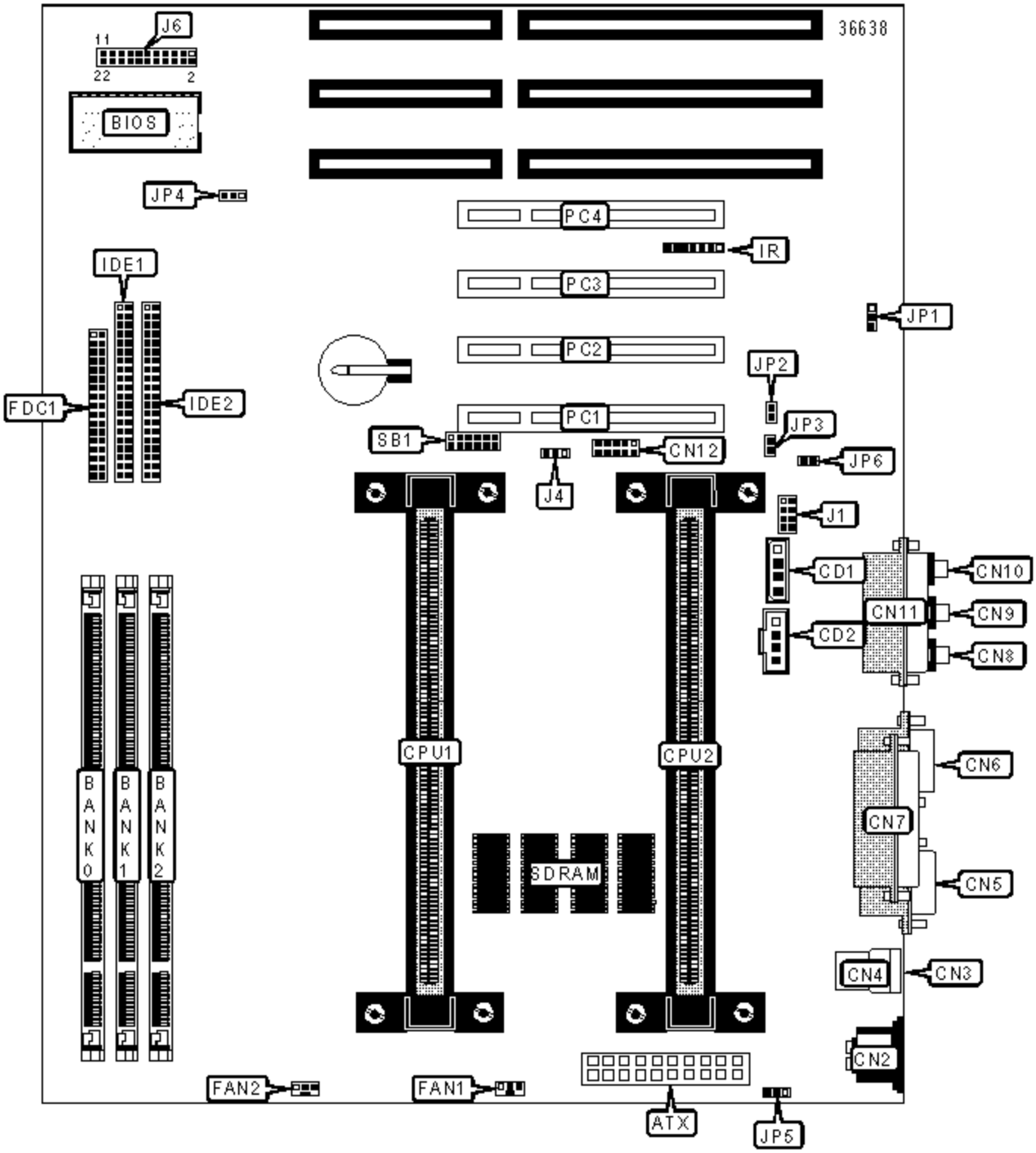
PC CHIPS MANUFACTURING, INC.

M750

Device Type	Mainboard
Processor	Pentium II/Celeron
Processor Speed	233/266/300/333/350/366/400/433/450/500MHz
Chip Set	Intel 440BX
Video Chip Set	Intel i740
Audio Chip Set	Sound Pro
Maximum Onboard Memory	768MB (SDRAM supported)
Maximum Video Memory	8MB
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the Pentium II CPU)
BIOS	AMI
Dimensions	305mm x 220mm
I/O Options (backplane)	32-bit PCI slots (4), floppy drive interface, game port, IDE interfaces (2), parallel port, PS/2 mouse port, serial port, VGA port, IR connector, USB ports (2), ATX power connector, line in, line out, microphone in, audio in - CD-ROMs (3), SB-Link connector, Wake-on-LAN connector



CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
ATX power connector	ATX	Floppy drive interface	FDC1
Audio in - CD-ROM (Panasonic)	CD1	IDE interface 1	IDE1
Audio in - CD-ROM (Sony)	CD2	IDE interface 2	IDE2
PS/2 mouse port	CN2	IR connector	IR
USB port 1	CN3	Digital audio in connector	J1

USB port 2	CN4	Wake-on-LAN connector	J4
Serial port 1	CN5	Speaker	J6/Pins 1, 3, 5 & 7
VGA port	CN6	Power LED & keylock	J6/Pins 2, 4, 6, 8 & 10
Parallel port	CN7	Turbo LED	J6/Pins 13 & 14
Line in	CN8	IDE interface LED	J6/Pins 15 & 16
Microphone in	CN9	Reset switch	J6/Pins 17 & 18
Line out	CN10	Power switch/Green PC switch	J6/Pins 21 & 22
Game port	CN11	Digital Audio in - CD-ROM	JP6
Serial interface 2	CN12	32-bit PCI slots	PC1 - PC4
CPU fan power 1	FAN1	SB-Link connector	SB1
CPU fan power 2	FAN2		

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
»	SPDIF OUT signal level selector 0.5v	JP2	Open
	SPDIF OUT signal level selector 5.0v	JP2	Closed
»	CMOS memory normal operation	JP4	Pins 1 & 2 closed
	CMOS memory clear	JP4	Pins 2 & 3 closed
»	Power-on keyboard disabled	JP5	Pins 2 & 3 closed
	Power-on keyboard enabled	JP5	Pins 1 & 2 closed

DIMM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64

32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64

768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports SDRAM memory.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU. 128KB is located on the Celeron 300A and greater CPUs.

MISCELLANEOUS TECHNICAL NOTE

Board supports dual Pentium II CPUs, but only single Celeron CPUs.