# DIAMOND FLOWER, INC.

## P2XBL (REV. D+)

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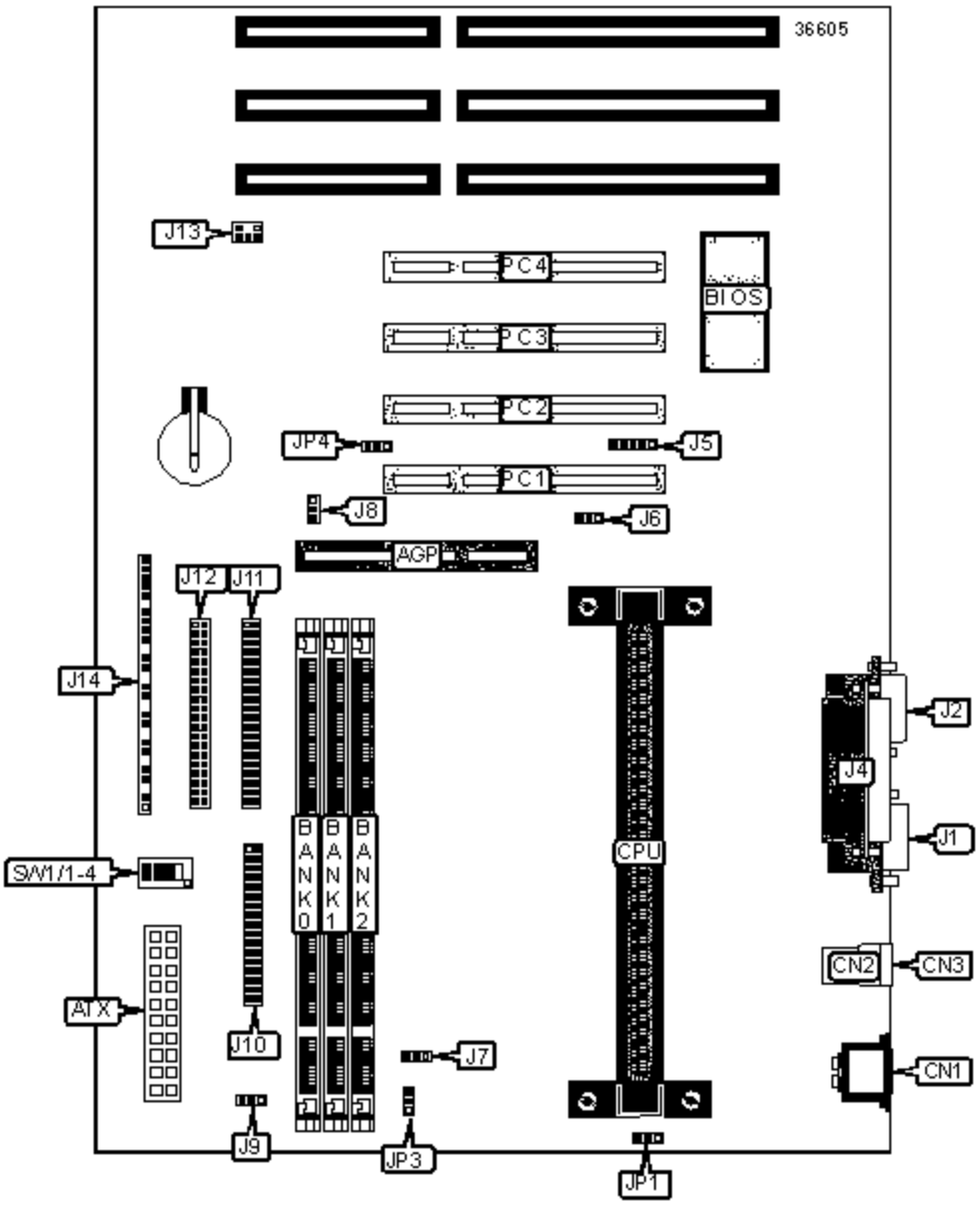
|---|---|
| **Device Type** | Mainboard |
| **Processor** | Celeron/Pentium II/Pentium III |
| **Processor Speed** | 233/266/300/333/350/366/400/433/450/500MHz |
| **Chip Set** | Intel 440BX |
| **Maximum Onboard Memory** | 768MB (SDRAM supported) |
| **Cache** | 0/128/256/512KB (located on the CPU) |
| **BIOS** | Award |
| **Dimensions** | 304mm x 170mm |
| **I/O Options** | 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connectors (2), ATX power connector, AGP slot, Wake-on-LAN connector, SB-Link connector |




| CONNECTIONS | | | |
|---|---|---|---|
| **Purpose** | **Location** | **Purpose** | **Location** |
| AGP slot | AGP | Floppy drive interface | J10 |

| | | | |
|---|---|---|---|
| ATX power connector | ATX | IDE interface 2 | J11 |
| PS/2 mouse port | CN1 | IDE interface 1 | J12 |
| USB connector 1 | CN2 | SB-Link connector | J13 |
| USB connector 2 | CN3 | IDE interface LED | J14/Pins 1 & 2 |
| Serial port 1 | J1 | Green PC LED | J14/Pins 4 & 5 |
| Serial port 2 | J2 | Soft off power supply | J14/Pins 7 & 8 |
| Parallel port | J4 | Green PC connector | J14/Pins 10 & 11 |
| IR connector | J5 | Reset switch | J14/Pins 13 & 14 |
| AGP fan power | J6 | Speaker | J14/Pins 16-19 |
| CPU fan power | J7 | Power LED & keylock | J14/Pins 21-25 |
| Wake-on-LAN connector | J8 | 32-bit PCI slots | PC1 - PC4 |
| Chassis fan power | J9 | | |

| USER CONFIGURABLE SETTINGS | | | |
|---|---|---|---|
| **Function** | | **Label** | **Position** |
| » | Keyboard/mouse power on disabled | JP1 | Pins 1 & 2 Closed |
| | Keyboard/mouse power on enabled | JP1 | Pins 3 & 3 Closed |
| » | Clock speed auto-detect | JP3 | Pins 1 & 2 Closed |
| » | CMOS memory normal operation | JP4 | Pins 1 & 2 Closed |
| | CMOS memory clear | JP4 | Pins 2 & 3 Closed |
| Note: JP3 can be set for 66MHz or 100MHz. For futher information see CPU speed selection tables. | | | |

| DIMM CONFIGURATION | | | |
|---|---|---|---|
| **Size** | **Bank 0** | **Bank 1** | **Bank 2** |
| 16MB | (1) 2M x 64 | None | None |
| 32MB | (1) 2M x 64 | (1) 2M x 64 | None |
| 32MB | (1) 4M x 64** | None | None |
| 48MB | (1) 2M x 64 | (1) 2M x 64 | (1) 2M x 64 |

| | | | |
|---|---|---|---|
| 48MB | (1) 2M x 64* | (1) 2M x 64* | (1) 2M x 64* |
| 64MB | (1) 4M x 64** | (1) 4M x 64** | None |
| 64MB | (1) 8M x 64** | None | None |
| 64MB | (1) 4M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 96MB | (1) 4M x 64** | (1) 4M x 64** | (1) 4M x 64** |
| 96MB | (1) 8M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 128MB | (1) 8M x 64** | (1) 8M x 64** | None |
| 128MB | (1) 16M x 64** | None | None |
| 128MB | (1) 8M x 64** | (1) 4M x 64** | (1) 4M x 64** |
| 160MB | (1) 16M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 192MB | (1) 8M x 64** | (1) 8M x 64** | (1) 8M x 64** |
| 192MB | (1) 16M x 64** | (1) 4M x 64** | (1) 4M x 64** |
| 256MB | (1) 16M x 64** | (1) 16M x 64** | None |
| 256MB | (1) 32M x 64* | None | None |
| 256MB | (1) 16M x 64** | (1) 8M x 64** | (1) 8M x 64** |
| 320MB | (1) 32M x 64* | (1) 4M x 64* | (1) 4M x 64* |
| 384MB | (1) 16M x 64** | (1) 16M x 64** | (1) 16M x 64** |
| 384MB | (1) 32M x 64* | (1) 8M x 64* | (1) 8M x 64* |
| 512MB | (1) 32M x 64* | (1) 32M x 64* | None |
| 512MB | (1) 32M x 64* | (1) 16M x 64* | (1) 16M x 64* |
| 768MB | (1) 32M x 64* | (1) 32M x 64* | (1) 32M x 64* |

Note: Board supports SDRAM memory.

*Note: Designated DIMM's must be registered memory.

**Note: Designated DIMM combinations can be either registered or unbuffered memroy.

| CACHE CONFIGURATION |
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| Note: 256KB/512KB cache is located on the Pentium II/Pentium III CPU. 128KB cache is located on the Celeron 300A and greater CPU. |

| CPU SPEED SELECTION (INTEL CELERON) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP3** | **SW1/1** | **SW1/2** | **SW1/3** | **SW1/4** |
| 266MHz | 66MHz | 4x | Pins 2 & 3 | On | On | Off | On |
| 300MHz | 66MHz | 4.5x | Pins 2 & 3 | Off | On | Off | On |
| 300AMHz | 66MHz | 4.5x | Pins 2 & 3 | Off | On | Off | On |
| 333MHz | 66MHz | 5x | Pins 2 & 3 | On | Off | Off | On |
| 366MHz | 66MHz | 5.5x | Pins 2 & 3 | Off | Off | Off | On |
| 400MHz | 66MHz | 6x | Pins 2 & 3 | On | On | On | Off |
| 433MHz | 66MHz | 6.5x | Pins 2 & 3 | Off | On | On | Off |
| Note: Pins designated should be in the closed position. | | | | | | | |

| CPU SPEED SELECTION (PENTIUM II) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP3** | **SW1/1** | **SW1/2** | **SW1/3** | **SW1/4** |
| 233MHz | 66MHz | 3.5x | Pins 2 & 3 | Off | Off | On | On |
| 266MHz | 66MHz | 4x | Pins 2 & 3 | On | On | Off | On |
| 300MHz | 66MHz | 4.5x | Pins 2 & 3 | Off | On | Off | On |
| 333MHz | 66MHz | 5x | Pins 2 & 3 | On | Off | Off | On |
| 350MHz | 100MHz | 3.5x | Open | Off | Off | On | On |
| 400MHz | 100MHz | 4x | Open | On | On | Off | On |
| 450MHz | 100MHz | 4.5x | Open | Off | On | Off | On |
| Note: Pins designated should be in the closed position. | | | | | | | |

| CPU SPEED SELECTION (PENTIUM III) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP3** | **SW1/1** | **SW1/2** | **SW1/3** | **SW1/4** |
| 450MHz | 100MHz | 4.5x | Open | Off | On | Off | On |
| 500MHz | 100MHz | 5x | Open | On | Off | Off | On |

Note: Pins designated should be in the closed position.