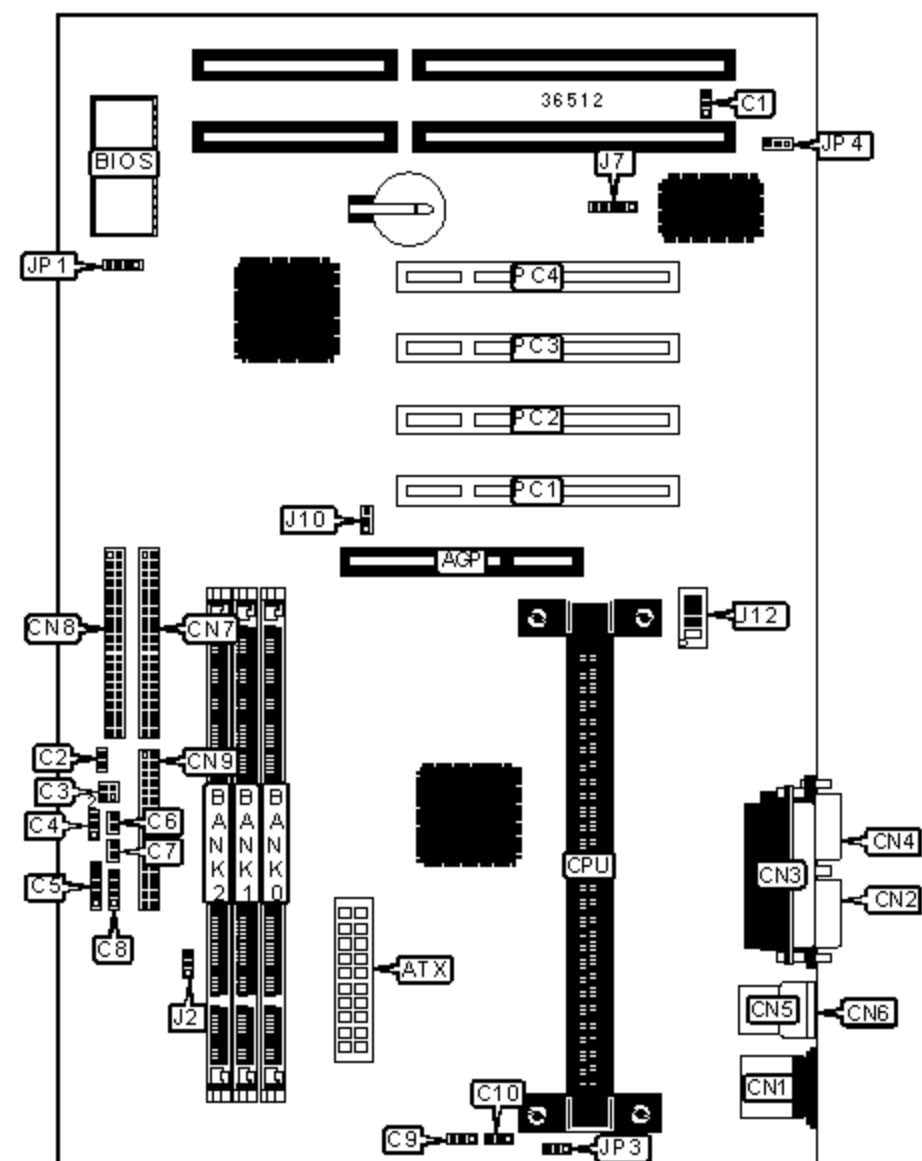


J-MARK COMPUTER CORPORATION

J-7BXAN

Device Type	Mainboard
Processor	Pentium II
Processor Speed	233/266/300/333/350/400/450MHz
Chip Set	Intel 440BX
Maximum Onboard Memory	768MB (EDO & SDRAM supported)
Cache	256/512KB (located on Pentium II CPU)
BIOS	Unidentified
Dimensions	305mm x 190mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connectors (2), ATX power connector, AGP slot



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	CPU fan power	C10

ATX power connector	ATX	PS/2 mouse port	CN1
Chassis fan power	C1	Serial port 2	CN2
Soft off power supply	C2	Parallel port	CN3
IDE interface LED	C3/pins 1 & 3	Serial port 1	CN4
Green PC connector	C3/pins 2 & 4	USB connector 1	CN5
Turbo switch	C4	USB connector 2	CN6
Power LED & keylock	C5	IDE interface 2	CN7
Turbo LED	C6	IDE interface 1	CN8
Reset switch	C7	Floppy drive interface	CN9
Speaker	C8	32-bit PCI slots	PC1 – PC4
Chassis fan power	C9	IR connector	J7

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Factory configured - do not alter	J2	Unidentified
»	Factory configured - do not alter	JP3	Unidentified
»	Factory configured - do not alter	JP4	Unidentified

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64	None
40MB	(1) 2M x 64	(1) 2M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64

48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
56MB	(1) 4M x 64	(1) 2M x 64	(1) 1M x 64
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64	None
72MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
88MB	(1) 8M x 64	(1) 2M x 64	(1) 1M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
104MB	(1) 8M x 64	(1) 4M x 64	(1) 1M x 64
112MB	(1) 8M x 64	(1) 4M x 64	(1) 2M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
152MB	(1) 16M x 64	(1) 2M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64	None
160MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
168MB	(1) 16M x 64	(1) 4M x 64	(1) 1M x 64
176MB	(1) 16M x 64	(1) 4M x 64	(1) 2M x 64

192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
200MB	(1) 16M x 64	(1) 8M x 64	(1) 1M x 64
208MB	(1) 16M x 64	(1) 8M x 64	(1) 2M x 64
224MB	(1) 16M x 64	(1) 8M x 64	(1) 4M x 64
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
264MB	(1) 32M x 64	(1) 1M x 64	None
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
272MB	(1) 32M x 64	(1) 2M x 64	None
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64	None
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
320MB	(1) 32M x 64	(1) 8M x 64	None
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board accepts EDO & SDRAM memory.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	J10	U12/1	U12/2	U12/3	U12/4
233MHz	66MHz	3.5x	2 & 3	Off	Off	On	On
266MHz	66MHz	4x	2 & 3	On	On	Off	On

300MHz	66MHz	4.5x	2 & 3	Off	On	Off	On
333MHz	66MHz	5x	2 & 3	On	Off	Off	On
350MHz	100MHz	3.5x	1 & 2	Off	Off	On	On
400MHz	100MHz	4x	1 & 2	On	On	Off	On
450MHz	100MHz	4.5x	1 & 2	Off	On	Off	On

Note: Pins designated should be in the closed position.

CMOS/BATTERY SELECTION	
Setting	JP1
»CMOS memory normal operation	Pins 2 & 3 closed
CMOS memory clear	Pins 3 & 4 closed
External battery installed	Closed