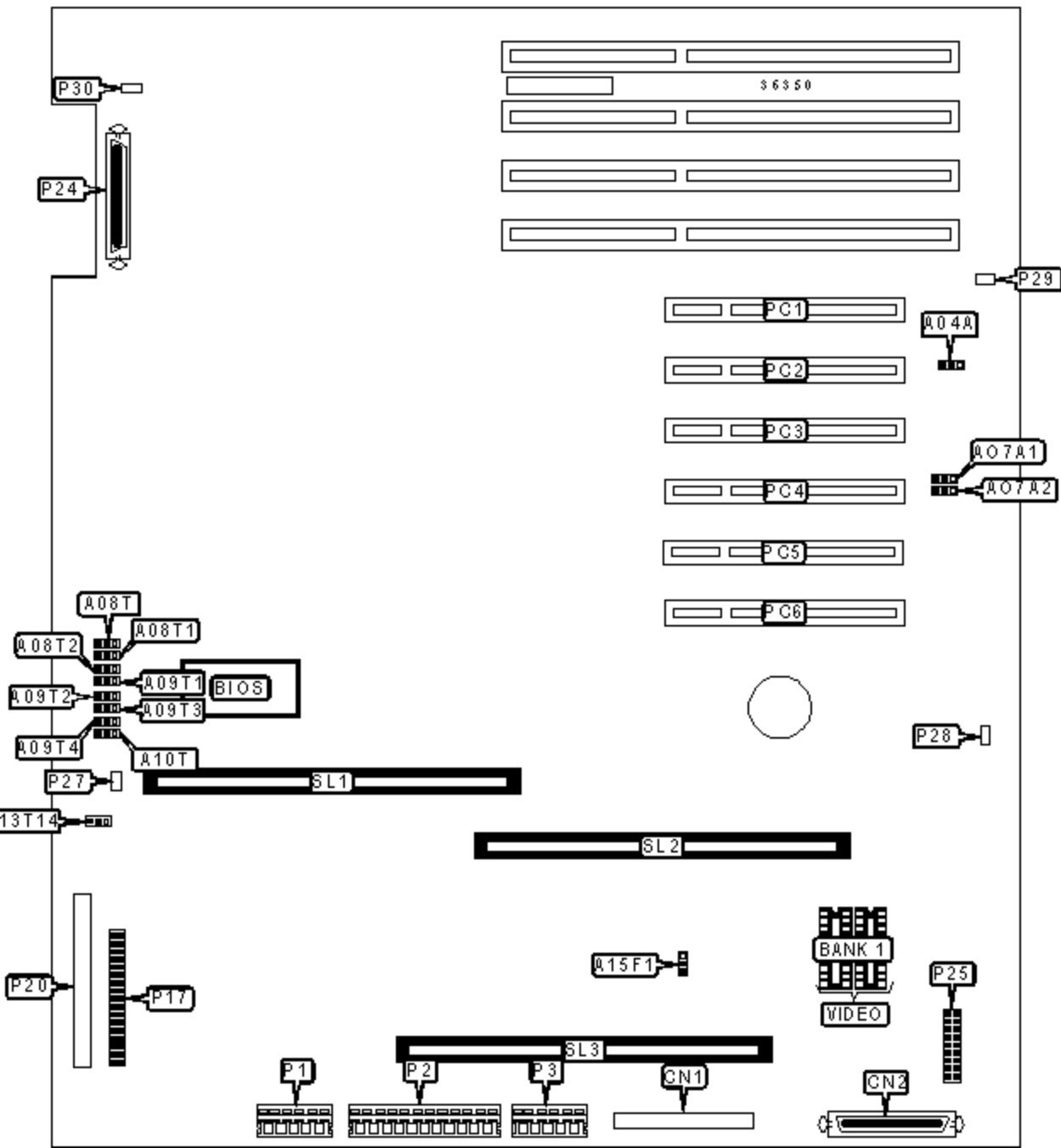


ZENITH DATA SYSTEMS

EXPRESS 5800 MH4000

Device Type	Mainboard
Processor	Pentium Pro
Processor Speed	200MHz
Chip Set	Unidentified
Video Chip Set	Unidentified
Maximum Onboard Memory	4GB (SDRAM supported)
Maximum Video Memory	2MB
Cache	512KB (located on Pentium Pro CPU)
BIOS	Unidentified
Dimensions	355mm x 304mm
I/O Options	External memory card, 32-bit PCI slots (6), floppy drive interface, SCSI interfaces (2), RAID slot, CPU slot



CONNECTIONS

Purpose	Location	Purpose	Location
SCSI status connector	CN1	Chassis fan power	P28
SCSI interface	CN2	Chassis fan power	P30
Power connector	P1	32-bit PCI slots	PC1 - PC6
Power connector	P2	CPU slot 1	SL1
Power connector	P3	CPU slot 2	SL2
Floppy drive interface	P17	External memory card	SL3

Front panel connector	P20	RAID slot	SL4
Ultra Wide SCSI-3 interface	P24	Riser board slot	SL5
Chassis fan power	P27	IR connector	

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Flash BIOS write protect disabled	A04A	Pins 1 & 2 closed
	Flash BIOS write protect enabled	A04A	Pins 2 & 3 closed
»	Factory configured - do not alter	A06K	Pins 1 & 2 closed
»	Factory configured - do not alter	A07A1	Pins 1 & 2 closed
»	Factory configured - do not alter	A07A2	Pins 1 & 2 closed
»	Factory configured - do not alter	A08T	Pins 1 & 2 closed
»	Factory configured - do not alter	A08T1	Pins 1 & 2 closed
»	Password enabled	A08T2	Pins 1 & 2 closed
	Password disabled	A08T2	Pins 2 & 3 closed
»	CMOS memory normal operation	A09T1	Pins 1 & 2 closed
	CMOS memory clear	A09T1	Pins 2 & 3 closed
»	Factory configured - do not alter	A13T14	Pins 1 & 2 closed
»	Factory configured - do not alter	A15F1	Pins 1 & 2 closed
»	Factory configured - do not alter	J1	Unidentified
»	Factory configured - do not alter	P25	All jumpers closed
»	Factory configured - do not alter	P29	Unidentified

VIDEO MEMORY CONFIGURATION

Size	Bank 0	Bank 1
1MB	(2) 256K x 16	None

2MB

(2) 256K x 16

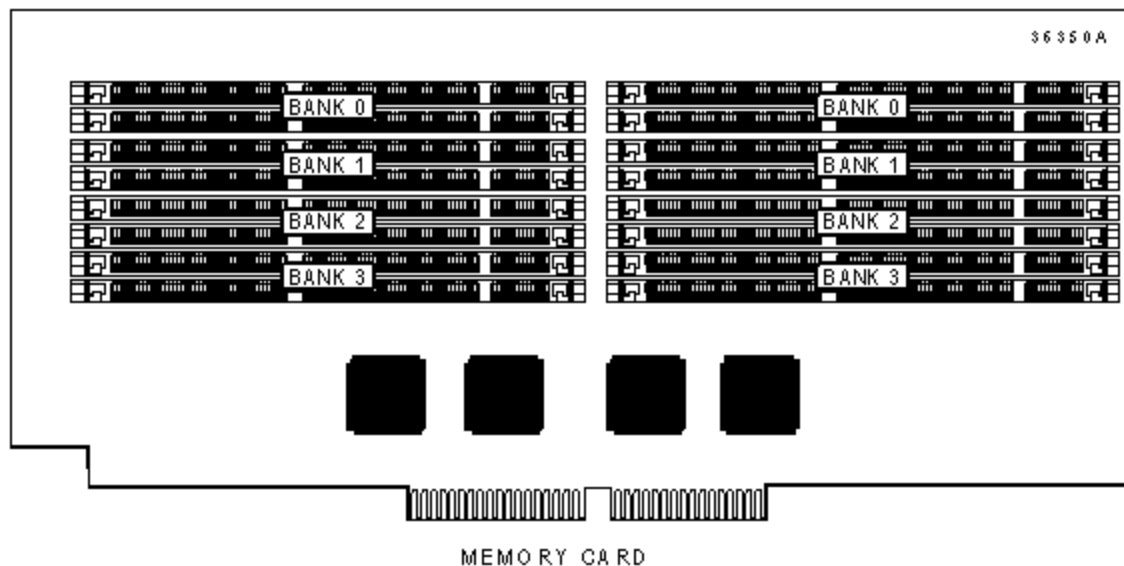
(2) 256K x 16

Note: The location of bank 0 is unidentified.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	A10T	A09T2	A09T3	A09T4
200MHz	66MHz	3x	2 & 3	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

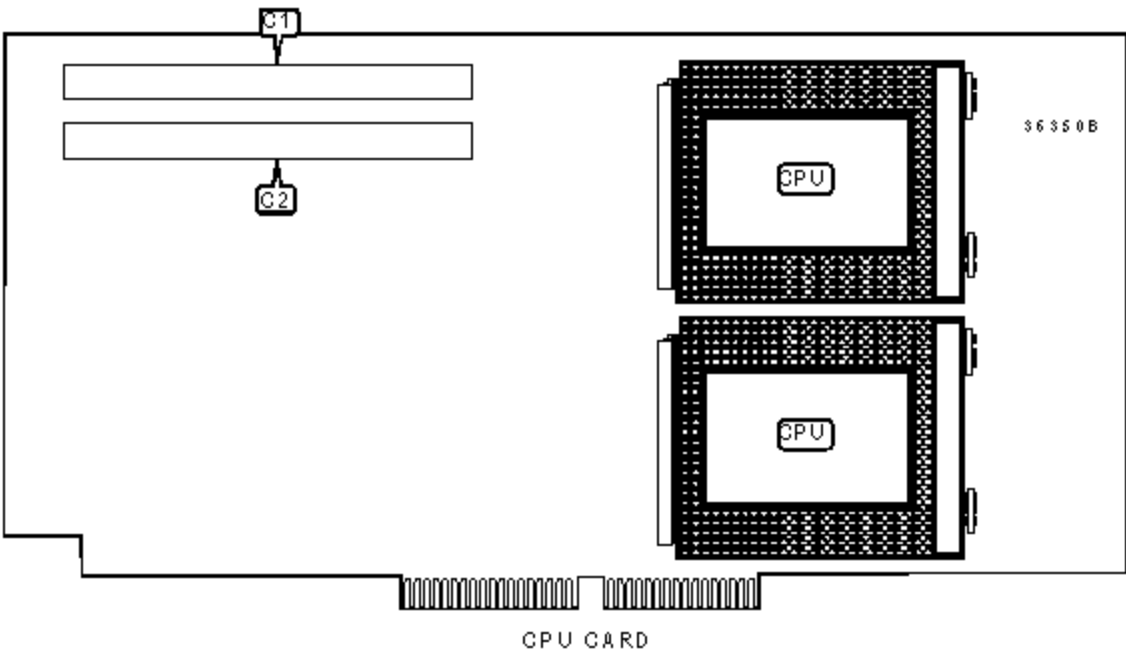


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DIMM CONFIGURATION

Size	Interleave configuration	Bank 0	Bank 1	Bank 2	Bank 3
64MB	Two-way	(2) 4M x 64	None	None	None
128MB	Four-way	(4) 4M x 64	None	None	None
256MB	Four-way	(4) 8M x 64	None	None	None
256MB	Four-way	(4) 4M x 64	(4) 4M x 64	None	None
384MB	Four-way	(4) 8M x 64	(4) 4M x 64	None	None
512MB	Four-way	(4) 16M x 64	None	None	None
512MB	Four-way	(4) 8M x 64	(4) 8M x 64	None	None
640MB	Four-way	(4) 16M x 64	(4) 4M x 64	None	None
768MB	Four-way	(4) 16M x 64	(4) 8M x 64	None	None

1024MB	Four-way	(4) 32M x 64	None	None	None
1024MB	Four-way	(4) 16M x 64	(4) 16M x 64	None	None
1152MB	Four-way	(4) 32M x 64	(4) 4M x 64	None	None
1280MB	Four-way	(4) 32M x 64	(4) 8M x 64	None	None
1536MB	Four-way	(4) 32M x 64	(4) 16M x 64	None	None
2084MB	Four-way	(4) 32M x 64	(4) 32M x 64	None	None
2176MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 4M x 64	None
2304MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 8M x 64	None
2560MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 16M x 64	None
3072MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 32M x 64	None
3200MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 32M x 64	(4) 4M x 64
3328MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 32M x 64	(4) 8M x 64
3584MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 32M x 64	(4) 16M x 64
4096MB	Four-way	(4) 32M x 64	(4) 32M x 64	(4) 32M x 64	(4) 32M x 64



CONNECTIONS			
Purpose	Location	Purpose	Location
DC to DC converter 1	C1	DC to DC converter 2	C2

CACHE CONFIGURATION

Note: 512KB cache is located on the Pentium Pro CPU.