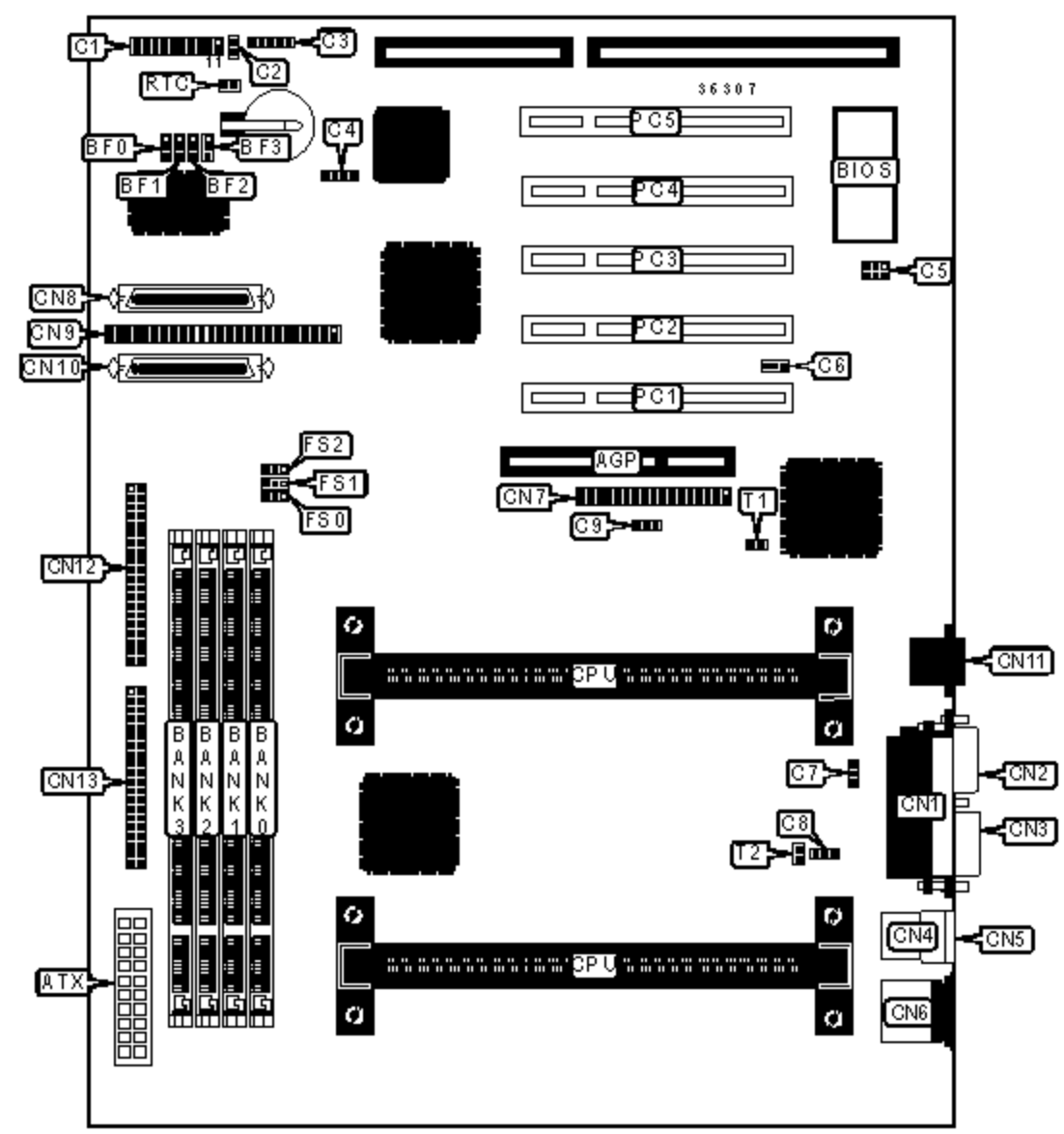


ASUS COMPUTER INTERNATIONAL

XG-DLS (REV. 1.03)

Device Type	Mainboard
Processor	Xeon
Processor Speed	450/500/550MHz
Chip Set	Intel 440GX
Maximum Onboard Memory	2GB (SDRAM & PC100 supported)
Cache	128KB (located on the Celeron CPU)
BIOS	Unidentified
Dimensions	305mm x 244mm
I/O Options	32-bit PCI slots (5), Ethernet 10BaseT connector, floppy drive interface, green PC connector, IDE interfaces (2), SCSI interface, SCSI-2 interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connectors (2), ATX power connector, AGP slot, SB-link connector, wake on LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	Parallel port	CN1
ATX power connector	ATX	Serial port 2	CN2

Turbo LED	C1/pins 2 & 3	Serial port 1	CN3
Green PC connector	C1/pins 4 & 5	USB connector 1	CN4
Soft off power supply	C1/pins 6 & 7	USB connector 2	CN5
Reset switch	C1/pins 9 & 10	PS/2 mouse port	CN6
Power LED & keylock	C1/pins 11 - 15	Floppy drive interface	CN7
Speaker	C1/pins 17 - 20	SCSI-2 interface	CN8
IDE interface LED	C2	SCSI interface	CN9
IR connector	C3	SCSI-2 interface	CN10
Chassis intrusion connector	C4	Ethernet 10BaseT connector	CN11
SB-link connector	C5	IDE interface 1	CN12
Wake on LAN connector	C6	IDE interface 2	CN13
Chassis fan power	C7	32-bit PCI slots	PC1 - PC5
Power fan power	C8		T1
CPU fan power	C9		T2

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	RTC	Open
	CMOS memory clear	RTC	Closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 64	None	None	None
16MB	(1) 2M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
24MB	(1) 2M x 64	(1) 1M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None

32MB	(1) 4M x 64	None	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
40MB	(1) 4M x 64	(1) 1M x 64	None	None
48MB	(1) 4M x 64	(1) 2M x 64	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None	None
72MB	(1) 8M x 64	(1) 1M x 64	None	None
80MB	(1) 8M x 64	(1) 2M x 64	None	None
96MB	(1) 8M x 64	(1) 4M x 64	None	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None

DIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
128MB	(1) 16M x 64	None	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
136MB	(1) 16M x 64	(1) 1M x 64	None	None
144MB	(1) 16M x 64	(1) 2M x 64	None	None
176MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
256MB	(1) 32M x 64	None	None	None
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64

280MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
448MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 64M x 64	None	None	None
512MB	(1) 32M x 64	(1) 32M x 64	None	None
512MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
640MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
1024MB	(1) 64M x 64	(1) 64M x 64	None	None
1024MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64
1536MB	(1) 64M x 64	(1) 64M x 64	(1) 64M x 64	None
2048MB	(1) 64M x 64	(1) 64M x 64	(1) 64M x 64	(1) 64M x 64

Note: Board accepts SDRAM & PC100 memory.

CACHE CONFIGURATION

Note: 128KB cache is located on the Xeon 300A & 333 CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	FS0	FS1	FS2
450MHz	100MHz	4.5x	1 & 2	1 & 2	1 & 2
500MHz	100MHz	5x	1 & 2	1 & 2	1 & 2
550MHz	100MHz	5.5x	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CON'T)

CPU speed	Clock speed	Multiplier	FS0	FS1	FS2

CPU speed	Clock speed	Multiplier	BF0	BF1	BF2	BF3
450MHz	100MHz	4.5x	2 & 3	1 & 2	2 & 3	1 & 2
500MHz	100MHz	5x	1 & 2	2 & 3	2 & 3	1 & 2
550MHz	100MHz	5.5x	*	*	*	*

Note: Pins designated should be in the closed position. * = Setting unavailable.