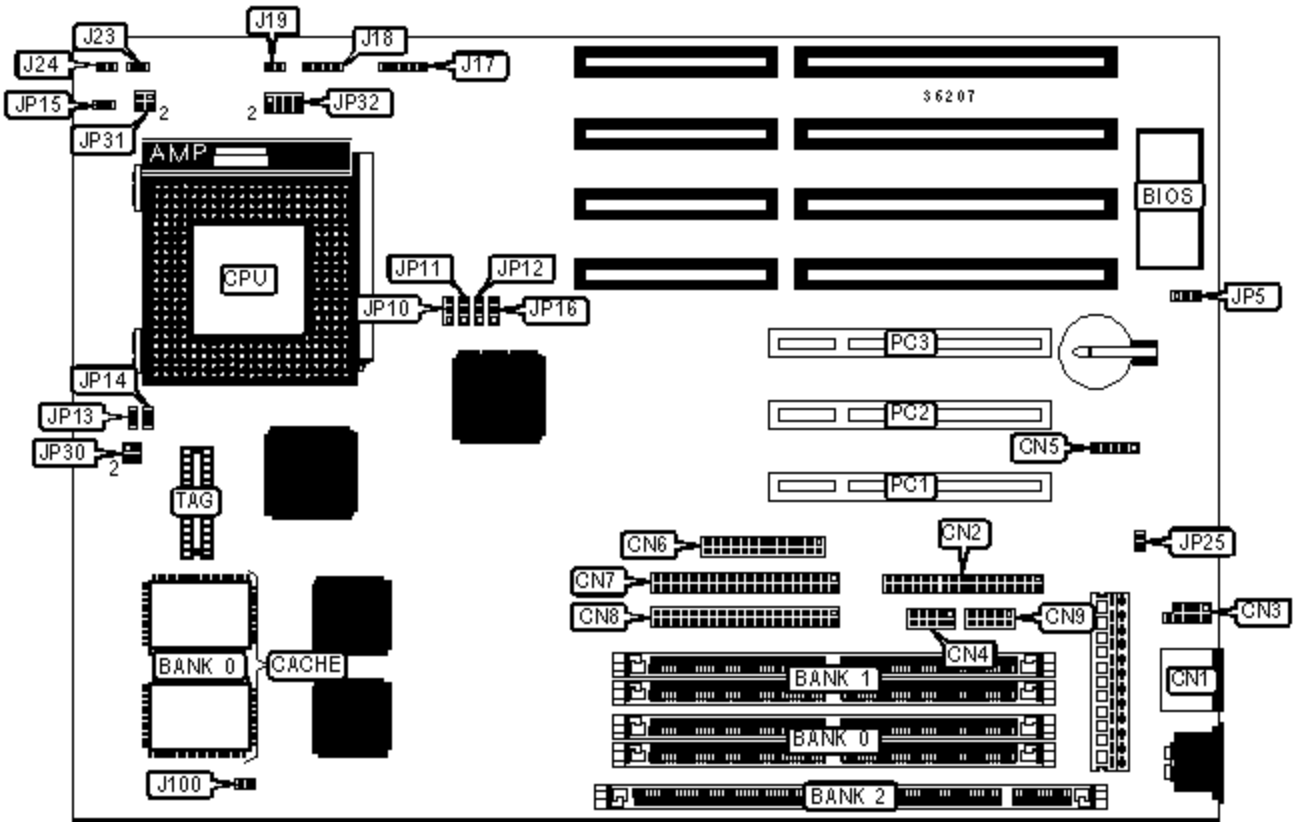
# SURIA COMPUTER CORPORATION

## SC-5EA5

| | |
|---|---|
| **Processor** | CX 6X86/CX 6X86L/CX 6X86MX/AM K5/AM K6/Pentium |
| **Processor Speed** | 75/90/100/120/133/150/166/180/200/233/266MHz |
| **Chip Set** | Unidentified |
| **Maximum Onboard Memory** | 256MB (EDO & SDRAM supported) |
| **Cache** | 512KB |
| **BIOS** | Unidentified |
| **Dimensions** | 254mm x 218mm |
| **I/O Options** | 32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connector |
| **NPU Options** | None |




| CONNECTIONS | | | |
|---|---|---|---|
| **Purpose** | **Location** | **Purpose** | **Location** |
| PS/2 mouse port | CN1 | Serial port 1 | CN9 |
| Floppy drive interface | CN2 | Power LED & keylock | J17 |
| USB connector | CN3 | Speaker | J18 |
| Serial port 2 | CN4 | Reset switch | J19 |
| IR connector | CN5 | Green PC connector | J23 |
| Parallel port | CN6 | IDE interface LED | J24 |
| IDE interface 2 | CN7 | 32-bit PCI slots | PC1 - PC3 |

| | | | |
|---|---|---|---|
| IDE interface 1 | CN8 | | |

| USER CONFIGURABLE SETTINGS | | | |
|---|---|---|---|
| **Function** | | **Label** | **Position** |
| » | Factory configured - do not alter | J100 | Open |
| » | CMOS memory normal operation | JP5 | Pins 1 & 2 closed |
| | CMOS memory clear | JP5 | Pins 2 & 3 closed |
| » | PCI bus select synchronous | JP16 | Pins 1 & 2 closed |
| | PCI bus select asynchronous | JP16 | Pins 2 & 3 closed |
| » | EMI signals disabled | JP25 | Closed |
| | EMI signals enabled | JP25 | Open |

| SIMM CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **Bank 1** |
| 8MB | (2) 1M x 36 | None |
| 16MB | (2) 2M x 36 | None |
| 16MB | (2) 1M x 36 | (2) 1M x 36 |
| 24MB | (2) 2M x 36 | (2) 1M x 36 |
| 32MB | (2) 4M x 36 | None |
| 32MB | (2) 2M x 36 | (2) 2M x 36 |
| 40MB | (2) 4M x 36 | (2) 1M x 36 |
| 48MB | (2) 4M x 36 | (2) 2M x 36 |
| 64MB | (2) 8M x 36 | None |
| 64MB | (2) 4M x 36 | (2) 4M x 36 |
| 72MB | (2) 8M x 36 | (2) 1M x 36 |
| 80MB | (2) 8M x 36 | (2) 2M x 36 |
| 96MB | (2) 8M x 36 | (2) 4M x 36 |

| | | |
|---|---|---|
| 128MB | (2) 8M x 36 | (2) 8M x 36 |
| 128MB | (2) 16M x 36 | None |
| 136MB | (2) 16M x 36 | (2) 1M x 36 |
| 144MB | (2) 16M x 36 | (2) 2M x 36 |
| 160MB | (2) 16M x 36 | (2) 4M x 36 |
| 192MB | (2) 16M x 36 | (2) 8M x 36 |
| 256MB | (2) 16M x 36 | (2) 16M x 36 |
| Note: Board accepts EDO memory. Banks are interchangeable. | | |

| DIMM CONFIGURATION | |
|---|---|
| **Size** | **Bank 0** |
| 8MB | (1) 1M x 64 |
| 16MB | (1) 2M x 64 |
| 32MB | (1) 4M x 64 |
| 64MB | (1) 8M x 64 |

| CACHE CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **TAG** |
| 512KB | (2) 64K x 32 | (1) 16K x 8 |

| CPU SPEED SELECTION (CX 6X86/6X 86L) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP10** | **JP11** | **JP12** | **JP13** | **JP14** |
| 120MHz | 50MHz | 2x | 2 & 3 | 2 & 3 | 2 & 3 | Closed | Open |
| 133MHz | 55MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | Closed | Open |
| 150MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | Closed | Open |
| 166MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Open |
| 200MHz | 75MHz | 2x | 1 & 2 | 2 & 3 | 1 & 2 | Closed | Open |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (CX 6X86MX) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP10** | **JP11** | **JP12** | **JP13** | **JP14** |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 2 & 3 | 1 & 2 | Closed | Closed |
| 200MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Closed |
| 233MHz | 66MHz | 3x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Closed |
| 233MHz | 75MHz | 2.5x | 1 & 2 | 2 & 3 | 1 & 2 | Closed | Closed |
| 266MHz | 75MHz | 3x | 1 & 2 | 2 & 3 | 1 & 2 | Open | Closed |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (AM K5) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP10** | **JP11** | **JP12** | **JP13** | **JP14** |
| 75MHz | 50MHz | 1.5x | 2 & 3 | 2 & 3 | 2 & 3 | Open | Open |
| 90MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | 2 & 3 | Open | Open |
| 100MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Open |
| 120MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | 2 & 3 | Open | Open |
| 133MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Open |
| 150MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | Closed | Open |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Closed |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (AM K6) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP10** | **JP11** | **JP12** | **JP13** | **JP14** |
| 150MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | Closed | Open |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Closed |
| 200MHz | 66MHz | 3x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Closed |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (INTEL) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP10** | **JP11** | **JP12** | **JP13** | **JP14** |
| 75MHz | 50MHz | 1.5x | 2 & 3 | 2 & 3 | 2 & 3 | Open | Open |
| 90MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | 2 & 3 | Open | Open |
| 100MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Open |
| 120MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | Closed | Open |
| 133MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Open |
| 150MHz | 60MHz | 2.5x | 1 & 2 | 2 & 3 | 2 & 3 | Closed | Closed |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Closed |

| CPU SPEED SELECTION (INTEL MMX) | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP10** | **JP11** | **JP12** | **JP13** | **JP14** | **JP15** |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | Closed | Closed | N/A |
| 180MHz | 60MHz | 3x | 1 & 2 | 2 & 3 | 2 & 3 | Open | Closed | N/A |
| 200MHz | 66MHz | 3x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Closed | N/A |
| 233MHz | 66MHz | 3.5x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Open | N/A |
| 266MHz | 66MHz | 4x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Closed | Closed |

Note: Pins designated should be in the closed position.

| CPU VOLTAGE SELECTION (SINGLE) | | | | |
|---|---|---|---|---|
| **Voltage** | | **JP30** | **JP31** | **JP32** |
| | 3.3v | Open | 1 & 2, 3 & 4 | 1 & 2 |
| » | 3.52v | Open | 1 & 2, 3 & 4 | 3 & 4 |

Note: Pins designated should be in the closed position.

| CPU VOLTAGE SELECTION (DUAL) | | | | |
|---|---|---|---|---|
| **Voltage** | | **JP30** | **JP31** | **JP32** |
| » | 2.8v | 1 & 2, 3 & 4 | Open | 5 & 6 |
| | 2.9v | 1 & 2, 3 & 4 | Open | 7 & 8 |
| Note: Pins designated should be in the closed position. | | | | |