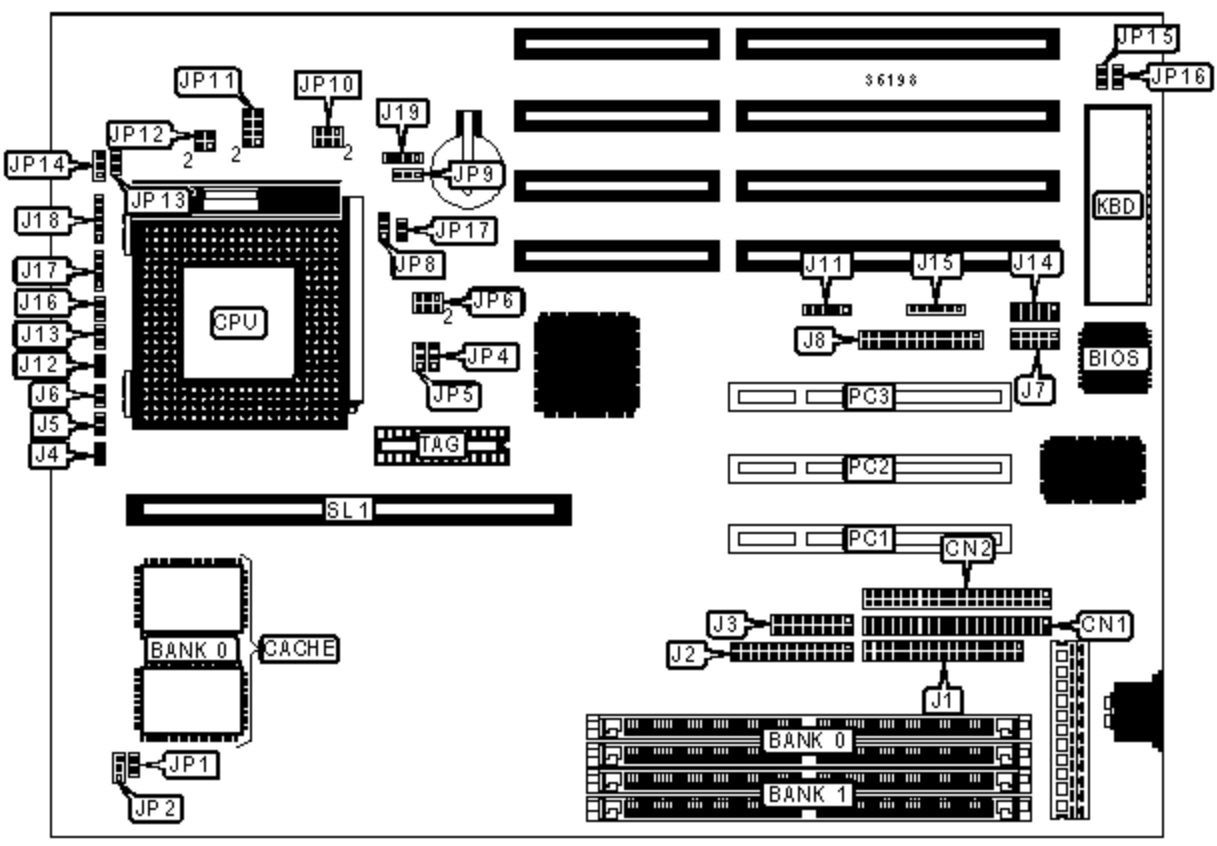


PHILIPS CONSUMER ELECTRONICS, CO.

E522-R2

Device Type	Mainboard
Processor	AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	SIS
Video Chip Set	SIS
Maximum Onboard Memory	256MB (EDO supported) Unified Memory Architecture (UMA)
Cache	256/512KB
BIOS	Award
Dimensions	260mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), VGA feature connector, VGA interface, cache slot, IR connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface 2	CN1	Turbo switch	J12
IDE interface 1	CN2	Green PC connector	J13
Floppy drive interface	J1	Serial port 1	J14
VGA feature connector	J2	PS/2 mouse interface	J15
VGA interface	J3	Reset switch	J16
Green PC LED	J4	Speaker	J17

IDE interface LED	J5	Power LED & keylock	J18
Turbo LED	J6	External battery	J19
Serial port 2	J7	32-bit PCI slots	PC1 - PC3
Parallel port	J8	Cache slot	SL1
IR connector	J11		

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Factory configured - do not alter	JP8	Unidentified
»	CMOS memory normal operation	JP9	Pins 1 & 2 closed
	CMOS memory clear	JP9	Pins 2 & 3 closed
»	Monitor type select color	JP15	Closed
	Monitor type select monochrome	JP15	Open
»	PS/2 mouse enabled	JP16	Closed
	PS/2 mouse disabled	JP16	Open
»	Battery type select internal	JP17	Closed
	Battery type select external	JP17	Open

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36

40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36

SIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION			
Size	Bank 0	SL1	TAG
256KB	(2) 32K x 32	Not installed	Unidentified
512KB (A)	(2) 64K x 32	Not installed	Unidentified
512KB (B)	(2) 32K x 32	256KB module installed	Unidentified

CACHE JUMPER CONFIGURATION			
Size	JP2	JP4	JP5

256KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
512KB (B)	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JP1	JP6/pins 1 & 2	JP6/pins 3 & 4
75MHz	50MHz	1.5x	Open	Open	Closed
90MHz	60MHz	1.5x	Open	Closed	Open
100MHz	66MHz	1.5x	Open	Open	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5, CON'T)

CPU speed	Clock speed	Multiplier	JP6/pins 5 & 6	JP13	JP14
75MHz	50MHz	1.5x	Open	Open	2 & 3
90MHz	60MHz	1.5x	Open	Open	2 & 3
100MHz	66MHz	1.5x	Open	Open	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP1	JP6/pins 1 & 2	JP6/pins 3 & 4
75MHz	50MHz	1.5x	Open	Open	Closed
90MHz	60MHz	1.5x	Open	Closed	Open
100MHz	66MHz	1.5x	Open	Open	Open
120MHz	60MHz	2x	Open	Closed	Open
133MHz	66MHz	2x	Open	Open	Open
150MHz	60MHz	2.5x	Open	Closed	Open
166MHz	66MHz	2.5x	Open	Open	Open
200MHz	66MHz	3x	Open	Open	Open

200MHz

66MHz

3x

Open

Open

Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL, CON'T)

CPU speed	Clock speed	Multiplier	JP6/pins 5 & 6	JP13	JP14
75MHz	50MHz	1.5x	Open	Open	2 & 3
90MHz	60MHz	1.5x	Open	Open	2 & 3
100MHz	66MHz	1.5x	Open	Open	2 & 3
120MHz	60MHz	2x	Open	Open	1 & 2
133MHz	66MHz	2x	Open	Open	1 & 2
150MHz	60MHz	2.5x	Open	Closed	1 & 2
166MHz	66MHz	2.5x	Open	Closed	1 & 2
200MHz	66MHz	3x	Open	Closed	2 & 3

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION

Voltage	JP10	JP11	JP12
2.7v	1 & 2	1 & 2	1 & 2
2.8v	3 & 4	1 & 2	1 & 2
2.9v	5 & 6	1 & 2	1 & 2
3.3v	Open	3 & 4, 5 & 6, 7 & 8	1 & 2
3.5v	Open	3 & 4, 5 & 6, 7 & 8	3 & 4

Note: Pins designated should be in the closed position.