PHILIPS CONSUMER ELECTRONICS, CO.

E532-A SYSTEM

Device Type	Mainboard
Processor	CX 6X86/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	SIS
Maximum Onboard Memory	256MB (EDO supported)
Cache	256/512KB
BIOS	Award
Dimensions	250mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector
NPU Options	None
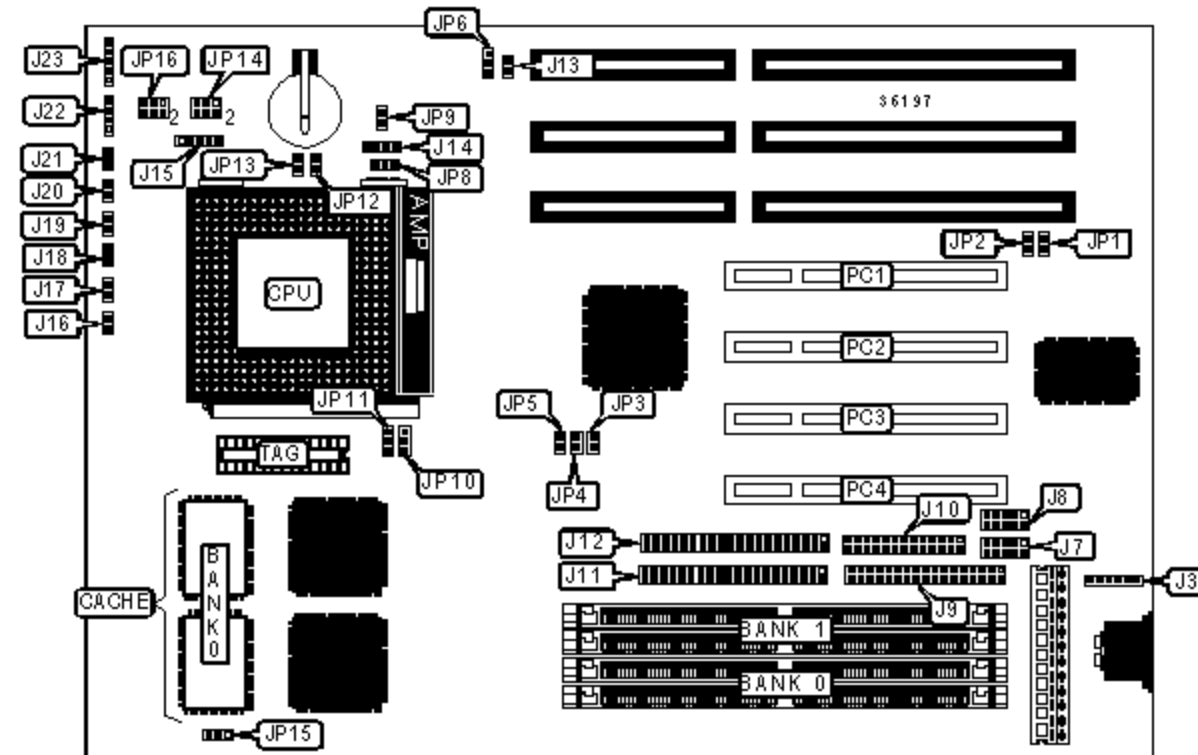
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
PS/2 mouse interface	J3	IDE interface LED	J16
Serial port 1	J7	Green PC connector	J17
Serial port 2	J8	Green PC LED	J18
Floppy drive interface	J9	Turbo LED	J19
Parallel port	J10	Turbo switch	J20
IDE interface 1	J11	Reset switch	J21
IDE interface 2	J12	Speaker	J22
Chassis fan power	J13	Power LED & keylock	J23

Chassis fan power	J13	Power LED & keylock	J23
External battery	J14	32-bit PCI slots	PC1 - PC4
IR connector	J15		

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
»	Monitor type select color	JP1	Closed
	Monitor type select monochrome	JP1	Open
»	PS/2 mouse enabled	JP2	Closed
	PS/2 mouse disabled	JP2	Open
»	CPU bus clock select /2	JP5	Open
	CPU bus clock select 32MHz	JP5	Closed
»	Factory configured - do not alter (29F010/29EE010)	JP6	Pins 1 & 2 closed
»	Factory configured - do not alter (28F010)	JP6	Pins 2 & 3 closed
»	CMOS memory normal operation	JP8	Pins 1 & 2 closed
	CMOS memory clear	JP8	Pins 2 & 3 closed
»	Battery type select external	JP9	Open
	Battery type select internal	JP9	Closed
»	Cache type select write back	JP11	Pins 1 & 2 closed
	Cache type select write through	JP11	Pins 2 & 3 closed

SIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36

24MB	(2) 2M x 36	(2) 1M x 36

SIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(2) 32K x 32	(1) 8K x 8
512KB	(2) 64K x 32	(1) 16K/32K x 8

CACHE JUMPER CONFIGURATION	
Size	**JP10**
256KB	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed

CPU SPEED SELECTION (CYRIX)						
CPU speed	**Clock speed**	**Multiplier**	**JP3**	**JP4**	**JP12**	**JP13**
120MHz	50MHz	2x	Closed	Closed	Closed	Open
133MHz	55MHz	2x	Open	Open	Closed	Open
150MHz	60MHz	2x	Open	Closed	Closed	Open
166MHz	66MHz	2x	Closed	Open	Closed	Open

CPU SPEED SELECTION (CYRIX, CON'T)					
CPU speed	**Clock speed**	**Multiplier**	**JP14**	**JP15**	**JP16**
120MHz	50MHz	2x	1 & 2	2 & 3	3 & 5, 4 & 6
133MHz	55MHz	2x	1 & 2	2 & 3	3 & 5, 4 & 6
150MHz	60MHz	2x	1 & 2	2 & 3	3 & 5, 4 & 6
166MHz	66MHz	2x	1 & 2	2 & 3	3 & 5, 4 & 6
Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (INTEL)						
CPU speed	**Clock speed**	**Multiplier**	**JP3**	**JP4**	**JP12**	**JP13**
75MHz	50MHz	1.5x	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	Open	Closed	Open	Open
100MHz	66MHz	1.5x	Closed	Open	Open	Open
120MHz	60MHz	2x	Open	Closed	Closed	Open
133MHz	66MHz	2x	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	Open	Closed	Closed	Closed

CPU speed	Clock speed	Multiplier				
150MHz	60MHz	2.5x	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	Closed	Open	Closed	Closed
180MHz	60MHz	3x	Open	Closed	Open	Closed
200MHz	66MHz	3x	Closed	Open	Open	Closed

CPU SPEED SELECTION (INTEL, CON'T)					
CPU speed	Clock speed	Multiplier	JP14	JP15	JP16
75MHz	50MHz	1.5x	1 & 2	1 & 2	3 & 5, 4 & 6
90MHz	60MHz	1.5x	1 & 2	1 & 2	3 & 5, 4 & 6
100MHz	66MHz	1.5x	1 & 2	1 & 2	3 & 5, 4 & 6
120MHz	60MHz	2x	1 & 2	1 & 2	3 & 5, 4 & 6
133MHz	66MHz	2x	1 & 2	1 & 2	3 & 5, 4 & 6
150MHz	60MHz	2.5x	1 & 2	1 & 2	3 & 5, 4 & 6
166MHz	66MHz	2.5x	1 & 2	1 & 2	3 & 5, 4 & 6
180MHz	60MHz	3x	1 & 2	1 & 2	3 & 5, 4 & 6
200MHz	66MHz	3x	1 & 2	1 & 2	3 & 5, 4 & 6

Note: Pins designated should be in the closed position.