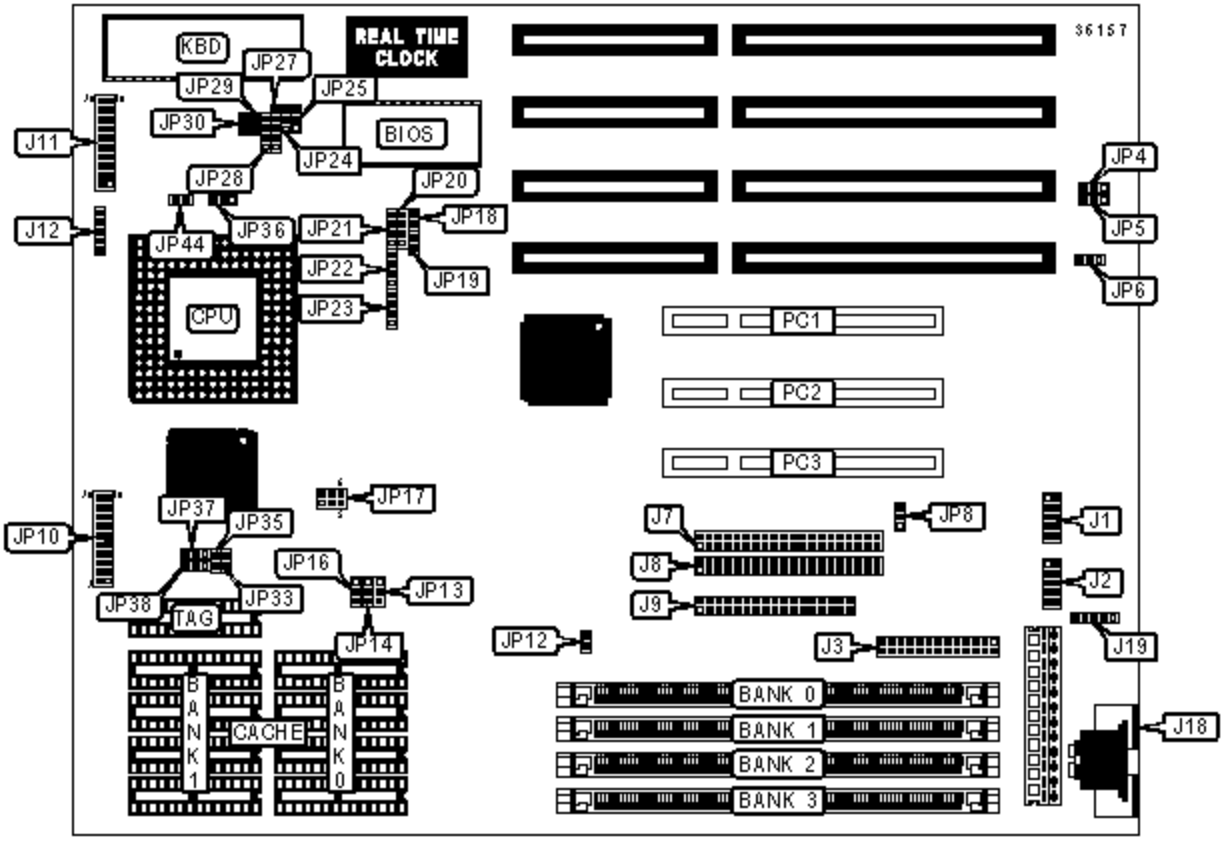


ELITEGROUP COMPUTER SYSTEMS, INC.

UM8810P-AIO (REV.3.0)

Device Type	Mainboard
Processor	80487SX/CX486S/UMC486(U5S, U5SD)/80486SX/80486SX2/ AM486DX/CX486DX/80486DX/CX486DX4(M7)/AM486DX2/ 80486DX2/CX486DX2/AM486DX2/80486DX2/CX486DX4/AM486DX4/ 80486DX4/P24T/CX 5X86
Processor Speed	25/33/40/50/66/80/100/120
Chip Set	UMC
Maximum Onboard Memory	128MB (EDO & SDRAM supported)
Cache	128/256/512KB
BIOS	Phoenix
Dimensions	Unidentified
I/O Options (backplane)	32-bit PCI slots (3), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 mouse interface (optional), serial ports (2)



CONNECTIONS

Purpose	Location	Purpose	Location
Serial port 2	J1	Turbo switch	J11/pins 6 & 7
Serial port 1	J2	Reset switch	J11/pins 9 & 10
Parallel port	J3	Power LED & keylock	J11/pins 11 - 15
IDE interface 1	J7	Speak connector	J11/pins 17 - 20
IDE interface 2	J8	HDD LED	J12
Floppy controller interface	J9	PS/2 mouse port	J18

Turbo LED	J11/pins2 & 3	PS/2 mouse interface (optional)	J19
Suspend switch	J11/pins 4 & 5		

USER CONFIGURABLE SETTINGS

Function		Jumper	Position
	BIOS type select EPROM	JP6	Open
	BIOS type select flash ROM 5v	JP6	pins 1 & 2 closed
	BIOS type select flash ROM 12v	JP6	pins 2 & 3 closed
»	On board 37C665 enabled	JP8	pins 1 & 2 closed
	On board 37C665 disabled	JP8	pins 2 & 3 closed
»	IDE controller enabled and controlled by BIOS	JP12	Closed
	IDE controller enabled	JP12	Open
»	CMOS memory normal operation	JP30	Open
	CMOS memory clear	JP30	Closed
»	Standard CPU type	JP44	Open
	Pentium Overdrive CPU	JP44	Closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 512K x 36	None	None	None
2MB	(1) 256K x 36	(1) 256K x 36	None	None
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	None
4MB	(1) 1M x 36	None	None	None
4MB	(1) 512K x 36	(1) 512K x 36	None	None
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
6MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	None

8MB	(1) 2M x 36	None	None	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36
10MB	(1) 1M x 36	(1) 1M x 36	(1) 512K x 36	None
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
16MB	(1) 4M x 36	None	None	None
16MB	(1) 2M x 36	(1) 2M x 36	None	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
64MB	(1) 8M x 36	(1) 8M x 36	None	None
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	None
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36

CPU TYPE CONFIGURATION

Type	JP18	JP19	JP20	JP21	JP22	JP23
CX486S	Open	Closed	2 & 3	Open	2 & 3	2 & 3
SL80486SX	Open	Open	2 & 3	Open	1 & 2	Open
80486SX	Open	Open	2 & 3	Open	Open	Open
80487SX	Open	Open	1 & 2, 3 & 4	2 & 3	Open	Open
SL80486SX2	Open	Open	2 & 3	Open	1 & 2	Open
AM486DX	Open	Open	1 & 2, 3 & 4	1 & 2	Open	Open
CX486DX	Open	Closed	1 & 2, 3 & 4	1 & 2	2 & 3	2 & 3
SL80486DX-OD	Open	Open	1 & 2, 3 & 4	1 & 2	1 & 2	Open
80486DX	Open	Open	1 & 2, 3 & 4	1 & 2	Open	Open

AM486DX2	Open	Open	1 & 2, 3 & 4	1 & 2	Open	Open
AM486DX2-SV8B	Closed	Open	1 & 2, 3 & 4	1 & 2	1 & 2, 3 & 4	Open
CX486DX2	Open	Closed	1 & 2, 3 & 4	1 & 2	2 & 3	2 & 3
SL80486DX2	Closed	Open	1 & 2, 3 & 4	1 & 2	1 & 2, 3 & 4	Open
SL80486DX2-OD	Open	Open	1 & 2, 3 & 4	1 & 2	1 & 2	Open
80486DX2	Open	Open	1 & 2, 3 & 4	1 & 2	Open	Open
CX486DX4	Open	Closed	1 & 2, 3 & 4	1 & 2	2 & 3	2 & 3
CX486DX4-100GP	Open	Closed	1 & 2, 3 & 4	1 & 2	2 & 3	2 & 3
CX486DX4-100GP4	Closed	Open*	1 & 2, 3 & 4	1 & 2*	1 & 2, 3 & 4	Open
U5S	Open	Open	2 & 3	1 & 2, 3 & 4	Open	Open
U5SD	Open	Open	1 & 2, 3 & 4	1 & 2, 3 & 4	Open	Open
SL80486DX4	Closed	Open	1 & 2, 3 & 4	1 & 2	1 & 2, 3 & 4	Open
SL80486DX4-OD	Open	Open	1 & 2, 3 & 4	1 & 2	1 & 2	Open
AM486DX4	Open	Open	1 & 2, 3 & 4	1 & 2	Open	Open
AM486DX4-SV8B	Closed	Open	1 & 2, 3 & 4	1 & 2	1 & 2, 3 & 4	Open
CX5x86	Closed	Open	1 & 2, 3 & 4	1 & 2	1 & 2, 3 & 4	Open
P24T	Open	Open	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

*Note: Pin JP19/2 and pin JP21/3 are connected with naked wire for CX486DX4-100GP4 setting.

CPU TYPE CONFIGURATION (CON'T)

Type	JP24	JP27	JP28	JP36	JP37	JP38
CX486S	2 & 3	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2	2 & 3
SL80486SX	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
80486SX	1 & 2	1 & 2	Open	Open	1 & 2	1 & 2
80487SX	1 & 2	Open	Open	Open	1 & 2	1 & 2
SL80486SX2	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
AM486DX	1 & 2	Open	Open	Open	1 & 2	1 & 2

CX486DX	2 & 3	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2	2 & 3
SL80486DX-OD	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
80486DX	1 & 2	Open	Open	Open	1 & 2	1 & 2
AM486DX2	1 & 2	Open	Open	Open	1 & 2	1 & 2
AM486DX2-SV8B	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
CX486DX2	2 & 3	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2	2 & 3
SL80486DX2	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
SL80486DX2-OD	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
80486DX2	1 & 2	Open	Open	Open	1 & 2	1 & 2
CX486DX4	2 & 3	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2	2 & 3
CX486DX4-100GP	2 & 3	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2	2 & 3
CX486DX4-100GP4	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	2 & 3
U5S	1 & 2	Open	3 & 4	2 & 3	2 & 3	1 & 2
U5SD	1 & 2	Open	3 & 4	2 & 3	2 & 3	1 & 2
SL80486DX4	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
SL80486DX4-OD	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
AM486DX4	1 & 2	Open	Open	Open	1 & 2	1 & 2
AM486DX4-SV8B	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
CX5x86	2 & 3	2 & 3, 4 & 5	Open	1 & 2	1 & 2	1 & 2
P24T	2 & 3	2 & 3, 4 & 5	2 & 3	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION (CON'T)

Type	JP25
CX486DX	Pins 1 & 2 closed
CX486DX2	Pins 1 & 2 closed
CX486DX4	Pins 1 & 2 closed
CX486DX4-100GP	Pins 1 & 2 closed

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION

Size	JP13	JP14	JP16	JP33	JP35
128KB	Open	1 & 2	2 & 3	Open	Open
256KB	Open	2 & 3	1 & 2	Open	Closed
512KB	Open	2 & 3	1 & 2	Open	Closed
512KB	1 & 2	2 & 3	2 & 3	Closed	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION

Speed	JP17
25MHz	pins 1 & 2 closed
33MHz	pins 1 & 2, 3 & 4, 5 & 6 closed
40MHz	pins 1 & 2, 3 & 4 closed
50MHz	pins 5 & 6 closed

CPU CLOCK CONFIGURATION (80486DX4, AM486DX2/DX4 SV8B)

Speed	JP29
2x	Closed

3x

Open

CPU CLOCK CONFIGURATION (AM486DX2/DX4 NV8T)

Speed

JP25

2x

Closed

3x

Open

CPU VOLTAGE CONFIGURATION

Voltage

JP10

3.3V/3.45V/3.6V/4V

VR-100 or VR-102 installed

5.0V

1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12, 13 & 14, 15 & 16, 17 & 18, 19 & 20

Note: Pins indicated are in the closed position

PARALLEL PORT DACK CONFIGURATION

Size

JP4

JP5

DRQ3/DACK 3

pins 1 & 2 closed

pins 2 & 3 closed

DRQ1/DACK 1

pins 2 & 3 closed

pins 1 & 2 closed