DIAMOND FLOWER, INC.

G586SP (REV.0+)

Device Type Processor Processor Speed Chip Set Maximum Onboard Memory Cache BIOS Dimensions I/O Options (backplane) Mainboard Pentium 75/90/100/120/133/150MHz SIS 256MB (EDO & SDRAM supported) 256/512/1024KB Award 280mm x 220mm 32-bit PCI slots (4), CD-ROM interface, floppy drive interface, green PC connector, IDE interfaces (2), PS/2 mouse port, cache slot, IR connector,



CONNECTIONS			
Purpose Location Purpose Location			
VRM connector	CN1	Cache module slot	SL1
IR connector	J16	32-bit PCI slots	PC1 - PC

	USER CONFIGURABLE SETTINGS			
	Function	Label	Position	
»	Factory configured - do not alter	J3	Unidentified	

»	Factory configured - do not alter	J4	Unidentified
»	Factory configured - do not alter	J5	Unidentified
»	Factory configured - do not alter	J6	Unidentified
»	Factory configured - do not alter	J7	Unidentified
»	Factory configured - do not alter	J8	Unidentified
»	Factory configured - do not alter	J9	Unidentified
»	Factory configured - do not alter	J11	Unidentified
»	Factory configured - do not alter	J14	Unidentified
»	Factory configured - do not alter	J17	Unidentified
»	Factory configured - do not alter	J18	Unidentified
»	Factory configured - do not alter	J19	Unidentified
»	Factory configured - do not alter	J20	Unidentified
»	Factory configured - do not alter	J21	Unidentified
»	Factory configured - do not alter	J22	Unidentified
»	Factory configured - do not alter	J23	Unidentified
»	Factory configured - do not alter	J24	Unidentified
»	Monitor type select color	JP2	Closed
	Monitor type select monochrome	JP2	Open
»	PS/2 mouse enabled	JP3	Closed
	PS/2 mouse disabled	JP3	Open
»	PCI bus clock select CPU bus clock/2	JP6	Open
	PCI bus clock select 32MHz	JP6	Closed
»	Battery type select external	JP9	Open
	Battery type select internal	JP9	Closed
»	Password enabled	JP12	Pins 1 & 2 closed
	CMOS clear enabled	JP12	Pins 2 & 3 closed

»	Flash BIOS voltage select 5v	JP13	Pins 1 & 2 closed
	Flash BIOS voltage select 12v	JP13	Pins 2 & 3 closed
»	Cache type select burst/pipelined burst	JP14	Pins 2 & 3 closed
	Cache type select asynchronous	JP14	Pins 1 & 2 closed
»	CPU voltage type select VRE	JP16	Pins 3 & 4 closed
	CPU voltage type select STD/VR	JP16	Pins 1 & 2 closed
»	Factory configured - do not alter	JP15	Reserved
»	Cache mode write-back enabled	JP18	Pins 1 & 2 closed
	Cache mode write-through enabled	JP18	Pins 2 & 3 closed
»	Factory configured - do not alter	JP19	Unidentified

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SIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	None
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None

136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION			
Size	Bank 0	SL1	TAG
256KB	(8) 32K x 8	Not installed	(1) 8K x 8
512KB	(8) 64K x 8	Not installed	(8) 32K x 8
1MB	(8) 128K x 8	Installed	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP10	JP11	
256K	Pins 1 & 2 closed	Pins 1 & 2 closed	
512K	Pins 1 & 2 closed	Pins 2 & 3 closed	
1MB	Pins 2 & 3 closed	Pins 2 & 3 closed	

CPU SPEED SELECTION			
Speed	JP7	JP8	
50MHz	Closed	Closed	
60MHz	Closed	Open	
66MHz	Open	Closed	

CPU TYPE SELECTION		
Type JP17		
Pentium 3.3v	Closed	
Future Pentium Pins 1 & 2, 3 & 4 close		

CPU MULTIPLIER SELECTION			
Multiplier	JP20	JP21	
1.5x	Open	Open	
2x	Open	Closed	
2.5x	Closed	Closed	
3x	Closed	Open	