ZIDA TECHNOLOGIES INC. 5SVA (VER. 1.60), 5SVA-E (VER. 1.60)

Device Type Mainboard

Processor CX 6X86/IBM 6X86/CX 6X86MX/IBM 6X86MX/AM K5/

AM K6/Pentium/Pentium MMX

Processor Speed 90/100/120/133/150/166/180/200/233/266/300/333MHz

Chip Set VIA VPX Video Chip Set None

Maximum Onboard Memory 256MB (EDO & SDRAM supported)

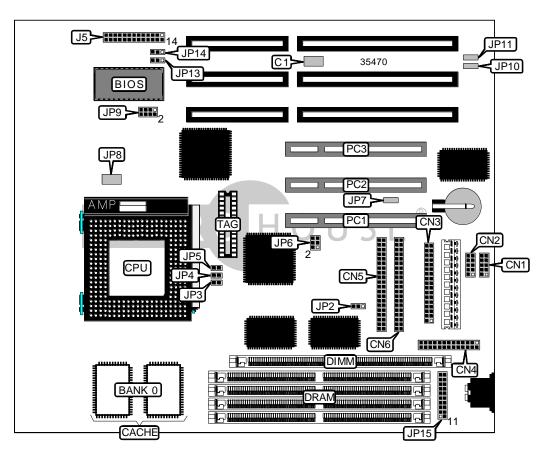
Maximum Video MemoryNoneCache256/512KBBIOSUnidentifiedDimensions254mm x 218mm

I/O Options 32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces

(2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB

connectors (2)

NPU Options None



ZIDA TECHNOLOGIES INC. 5 S V A (VER. 1.60), 5 S V A - E (VER. 1.60)

. . . continued from previous page

CONNECTIONS										
Purpose	Location	Purpose	Location							
Serial port 1	CN1	IDE interface LED	J5/pins 14 & 15							
Serial port 2	CN2	Reset switch	J5/pins 22 & 23							
Floppy drive interface	CN3	Turbo LED	J5/pins 25 & 26							
Parallel port	CN4	USB connector 1	JP15/pins 1 - 5							
IDE interface	CN5	PS/2 mouse interface	JP15/pins 6 – 10							
IDE interface	CN6	USB connector 2	JP15/pins 11 – 15							
Power LED & keylock	J5/pins 1 – 5	IR connector	JP15/pins 16 - 20							
Green PC connector	J5/pins 7 & 8	32-bit PCI slots	PC1 – PC3							
Speaker	J5/pins 10 – 13									

	USER CONFIGURABLE SETTINGS									
Function Label Position										
í Fact	ory configured - do not alter	C1	Unidentified							
í Fact	ory configured - do not alter	JP7	Unidentified							
í Fact	ory configured - do not alter	JP10	Unidentified							
í Fact	ory configured - do not alter	JP11	Unidentified							
í CMC	OS memory normal operation	JP14	Pins 1 & 2 closed							
CMC	OS memory clear	JP14	Pins 2 & 3 closed							

SIMM CONFIGURATION									
Size	Bank 0	Bank 1							
8MB	(2) 1M x 36	None							
16MB	(2) 2M x 36	None							
16MB	(2) 1M x 36	(2) 1M x 36							
24MB	(2) 2M x 36	(2) 1M x 36							
32MB	(2) 4M x 36	None							
32MB	(2) 2M x 36	(2) 2M x 36							
40MB	(2) 4M x 36	(2) 1M x 36							
48MB	(2) 4M x 36	(2) 2M x 36							
64MB	(2) 8M x 36	None							
64MB	(2) 4M x 36	(2) 4M x 36							
72MB	(2) 8M x 36	(2) 1M x 36							
80MB	(2) 8M x 36	(2) 2M x 36							
96MB	(2) 8M x 36	(2) 4M x 36							
128MB	(2) 8M x 36	(2) 8M x 36							
Note: Board accepts EDO memory.									

Continued on next page. . .

ZIDA TECHNOLOGIES INC. 5SVA (VER. 1.60), 5SVA-E (VER. 1.60)

. . . continued from previous page

DIMM CONFIGURATION								
Size	Bank 2							
8MB	(1) 1M x 64							
16MB	(1) 2M x 64							
32MB	(1) 4M x 64							
64MB	(1) 8M x 64							
128MB	(1) 16M x 64							
256MB	(1) 32M x 64							
Note: Board accepts EDO & SDRAM memory.								

	CACHE CONFIGURATION	
Size	Bank 0	TAG
256KB	(2) 32K x 32	Unidentified
512KB	(2) 64K x 32	Unidentified

CPU SPEED SELECTION (CX 6X86)										
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6			
133MHz	55MHz	2x	1 & 2	Open	Closed	Open	3 & 5, 4 & 6			
150MHz	60MHz	2x	2 & 3	Open	Closed	Open	1 & 3, 4 & 6			
166MHz	66MHz	2x	2 & 3	Open	Closed	Open	2 & 4, 3 & 5			
200MHz	75MHz	2x	1 & 2	Open	Closed	Open	1 & 3, 4 & 6			
166MHz	60MHz	2.5x	2 & 3	Closed	Closed	Open	1 & 3, 4 & 6			
Note: Pins desig	Note: Pins designated should be in the closed position.									

CPU SPEED SELECTION (IBM 6X86)										
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6			
133MHz	55MHz	2x	1 & 2	Open	Closed	Open	3 & 5, 4 & 6			
150MHz	60MHz	2x	2 & 3	Open	Closed	Open	1 & 3, 4 & 6			
166MHz	66MHz	2x	2 & 3	Open	Closed	Open	2 & 4, 3 & 5			
200MHz	75MHz	2x	1 & 2	Open	Closed	Open	1 & 3, 4 & 6			
166MHz	60MHz	2.5x	2 & 3	Closed	Closed	Open	1 & 3, 4 & 6			
Note: Pins desig	nated should be ir	the closed posit	ion.			•				

Continued on next page. . .

ZIDA TECHNOLOGIES INC.

5SVA (VER. 1.60), 5SVA-E (VER. 1.60)

. . . continued from previous page

	CPU SPEED SELECTION (CX 6X86MX)										
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6				
200MHz	75MHz	2x	1 & 2	Open	Closed	Open	1 & 3, 4 & 6				
233MHz	75MHz	3x	1 & 2	Closed	Closed	Open	1 & 3, 4 & 6				
300MHz	66MHz	3.5x	2 & 3	Open	Open	Open	2 & 4, 3 & 5				
300MHz	75MHz	3x	1 & 2	Closed	Open	Open	1 & 3, 4 & 6				
333MHz	75MHz	3.5x	1 & 2	Open	Open	Open	1 & 3, 4 & 6				
Note: Pins desig	nated should be ir	the closed posit	ion.								

CPU SPEED SELECTION (IBM 6X86MX)										
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6			
200MHz	75MHz	2x	1 & 2	Open	Closed	Open	1 & 3, 4 & 6			
233MHz	75MHz	3x	1 & 2	Closed	Closed	Open	1 & 3, 4 & 6			
300MHz	66MHz	3.5x	2 & 3	Open	Open	Open	2 & 4, 3 & 5			
300MHz	75MHz	3x	1 & 2	Closed	Open	Open	1 & 3, 4 & 6			
333MHz	75MHz	3.5x	1 & 2	Open	Open	Open	1 & 3, 4 & 6			
Note: Pins desig	gnated should be in	the closed posit	ion.							

CPU SPEED SELECTION (AM K5)										
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6			
90MHz	60MHz	1.5x	2 & 3	Open	Open	Closed	1 & 3, 4 & 6			
100MHz	66MHz	1.5x	2 & 3	Open	Open	Closed	2 & 4, 3 & 5			
133MHz	66MHz	1.5x	2 & 3	Open	Open	Closed	2 & 4, 3 & 5			
150MHz	60MHz	1.75x	2 & 3	Closed	Closed	Open	1 & 3, 4 & 6			
166MHz	66MHz	1.75x	2 & 3	Closed	Closed	Open	2 & 4, 3 & 5			
Note: Pins desig	Note: Pins designated should be in the closed position.									

	CPU SPEED SELECTION (AM K6)									
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6			
166MHz	66MHz	2.5x	2 & 3	Closed	Closed	Open	2 & 4, 3 & 5			
200MHz	66MHz	3x	2 & 3	Closed	Open	Open	2 & 4, 3 & 5			
233MHz	66MHz	3.5x	2 & 3	Open	Open	Open	2 & 4, 3 & 5			
266MHz	66MHz	4x	2 & 3	Open	Closed	Closed	2 & 4, 3 & 5			
300MHz	66MHz	4.5x	2 & 3	Closed	Closed	Closed	2 & 4, 3 & 5			
333MHz	66MHz	5x	2 & 3	Closed	Open	Closed	2 & 4, 3 & 5			
Note: Pins desig	nated should be in	Note: Pins designated should be in the closed position.								

Continued on next page. . .

ZIDA TECHNOLOGIES INC. 5 S V A (VER. 1.60), 5 S V A - E (VER. 1.60)

. . . continued from previous page

	CPU SPEED SELECTION (INTEL)									
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6			
90MHz	60MHz	1.5x	2 & 3	Open	Open	Closed	1 & 3, 4 & 6			
100MHz	66MHz	1.5x	2 & 3	Open	Open	Closed	2 & 4, 3 & 5			
120MHz	60MHz	2x	2 & 3	Open	Closed	Open	1 & 3, 4 & 6			
133MHz	66MHz	2x	2 & 3	Open	Closed	Open	2 & 4, 3 & 5			
150MHz	60MHz	2.5x	2 & 3	Closed	Closed	Open	1 & 3, 4 & 6			
166MHz	66MHz	2.5x	2 & 3	Closed	Closed	Open	2 & 4, 3 & 5			
180MHz	60MHz	3x	2 & 3	Closed	Open	Open	1 & 3, 4 & 6			
200MHz	66MHz	3x	2 & 3	Closed	Open	Open	2 & 4, 3 & 5			
Note: Pins desig	nated should be in	the closed posit	ion.							

CPU SPEED SELECTION (INTEL MMX)									
CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5	JP6		
166MHz	66MHz	2.5x	2 & 3	Closed	Closed	Open	2 & 4, 3 & 5		
200MHz	66MHz	3x	2 & 3	Closed	Open	Open	2 & 4, 3 & 5		
233MHz	66MHz	3.5x	2 & 3	Open	Open	Open	2 & 4, 3 & 5		
Note: Pins designated should be in the closed position.									

CPU VOLTAGE SELECTION (SINGLE)						
Voltage	JP8	JP9	JP13			
3.5v	Open	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed	Open			

CPU VOLTAGE SELECTION (DUAL)								
Voltage	V core	JP8	JP9	JP13				
3.3v	2.2v	Open	Pins 3 & 4 closed	Pins 1 & 2 closed				
3.3v	2.8v	Open	Pins 7 & 8 closed	Pins 1 & 2 closed				
3.3v	2.9v	Open	Pins 1 & 2, 7 & 8 closed	Pins 1 & 2 closed				
3.3v	3.2v	Open	Pins 5 & 6, 7 & 8 closed	Pins 1 & 2 closed				