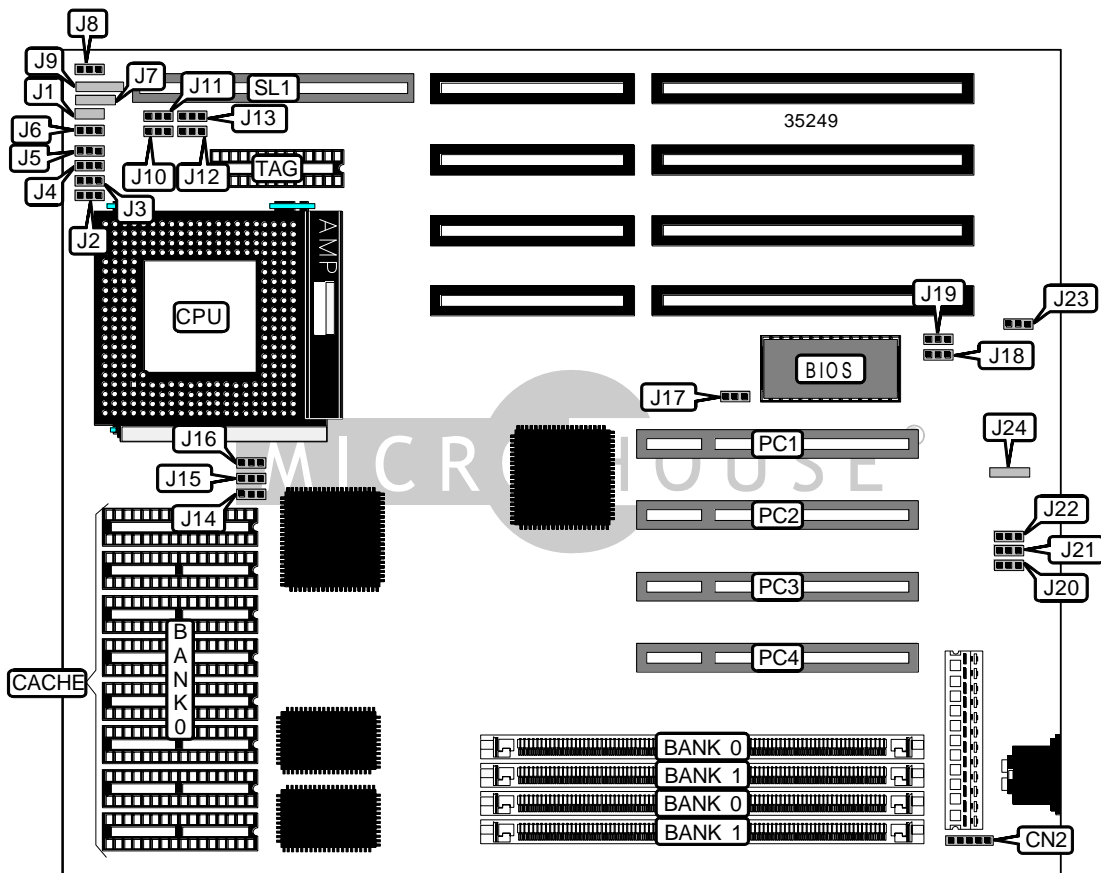


DIAMOND FLOWER, INC.
G586VPA (REV. 0)

Device Type	Mainboard
Processor	Pentium
Processor Speed	75/90/100MHz
Chip Set	VIA
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	1MB
Cache	256/512/1024KB
BIOS	Award
Dimensions	270mm x 220mm
I/O Options	32-bit VESA local bus slot, 32-bit PCI slots (4), PS/2 mouse interface
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse interface	CN2	32-bit VESA local bus slot	SL1
32-bit PCI slots	PC1 – PC4		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	J1	Unidentified
í Factory configured - do not alter	J4	Pins 2 & 3 closed
í Factory configured - do not alter	J5	Pins 2 & 3 closed
í Factory configured - do not alter	J6	Pins 1 & 2 closed
í Factory configured - do not alter	J7	Unidentified
í Factory configured - do not alter	J14	Pins 1 & 2 closed
í PCI delay select normal	J17	Pins 1 & 2 closed
PCI delay select 5ns delay	J17	Pins 2 & 3 closed
í PS/2 mouse disabled	J18	Pins 2 & 3 closed
PS/2 mouse enabled	J18	Pins 1 & 2 closed
í Flash BIOS voltage select 12v	J19	Pins 1 & 2 closed
Flash BIOS voltage select 5v	J19	Pins 2 & 3 closed
í CMOS memory normal operation	J23	Pins 1 & 2 closed
CMOS memory clear	J23	Pins 2 & 3 closed
í Factory configured - do not alter	J24	Unidentified

SIMM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	None
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 512K x 36	(2) 256K x 36
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36

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SIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(8) 32K x 8	(1) 32K x 8
512KB	(8) 64K x 8	(1) 32K x 8
1MB	(8) 128K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	J10	J11	J12	J13
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
512KB	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
1MB	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

CACHE VOLTAGE SELECTION		
Voltage	J15	J16
í 3.3v	Pins 1 & 2 closed	Pins 1 & 2 closed
Mixed voltage	Pins 2 & 3 closed	Pins 2 & 3 closed

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CPU SPEED SELECTION							
CPU speed	Clock speed	Multiplier	J2	J3	J20	J21	J22
75MHz (WB)	50MHz	1.5x	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
75MHz (WT)	50MHz	1.5x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
90MHz (WB)	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
90MHz (WT)	60MHz	1.5x	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
100MHz (WT)	66MHz	1.5x	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
100MHz (WT)	66MHz	1.5x	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

VL BUS WAIT STATE SELECTION	
Setting	J8
0	Pins 2 & 3 closed
1	Pins 1 & 2 closed