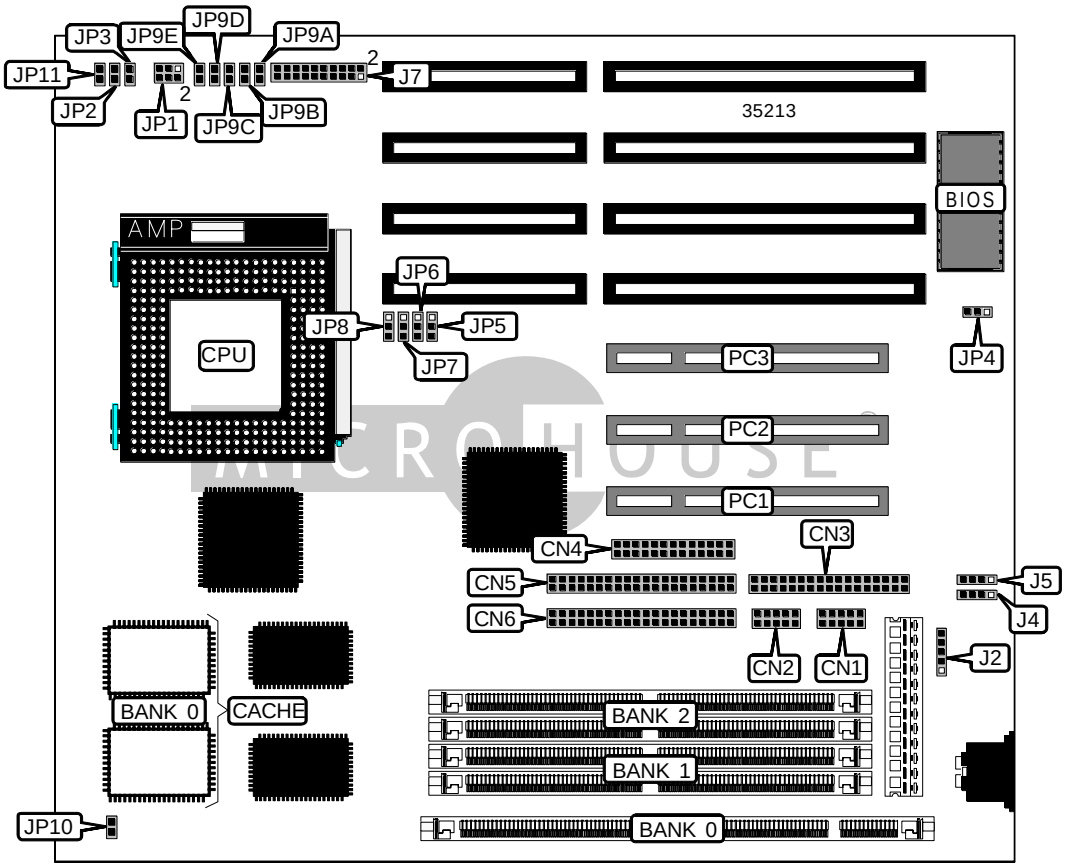
EFA CORPORATION
P5V580-AT-C

Device Type	Mainboard
Processor	CX 6X86/CX 6X86L/CX 686MX/AM K5/AM K6/Pentium/Pentium MMX
Processor Speed	75/90/100/120/133/150/166/200/233MHz
Chip Set	VIA
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	230mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Serial port 1	CN1	Power LED & keylock	J7/pins 1/3/5/7/9
Serial port 2	CN2	Speaker	J7/pins 2/4/6/8
Floppy drive interface	CN3	Turbo LED	J7/pins 11 & 12
Parallel port	CN4	Green PC connector	J7/pins 13 & 14
IDE interface 2	CN5	IDE interface LED	J7/pins 15 & 16
IDE interface 1	CN6	Reset switch	J7/pins 17 & 18
PS/2 mouse interface	J2	32-bit PCI slots	PC1 – PC3
IR connector	J6		
Note: The location of J6 is unidentified.			

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	J4	Unidentified
í Factory configured - do not alter	J5	Unidentified
í Factory configured - do not alter (CMOS memory)	JP4	Unidentified
í Factory configured - do not alter	JP11	Unidentified

SIMM CONFIGURATION		
Size	**Bank 1**	**Bank 2**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36

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SIMM CONFIGURATION (CON'T)		
Size	**Bank 1**	**Bank 2**
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

DIMM CONFIGURATION	
Size	**Bank 0**
8MB	(1) 1M x 64
16MB	(1) 2M x 64
32MB	(1) 4M x 64
64MB	(1) 8M x 64

CACHE CONFIGURATION	
Size	**Bank 0**
256KB	(2) 32K x 32
512KB	(2) 64K x 32

CPU SPEED SELECTION (CX 6X86)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
120MHz	50MHz	2x	Open	Closed	2 & 3	2 & 3	2 & 3
133MHz	55MHz	2x	Open	Closed	1 & 2	2 & 3	2 & 3
150MHz	60MHz	2x	Open	Closed	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2x	Open	Closed	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (CX 6X86L)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
166MHz	66MHz	2x	Open	Closed	2 & 3	1 & 2	2 & 3
200MHz	75MHz	2.5x	Closed	Closed	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.							

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CPU SPEED SELECTION (CX 6X86MX)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
166MHz	60MHz	2.5x	Closed	Closed	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2x	Open	Closed	2 & 3	1 & 2	2 & 3
200MHz	66MHz	2.5x	Closed	Closed	2 & 3	1 & 2	2 & 3
200MHz	75MHz	2x	Open	Closed	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AM K5)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
75MHz	50MHz	1.5x	Open	Open	2 & 3	2 & 3	2 & 3
90MHz	60MHz	1.5x	Open	Open	2 & 3	2 & 3	1 & 2
100MHz	66MHz	1.5x	Open	Open	2 & 3	1 & 2	2 & 3
120MHz	60MHz	1.5x	Open	Open	2 & 3	2 & 3	1 & 2
133MHz	66MHz	1.5x	Open	Open	2 & 3	1 & 2	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2.5x	Closed	Closed	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AM K6)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
166MHz	66MHz	2.5x	Closed	Closed	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	Closed	Open	2 & 3	1 & 2	2 & 3
233MHz	66MHz	3.5x	Open	Open	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
75MHz	50MHz	1.5x	Open	Open	2 & 3	2 & 3	2 & 3
90MHz	60MHz	1.5x	Open	Open	2 & 3	2 & 3	1 & 2
100MHz	66MHz	1.5x	Open	Open	2 & 3	1 & 2	2 & 3
120MHz	60MHz	2x	Open	Closed	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	Open	Closed	2 & 3	1 & 2	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2.5x	Closed	Closed	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	Closed	Open	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.							

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CPU SPEED SELECTION (INTEL MMX)							
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP6**	**JP7**	**JP8**
166MHz	66MHz	2.5x	Closed	Closed	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	Closed	Open	2 & 3	1 & 2	2 & 3
233MHz	66MHz	3.5x	Open	Open	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.							

CPU TYPE SELECTION		
Type	**JP5**	**JP10**
AMD	Pins 1 & 2 closed	Open
Cyrix	Pins 1 & 2 closed	Closed
CX 6X86L-200	Pins 2 & 3 closed	Closed
CX 6X86MX-200 (75MHz)	Pins 2 & 3 closed	Closed
Intel	Pins 1 & 2 closed	Open

CPU VOLTAGE SELECTION						
Voltage	**JP1**	**JP9A**	**JP9B**	**JP9C**	**JP9D**	**JP9E**
2.1v	*	Open	Open	Open	Open	Closed
2.8v	3 & 5, 4 & 6	Open	Closed	Open	Open	Open
2.9v	3 & 5, 4 & 6	Open	Closed	Open	Open	Closed
3.2v	3 & 5, 4 & 6	Open	Closed	Closed	Open	Open
3.3v	1 & 3, 2 & 4	Open	Closed	Closed	Open	Closed
3.5v	1 & 3, 2 & 4	Open	Closed	Closed	Closed	Closed
Note: Pins designated should be in the closed position. * = Reserved for future CPUs.						