DTK COMPUTER INC.
PAM-0050V (VER. 1.10)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	VIA
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI/Award
Dimensions	270mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connectors (2), USB connectors (2)
NPU Options	None
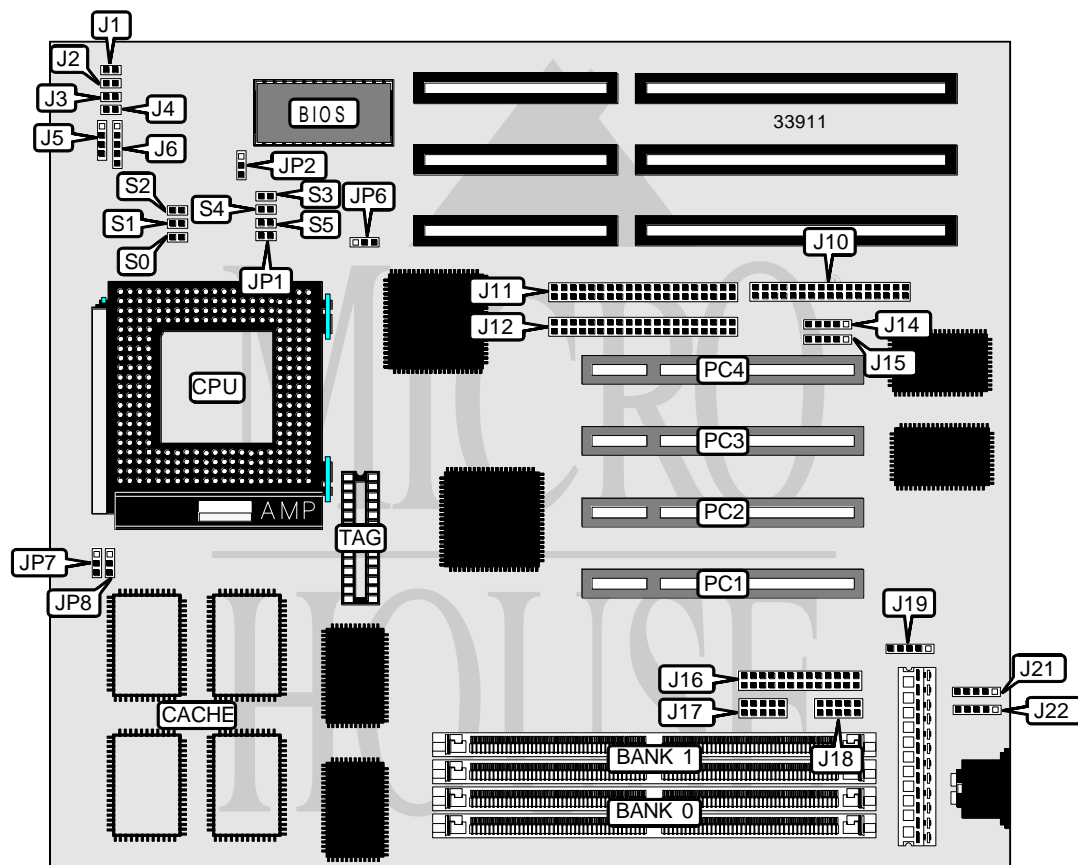
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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Reset switch	J1	IR connector	J14
Turbo switch	J2	IR connector (fast)	J15
Turbo LED	J3	Parallel port	J16
IDE interface LED	J4	Serial port 2	J17
Speaker	J5	Serial port 1	J18
Power LED & keylock	J6	PS/2 mouse interface	J19
Floppy drive interface	J10	USB connector 2	J21
IDE interface 2	J11	USB connector 1	J22
IDE interface 1	J12	32-bit PCI slots	PC1 - PC4

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
Flash BIOS voltage select 12v	JP2	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP2	Pins 1 & 2 closed
í CMOS memory normal operation	JP6	Pins 1 & 2 closed
CMOS memory clear	JP6	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 16M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks are interchangeable.		

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P A M - 0 0 5 0 V (V E R . 1 . 1 0)

CACHE CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**TAG**
256KB	(2) 32K x 32	None	(1) 8K/16K/32K x 8
512KB	(2) 32K x 32	(2) 32K x 32	(1) 16K/32K x 8
Note: The location of banks 0 & 1 are unidentified.			

CPU SPEED SELECTION (CYRIX)							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**S3**	**S4**	**S5**
120MHz	50MHz	2x	1 & 2	2 & 3	Closed	Closed	Open
133MHz	55MHz	2x	1 & 2	2 & 3	Open	Open	Open
150MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
166MHz	66MHz	3x	1 & 2	2 & 3	Open	Closed	Open
200MHz	66MHz	3x	1 & 2	2 & 3	Open	Open	Closed
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AMD)							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**S3**	**S4**	**S5**
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
120MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
133MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed	Open
150MHz	66MHz	1.5x	1 & 2	2 & 3	Closed	Open	Open
166MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**S3**	**S4**	**S5**
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
120MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
133MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed	Open
150MHz	60MHz	2.5x	2 & 3	2 & 3	Closed	Open	Open
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Closed	Open
180MHz	60MHz	3x	2 & 3	1 & 2	Closed	Open	Open
200MHz	66MHz	3x	2 & 3	1 & 2	Open	Closed	Open
Note: Pins designated should be in the closed position.							

CPU VOLTAGE SELECTION					
Voltage	**V core**	**JP1**	**S0**	**S1**	**S2**
3.4v (single)	N/A	Closed	Closed	Closed	Closed
3.4v (dual)	2.9v	Closed	Open	Open	Open
3.5v (single)	N/A	Open	Closed	Closed	Closed