ABIT COMPUTER CORPORATION
P H 5 (R E V . 1 . 1)

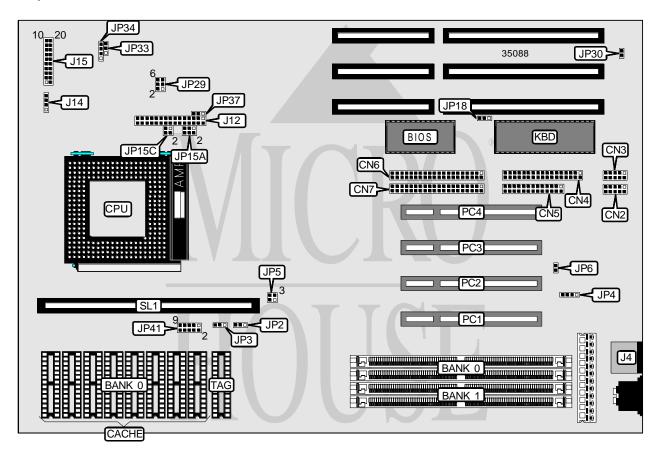|	
Processor	Pentium
Processor Speed	75/90/100/120/125/133/150/166/180MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VRM module connector, cache slot
NPU Options	None

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN2	Speaker	J15 pins 7 - 10
Serial port 2	CN3	Turbo LED	J15 pins 12 & 13
Floppy drive interface	CN4	Turbo switch	J15 pins 16 - 18
Parallel port	CN5	Reset switch	J15 pins 19 & 20
IDE interface 2	CN6	IR connector	JP4
IDE interface 1	CN7	Green PC connector	JP5
PS/2 mouse port	J4	External battery	JP34
VRM module connector	J12	Chassis fan power	JP37
IDE interface LED	J14	32-bit PCI slots	PC1 - PC4
Power LED & keylock	J15 pins 1 - 5	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í On board I/O enabled	JP6	Open
On board I/O disabled	JP6	Closed
í BIOS type select 5v EPROM	JP18	pins 2 & 3 closed
BIOS type select 12v flash	JP18	pins 1 & 2 closed
í Monitor type select color	JP30	Closed
Monitor type select monochrome	JP30	Open
í CMOS memory normal operation	JP33	pins 1 & 2 closed
CMOS memory clear	JP33	pins 2 & 3 closed
í Factory configured - do not alter	JP41	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 32	NONE
2MB	NONE	(2) 256K x 32
4MB	(2) 256K x 32	(2) 256K x 32
8MB	(2) 1M x 32	NONE
8MB	NONE	(2) 1M x 32
10MB	(2) 1M x 32	(2) 256K x 32
10MB	(2) 256K x 32	(2) 1M x 32
16MB	(2) 2M x 32	NONE
16MB	(2) 1M x 32	(2) 1M x 32
16MB	NONE	(2) 2M x 32
18MB	(2) 2M x 32	(2) 256K x 32
18MB	(2) 256K x 32	(2) 2M x 32
24MB	(2) 2M x 32	(2) 1M x 32
24MB	(2) 1M x 32	(2) 2M x 32
32MB	(2) 4M x 32	NONE
32MB	(2) 2M x 32	(2) 2M x 32
32MB	NONE	(2) 4M x 32
34MB	(2) 4M x 32	(2) 256K x 32
34MB	(2) 256K x 32	(2) 4M x 32

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
40MB	(2) 4M x 32	(2) 1M x 32
40MB	(2) 1M x 32	(2) 4M x 32
48MB	(2) 4M x 32	(2) 2M x 32
48MB	(2) 2M x 32	(2) 4M x 32
64MB	(2) 8M x 32	NONE
64MB	(2) 4M x 32	(2) 4M x 32
64MB	NONE	(2) 8M x 32
66MB	(2) 8M x 32	(2) 256K x 32
66MB	(2) 256K x 32	(2) 8M x 32
72MB	(2) 8M x 32	(2) 1M x 32
72MB	(2) 1M x 32	(2) 8M x 32
80MB	(2) 8M x 32	(2) 2M x 32
80MB	(2) 2M x 32	(2) 8M x 32
96MB	(2) 8M x 32	(2) 4M x 32
96MB	(2) 4M x 32	(2) 8M x 32
128MB	(2) 8M x 32	(2) 8M x 32

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB (A)	(8) 32K x 8	(1) 8K or (1)32K x 8	Not installed
256KB (B)	NONE	NONE	Installed
512KB (A)	(8) 64K x 8	(1) 16K or (1) 32K x 8	Not installed
512KB (B)	NONE	NONE	Installed
Note: If SL1 is used, remove all chips from Bank 0 & TAG.			

CACHE JUMPER CONFIGURATION			
Size	Type	JP2	JP3
256KB (A)	Asynchronous	pins 1 & 2 closed	pins 1 & 2 closed
256KB (B)	Burst/pipeline burst	pins 1 & 2 closed	pins 1 & 2 closed
512KB (A)	Asynchronous	pins 1 & 2 closed	pins 2 & 3 closed
512KB (B)	Burst/pipeline burst	pins 2 & 3 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION		
Speed	JP15A	JP15C
75MHz	pins 3 & 4, 5 & 6 closed	Open
90MHz	pins 1 & 2, 5 & 6 closed	Open
100MHz	pins 1 & 2, 3 & 4, 5 & 6 closed	Open
120MHz	pins 1 & 2, 5 & 6 closed	pins 1 & 2 closed
125MHz	pins 3 & 4, 5 & 6 closed	pins 1 & 2, 3 & 4 closed
133MHz	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 1 & 2 closed
150MHz	pins 1 & 2, 5 & 6 closed	pins 1 & 2, 3 & 4 closed
166MHz	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 1 & 2, 3 & 4 closed
180MHz	pins 1 & 2, 5 & 6 closed	pins 3 & 4 closed

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CPU VOLTAGE CONFIGURATION	
Voltage	JP29
3.3v	pins 1 & 2 closed
3.45v	pins 3 & 4 closed
3.6v	pins 5 & 6 closed

VRM CONFIGURATION	
Setting	JP12
Installed	VRM module installed
Not installed	pins 7 & 9, 8 & 10, 11 & 13, 12 & 14 closed