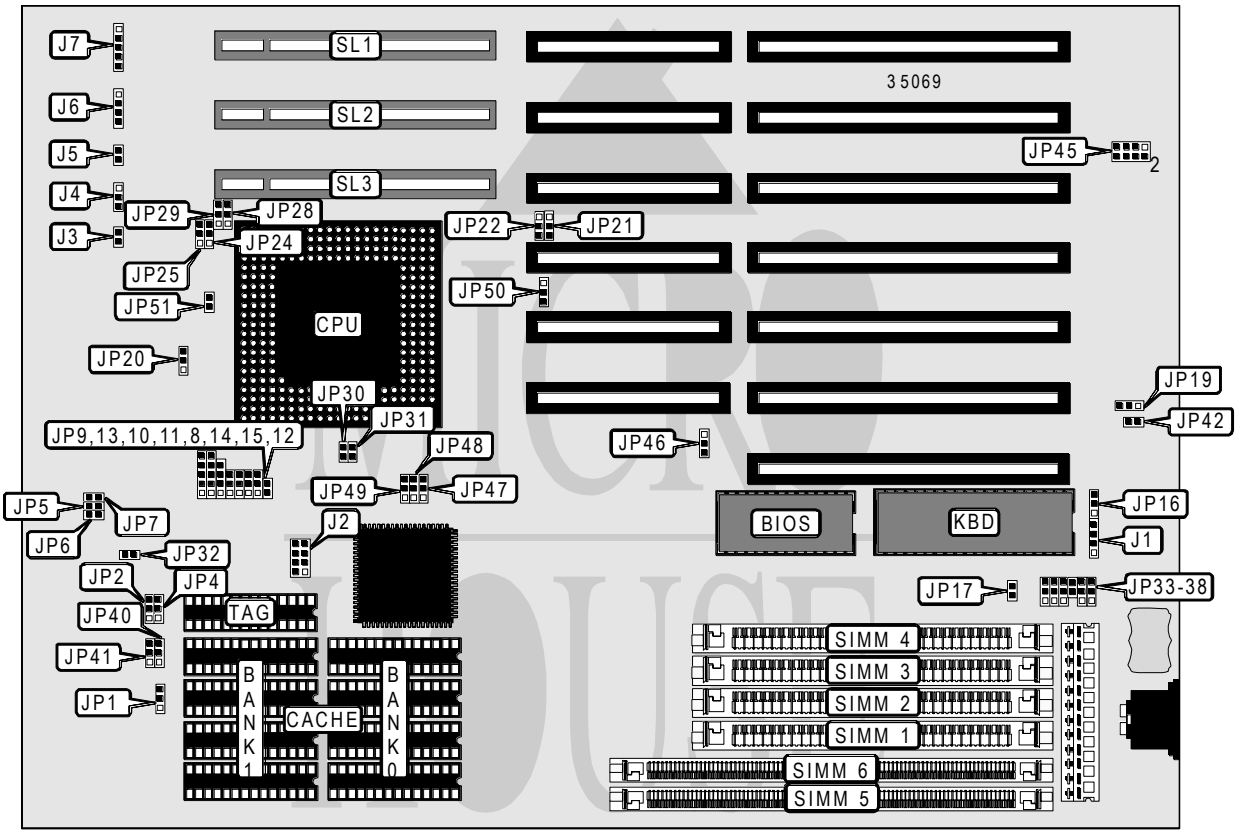
ABIT COMPUTER CORPORATION
4 8 6 V E S A A G 4

Processor	CX486SX/80486SX/80487SX/CX486DX/80486DX/80486DX2/Pentium Overdrive
Processor Speed	20/25/33/40(internal)/50(internal)/50/66(internal)/75(internal)/83(internal)/ 100(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	128/256/512KB
BIOS	AMI/Award
Dimensions	260mm x 220mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Reset switch	J6
Turbo switch	J3	Power LED & keylock	J7
Turbo LED	J4	Green PC connector	JP45
Speaker	J5	32-bit VESA Local bus slots	SL1 - SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J2	Open
í Turbo mode enabled	J3	pins 2 & 3 closed
Turbo mode disabled	J3	pins 1 & 2 closed
í CMOS memory normal operation	JP16	pins 1 & 2 closed
CMOS memory clear	JP16	pins 2 & 3 closed
í Monitor type select color	JP17	Closed
Monitor type select monochrome	JP17	Open
í Enter break switch	JP19	pins 1 & 2 closed
Leave break switch	JP19	pins 2 & 3 closed
í Factory configured - do not alter	JP23	pins 1 & 2 closed
í Suspend mode disabled	JP26	Open
Suspend mode enabled	JP26	Closed
í Factory configured - do not alter	JP30	Open
í Factory configured - do not alter	JP31	Open
í Factory configured - do not alter	JP42	Closed
Note: The location of JP23 & JP26 is unidentified.		

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
1MB	NONE	NONE	(1) 256K x 36
1MB	(4) 256K x 9	NONE	NONE
2MB	NONE	(1) 256K x 36	(1) 256K x 36
2MB	NONE	NONE	(1) 512K x 36
2MB	(4) 256K x 9	NONE	(1) 256K x 36
4MB	NONE	NONE	(1) 1M x 36
4MB	NONE	(1) 512K x 36	(1) 512K x 36
4MB	(4) 1M x 9	NONE	NONE
5MB	NONE	(1) 1M x 36	(1) 256K x 36
5MB	(4) 256K x 9	NONE	(1) 1M x 36
6MB	(4) 1M x 9	(1) 256K x 36	(1) 256K x 36
6MB	NONE	(1) 1M x 36	(1) 512K x 36
6MB	(4) 256K x 9	(1) 1M x 36	(1) 256K x 36
8MB	(4) 1M x 9	(1) 512K x 36	(1) 512K x 36
8MB	NONE	(1) 1M x 36	(1) 1M x 36
8MB	NONE	NONE	(1) 2M x 36
8MB	(4) 1M x 9	NONE	(1) 1M x 36
10MB	(4) 256K x 9	(1) 2M x 36	(1) 256K x 36
12MB	(4) 1M x 9	NONE	(1) 2M x 36
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	NONE	(1) 2M x 36	(1) 2M x 36
16MB	NONE	NONE	(1) 4M x 36
16MB	(4) 1M x 9	(1) 2M x 36	(1) 1M x 36
16MB	(4) 1M x 9	(1) 1M x 36	(1) 2M x 36
17MB	NONE	(1) 4M x 36	(1) 256K x 36
17MB	(4) 256K x 9	NONE	(1) 4M x 36

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DRAM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
18MB	(4) 4M x 9	(1) 256K x 36	(1) 256K x 36
18MB	NONE	(1) 4M x 36	(1) 512K x 36
18MB	(4) 256K x 9	(1) 4M x 36	(1) 256K x 36
20MB	(4) 4M x 9	(1) 512K x 36	(1) 512K x 36
20MB	NONE	(1) 4M x 36	(1) 1M x 36
24MB	(4) 4M x 9	(1) 1M x 36	(1) 1M x 36
24MB	(4) 1M x 9	(1) 4M x 36	(1) 1M x 36
32MB	NONE	(1) 4M x 36	(1) 4M x 36
32MB	NONE	NONE	(1) 8M x 36
32MB	(4) 4M x 9	NONE	(1) 4M x 36
36MB	(4) 4M x 9	(1) 4M x 36	(1) 1M x 36
36MB	NONE	(1) 8M x 36	(1) 1M x 36
36MB	(4) 1M x 9	NONE	(1) 8M x 36
36MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
40MB	(4) 1M x 9	(1) 8M x 36	(1) 1M x 36
48MB	NONE	(1) 8M x 36	(1) 4M x 36
48MB	(4) 4M x 9	NONE	(1) 8M x 36
48MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
64MB	NONE	(1) 8M x 36	(1) 8M x 36
64MB	NONE	NONE	(1) 16M x 36
64MB	(4) 4M x 9	(1) 4M x 36	(1) 8M x 36
64MB	(4) 16M x 9	NONE	NONE
64MB	(4) 4M x 9	(1) 8M x 36	(1) 4M x 36
64MB	(4) 16M x 9	NONE	NONE
65MB	NONE	(1) 16M x 36	(1) 256K x 36
65MB	(4) 256K x 9	NONE	(1) 16M x 36
68MB	NONE	(1) 16M x 36	(1) 1M x 36
72MB	(4) 16M x 9	(1) 1M x 36	(1) 1M x 36
72MB	(4) 1M x 9	(1) 16M x 36	(1) 1M x 36
80MB	NONE	(1) 16M x 36	(1) 4M x 36
80MB	(4) 4M x 9	NONE	(1) 16M x 36
96MB	(4) 16M x 9	(1) 4M x 36	(1) 4M x 36
128MB	NONE	(1) 16M x 36	(1) 16M x 36
128MB	(4) 16M x 9	NONE	(1) 16M x 36

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DRAM JUMPER CONFIGURATION					
Bank 0	Bank 1	Bank 2	JP33	JP34	JP35
SIMM 1-4	SIMM 6 (S)	SIMM 5 (S)	Open	1 & 2	Open
SIMM 1-4	SIMM 6 (D)	SIMM 5 (S)	Open	1 & 2	Closed
SIMM 1-4	SIMM 6 (S)	SIMM 5 (D)	2 & 3	1 & 2	Open
SIMM 6 (S)	SIMM 5 (S)	SIMM 1-4	1 & 2	2 & 3	Open
SIMM 6 (D)	SIMM 5 (D)	SIMM 1-4	1 & 2	2 & 3	Open
SIMM 6 (D)	SIMM 5 (S)	NONE	1 & 2	2 & 3	Open
SIMM 6 (D)	SIMM 5 (D)	SIMM 1-4	1 & 2	2 & 3	Open
Note: Pins designated should be in the closed positon.					

DRAM JUMPER CONFIGURATION (CON'T)					
Bank 0	Bank 1	Bank 2	JP36	JP37	JP38
SIMM 1-4	SIMM 6 (S)	SIMM 5 (S)	2 & 3	Open	2 & 3
SIMM 1-4	SIMM 6 (D)	SIMM 5 (S)	2 & 3	2 & 3	Open
SIMM 1-4	SIMM 6 (S)	SIMM 5 (D)	2 & 3	Open	2 & 3
SIMM 6 (S)	SIMM 5 (S)	SIMM 1-4	1 & 2	1 & 2	1 & 2
SIMM 6 (D)	SIMM 5 (D)	SIMM 1-4	1 & 2	1 & 2	1 & 2
SIMM 6 (D)	SIMM 5 (S)	NONE	1 & 2	1 & 2	1 & 2
SIMM 6 (D)	SIMM 5 (D)	SIMM 1-4	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed positon.					

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB	(4) 64K x 8	NONE	(1) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP1	JP2	JP4	JP40	JP41
128KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2
256KB	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
512KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
512KB	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

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CPU TYPE CONFIGURATION								
Type	JP8	JP9	JP10	JP11	JP12	JP13	JP14	JP15
CX486SX	2 & 3	Open	2 & 3	Open	2-3, 4-5	Open	1-2, 3-4	2 & 3
486SX	2 & 3	Open	2 & 3	Open	Open	Open	Open	Open
486SL	1 & 2	Closed	2 & 3	2 & 3	3 & 4	Open	2 & 3	4 & 5
CX486DX	1 & 2	Closed	2 & 3	2 & 3	2 & 3	Open	1-2, 3-4	2 & 3
486DX/DX2	1 & 2	Closed	2 & 3	2 & 3	Open	Open	Open	Open
486DX4	1 & 2	Closed	2 & 3	2 & 3	3 & 4	Open	2 & 3	4 & 5
Note: Pins designated should be in the closed position.								

CPU TYPE CONFIGURATION (CON'T)							
Type	JP21	JP22	JP24	JP25	JP46	JP48	JP50
CX486SX	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
486SX	2 & 3	1 & 2	1 & 2	Open	2 & 3	2 & 3	2 & 3
486SL	2 & 3	1 & 2	Open	Open	2 & 3	2 & 3	2 & 3
CX486DX	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
486DX/DX2	2 & 3	1 & 2	1 & 2	Open	2 & 3	2 & 3	2 & 3
486DX4	2 & 3	1 & 2	Open	Open	2 & 3	1 & 2	1 & 2
Note: Pins designated should be in the closed position.							

CPU SPEED CONFIGURATION								
Speed	JP5	JP6	JP7	JP20	JP32	JP47	JP49	JP51
20MHz	Closed	Open	Closed	1 & 2	Closed	Open	Open	Open
25MHz	Closed	Closed	Open	1 & 2	Closed	Open	Open	Open
33MHz	Open	Closed	Closed	1 & 2	Closed	Open	Open	Open
40MHz	Closed	Open	Open	2 & 3	Closed	Open	Open	Open
50iMHz	Closed	Closed	Open	1 & 2	Closed	Open	Open	Open
50MHz	Open	Open	Closed	1 & 2	Closed	Open	Open	Open
66iMHz	Open	Closed	Closed	1 & 2	Closed	Open	Open	Open
Note: Pins designated should be in the closed position.								

CPU SPEED CONFIGURATION (DX4 ONLY)								
Speed	JP5	JP6	JP7	JP20	JP32	JP47	JP49	JP51
25/75MHz	Closed	Closed	Open	1 & 2	Open	2 & 3	1 & 2	Open
33/83MHz	Open	Closed	Closed	1 & 2	Open	2 & 3	Open	Closed
33/100MHz	Open	Closed	Closed	1 & 2	Open	1 & 2	1 & 2	Open
50/100MHz	Open	Open	Closed	1 & 2	Open	1 & 2	2 & 3	Open
Note: Pins designated should be in the closed position.								

VESA WAIT STATE CONFIGURATION	
Wait states	JP28
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

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VESA BUS SPEED CONFIGURATION	
CPU speed	JP29
< = 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed

GREEN PC CONFIGURATION	
Description	JP45
Control video HSYNC	pin 1
Ground	pin 2
Control video VSYNC	pin 3
Ground	pin 4
Control ENVIDEO	pin 5
Ground	pin 6
Monitor AC power	pin 7
Ground	pin 8