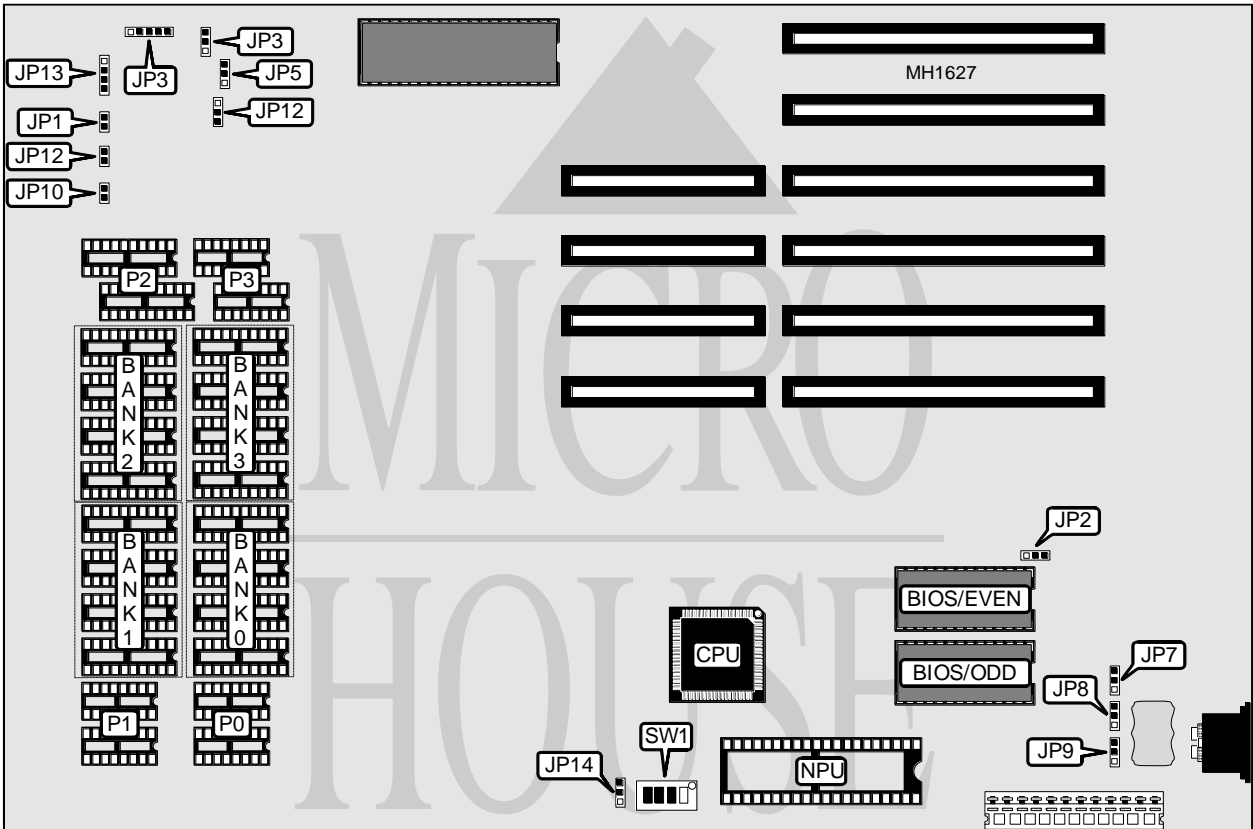


# PC PARTNER

## 35-7008-1 2

<b>Processor</b>	80286
<b>Processor Speed</b>	12MHz
<b>Chip Set</b>	ACT
<b>Max. onboard DRAM</b>	2MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP1	Reset	JP10
Power LED & keylock	JP4	Turbo switch	JP12
External battery	JP8	Speaker	JP13

Continued on next page . . .

**PC PARTNER**  
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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í BIOS type select 27256	JP2	pins 1 & 2 closed
BIOS type select 27128	JP2	pins 2 & 3 closed
í Monitor type select color	JP3	pins 2 & 3 closed
Monitor type select monochrome	JP3	pins 1 & 2 closed
í Speed select via keyboard enabled	JP5	pins 1 & 2 closed
Speed select via keyboard disabled	JP5	pins 2 & 3 closed
í Battery type select onboard	JP7	pins 2 & 3 closed
Battery type select external	JP7	pins 1 & 2 closed
í Power good signal detect from board	JP9	pins 1 & 2 closed
Power good signal detect from power supply	JP9	pins 2 & 3 closed
í Parity check disabled	JP14	pins 2 & 3 closed
Parity check enabled	JP14	pins 1 & 2 closed
í Factory configured - do not alter	SW1/switch 3	Off
í RAM select 0 wait states	SW1/switch 4	Off
RAM select 1 wait state	SW1/switch 4	On

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(4) 44256	NONE	NONE	NONE
640KB	(4) 44256	(4) 4464	NONE	NONE
1024KB	(4) 44256	(4) 44256	NONE	NONE
1152KB	(4) 44256	(4) 44256	(4) 4464	NONE
1536KB	(4) 44256	(4) 44256	(4) 44256	NONE
1664KB	(4) 44256	(4) 44256	(4) 44256	(4) 4464
2048KB	(4) 44256	(4) 44256	(4) 44256	(4) 44256

DRAM SWITCH CONFIGURATION				
Size	System RAM	Extended RAM	SW1/1	SW1/2
512KB	512KB	0KB	On	On
640KB	640KB	0KB	On	Off
1024KB	640KB	384KB	On	Off
1152KB	640KB	512KB	Off	On
1536KB	640KB	896KB	Off	On
1664KB	640KB	1024KB	Off	Off
2048KB	640KB	1408KB	Off	Off