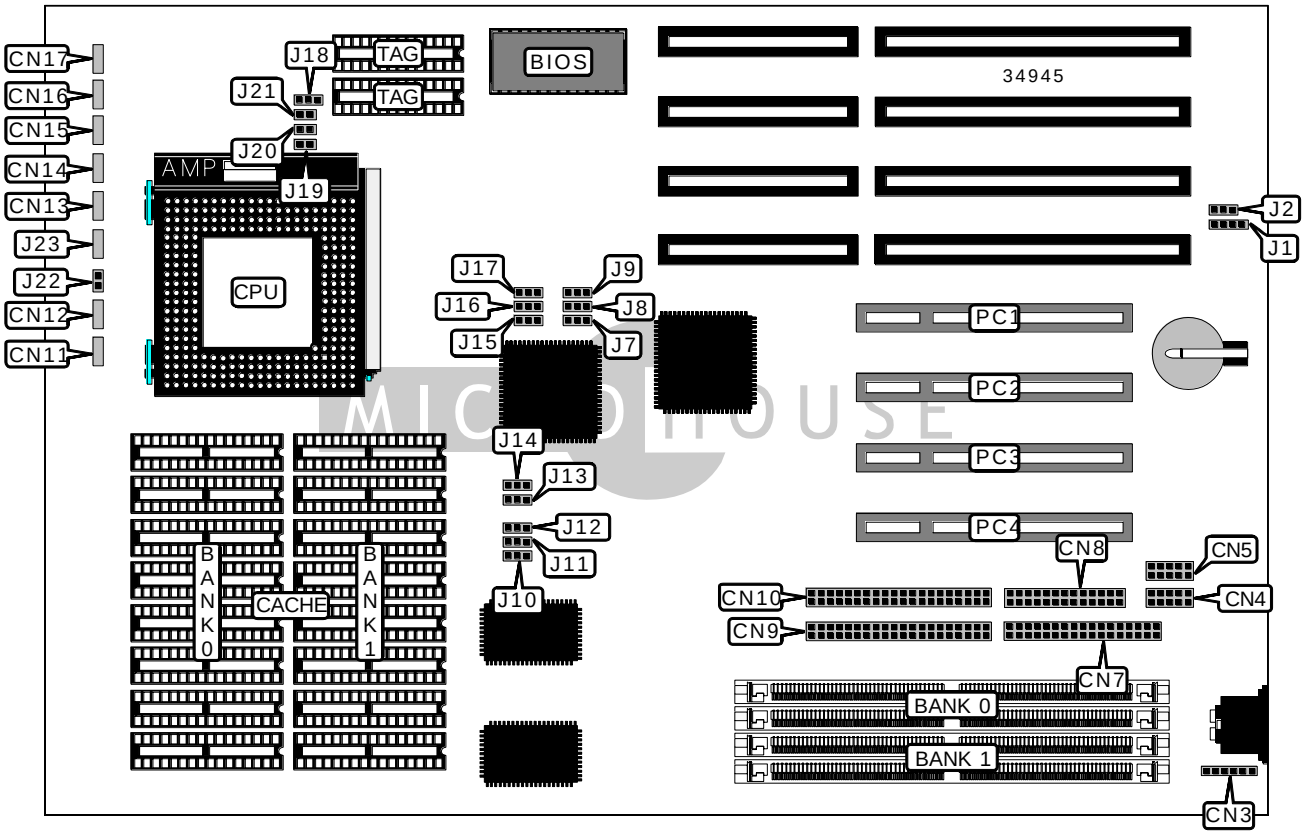
DIAMOND FLOWER, INC.
G586VBP (REV. 0)

Device Type	Mainboard
Processor	Pentium
Processor Speed	75/90/100/120MHz
Chip Set	VIA
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	256/512/1024/2048KB
BIOS	Award
Dimensions	330mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2)
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
PS/2 mouse interface	CN3	IDE interface 1	CN9
Serial port 1	CN4	IDE interface 2	CN10
Serial port 2	CN5	External battery	J1
Floppy drive interface	CN7	32-bit PCI slots	PC1 – PC4
Parallel port	CN8		

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	CN11	Unidentified
í Factory configured - do not alter	CN12	Unidentified
í Factory configured - do not alter	CN13	Unidentified
í Factory configured - do not alter	CN14	Unidentified
í Factory configured - do not alter	CN15	Unidentified
í Factory configured - do not alter	CN16	Unidentified
í Factory configured - do not alter	CN17	Unidentified
í Battery type select internal	J2	Pins 1 & 2 closed
Battery type select external	J2	Open
í Cache type select write back	J18	Pins 1 & 2 closed
Cache type select write through	J18	Pins 2 & 3 closed
í Factory configured - do not alter	J23	Unidentified

SIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
2MB	(2) 256K x 36	None
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 512K x 36	(2) 256K x 36
8MB	(2) 1M x 36	None
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 2M x 36	(2) 512K x 36

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SIMM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 4M x 36	(2) 512K x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 8M x 36	(2) 512K x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**TAG**
256KB	(8) 32K x 8	None	(2) 32K x 8
512KB (A)	(8) 64K x 8	None	(2) 32K x 8
512KB (B)	(8) 32K x 8	(8) 32K x 8	(2) 32K x 8
1MB (A)	(8) 128K x 8	None	(2) 32K x 8
1MB (B)	(8) 64K x 8	(8) 64K x 8	(2) 32K x 8
2MB	(8) 128K x 8	(8) 128K x 8	(2) 128K x 8

CACHE JUMPER CONFIGURATION						
Size	**J7**	**J8**	**J9**	**J15**	**J16**	**J17**
256KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
512KB (A)	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
512KB (B)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3
1MB (A)	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
1MB (B)	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
2MB	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.						

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CACHE TYPE SELECTION		
Type	**J13**	**J14**
í Mixed	Pins 1 & 2 closed	Pins 1 & 2 closed
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED SELECTION						
CPU speed	**Clock speed**	**Multiplier**	**J10**	**J11**	**J12**	**J22**
75MHz	50MHz	1.5x	2 & 3	1 & 2	2 & 3	Closed
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	Open
100MHz	66MHz	1.5x	1 & 2	2 & 3	2 & 3	Open
120MHz	60MHz	2x	1 & 2	1 & 2	1 & 2	Open
Note: Pins designated should be in the closed position.						

CPU VOLTAGE SELECTION			
Voltage	**J19**	**J20**	**J21**
í 3.3v	Closed	Open	Open
3.45v	Open	Closed	Open
3.8v	Open	Open	Closed

CMOS MEMORY SELECTION		
Setting	**J1**	**J2**
í Normal operation	Open	Pins 1 & 2 closed
Clear	Pins 2 & 3 closed	Pins 2 & 3 closed
Note: J1 & J2 configure both CMOS and the external battery.		