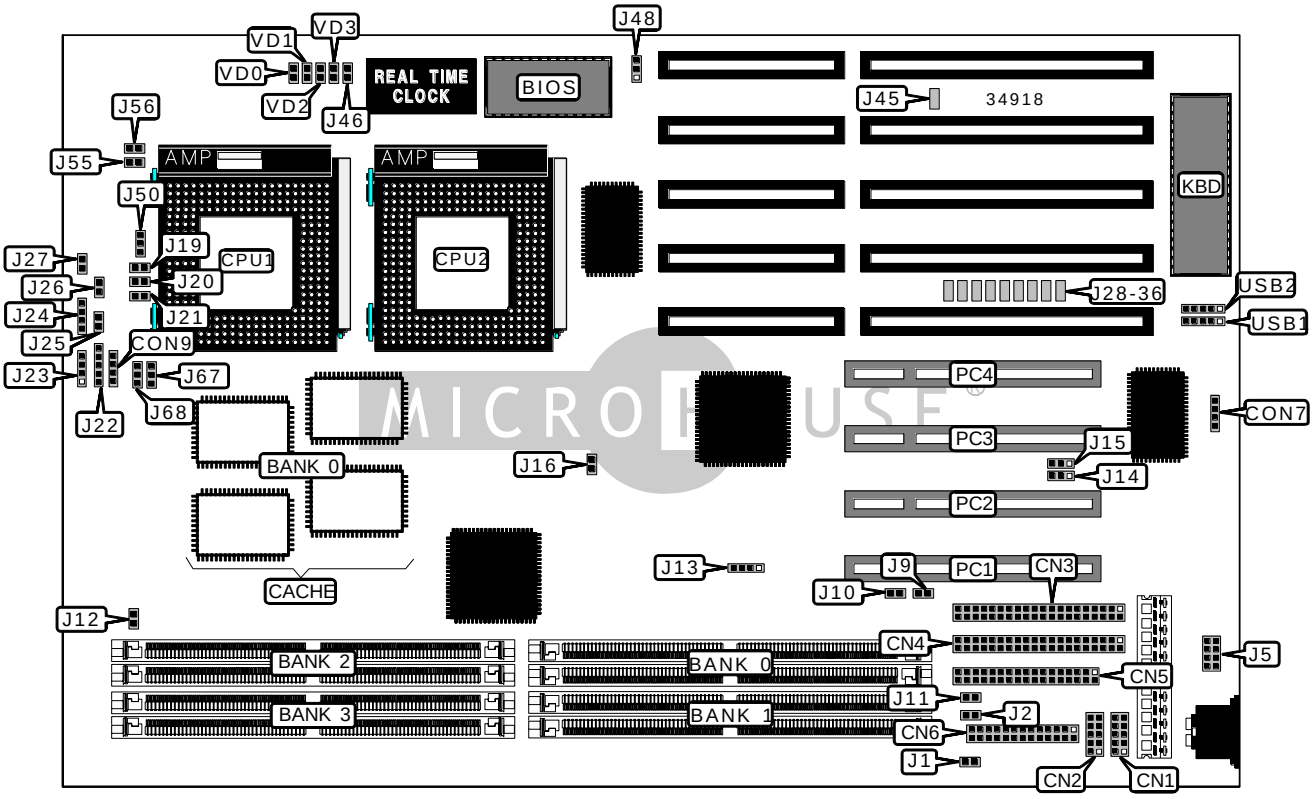
TYAN COMPUTER CORPORATION
S 1 5 6 4 D

Device Type	Mainboard
Processor	CX 6X86/IBM 6X86/CX 686MX/IBM 6X86MX/AM K5/AM K6/Pentium
Processor Speed	75/90/100/120/133/150/166/200/233MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	512KB
BIOS	AMI, Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connectors (2), USB connectors (2)
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Serial port 2	CN1	Speaker	J23
Serial port 1	CN2	IDE interface LED	J24
IDE interface 2	CN3	Turbo LED	J25
IDE interface 1	CN4	Turbo switch	J26
Floppy drive interface	CN5	Reset switch	J27
Parallel port	CN6	CPU 1 fan power	J67
IR connector	CON7	CPU 2 fan power	J68
IR connector	CON9	32-bit PCI slots	PC1 – PC4
PS/2 mouse interface	J5	USB connector 1	USB1
Power LED & keylock	J22	USB connector 2	USB2

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	J16	Unidentified
í Factory configured - do not alter	J28	Unidentified
í Factory configured - do not alter	J29	Unidentified
í Factory configured - do not alter	J30	Unidentified
í Factory configured - do not alter	J31	Unidentified
í Factory configured - do not alter	J32	Unidentified
í Factory configured - do not alter	J33	Unidentified
í Factory configured - do not alter	J34	Unidentified
í Factory configured - do not alter	J35	Unidentified
í Factory configured - do not alter	J36	Unidentified
í Factory configured - do not alter	J45	Unidentified
í CMOS memory normal operation	J46	Open
CMOS memory clear	J46	Closed
Flash BIOS voltage select 12v	J48	Pins 2 & 3 closed
Flash BIOS voltage select 5v	J48	Pins 1 & 2 closed
í Factory configured - do not alter	J55	Unidentified
í Factory configured - do not alter	J56	Unidentified

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SIMM CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
8MB	(2) 1M x 36	None	None	None
16MB	(2) 2M x 36	None	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None
24MB	(2) 2M x 36	(2) 1M x 36	None	None
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	None
32MB	(2) 4M x 36	None	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None	None
32MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
40MB	(2) 4M x 36	(2) 1M x 36	None	None
48MB	(2) 4M x 36	(2) 2M x 36	None	None
48MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	None
64MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None	None
72MB	(2) 8M x 36	(2) 1M x 36	None	None
80MB	(2) 8M x 36	(2) 2M x 36	None	None
96MB	(2) 8M x 36	(2) 4M x 36	None	None
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	None
128MB	(2) 16M x 36	None	None	None
128MB	(2) 8M x 36	(2) 8M x 36	None	None
136MB	(2) 16M x 36	(2) 1M x 36	None	None
144MB	(2) 16M x 36	(2) 2M x 36	None	None
152MB	(2) 16M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
160MB	(2) 16M x 36	(2) 4M x 36	None	None
192MB	(2) 16M x 36	(2) 8M x 36	None	None
192MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	None
224MB	(2) 16M x 36	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
256MB	(2) 16M x 36	(2) 16M x 36	None	None
256MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36
272MB	(2) 16M x 36	(2) 16M x 36	(2) 1M x 36	(2) 1M x 36
288MB	(2) 16M x 36	(2) 16M x 36	(2) 2M x 36	(2) 2M x 36
320MB	(2) 16M x 36	(2) 16M x 36	(2) 4M x 36	(2) 4M x 36
320MB	(2) 16M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36
384MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36	None
512MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.				

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DIMM/SIMM VOLTAGE CONFIGURATION							
Voltage	**J1**	**J2**	**J8**	**J9**	**J10**	**J11**	**J12**
3.3v	Open	Open	Closed	Closed	Closed	Open	Open
5v	Closed	Closed	Open	Open	Open	Closed	Closed
Note: The location of J8 is unidentified.							

CACHE CONFIGURATION	
Size	**Bank 0**
512KB	(4) 32K x 32

CPU SPEED SELECTION (CX 6X86)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
120MHz	50MHz	2x	1 & 2, 3 & 4	Closed	Open	Open	Open
150MHz	60MHz	2x	3 & 4	Closed	Open	Open	Open
166MHz	66MHz	2x	1 & 2	Closed	Open	Open	Open
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

CPU SPEED SELECTION (IBM 6X86)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
120MHz	50MHz	2x	1 & 2, 3 & 4	Closed	Open	Open	Open
150MHz	60MHz	2x	3 & 4	Closed	Open	Open	Open
166MHz	66MHz	2x	1 & 2	Closed	Open	Open	Open
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

CPU SPEED SELECTION (CX 6X86MX)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
166MHz	60MHz	2.5x	3 & 4	Closed	Closed	Open	Closed
166MHz	66MHz	2x	1 & 2	Closed	Open	Open	Closed
200MHz	66MHz	2.5x	1 & 2	Closed	Closed	Open	Closed
200MHz	75MHz	2.5x	Unidentified	Closed	Closed	Open	Closed
233MHz	66MHz	3x	1 & 2	Open	Closed	Open	Closed
233MHz	75MHz	3x	Unidentified	Open	Closed	Open	Closed
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

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CPU SPEED SELECTION (IBM 6X86MX)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
166MHz	60MHz	2.5x	3 & 4	Closed	Closed	Open	Closed
166MHz	66MHz	2x	1 & 2	Closed	Open	Open	Closed
200MHz	66MHz	2.5x	1 & 2	Closed	Closed	Open	Closed
200MHz	75MHz	2.5x	Unidentified	Closed	Closed	Open	Closed
233MHz	66MHz	3x	1 & 2	Open	Closed	Open	Closed
233MHz	75MHz	3x	Unidentified	Open	Closed	Open	Closed
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

CPU SPEED SELECTION (AM K5)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
75MHz	50MHz	1.5x	1 & 2, 3 & 4	Open	Open	Open	Open
90MHz	60MHz	1.5x	3 & 4	Open	Open	Open	Open
100MHz	66MHz	1.5x	1 & 2	Open	Open	Open	Open
120MHz	60MHz	2x	3 & 4	Closed	Open	Open	Open
133MHz	66MHz	2x	1 & 2	Closed	Open	Open	Open
150MHz	60MHz	2.5x	3 & 4	Closed	Closed	Open	Open
166MHz	66MHz	2.5x	1 & 2	Closed	Closed	Open	Open
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

CPU SPEED SELECTION (AM K6)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
166MHz	66MHz	2.5x	1 & 2	Closed	Closed	Open	Closed
200MHz	66MHz	3x	1 & 2	Open	Closed	Open	Closed
233MHz	66MHz	3.5x	1 & 2	Open	Open	Open	Closed
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

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CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
75MHz	50MHz	1.5x	1 & 2, 3 & 4	Open	Open	Open	Open
90MHz	60MHz	1.5x	3 & 4	Open	Open	Open	Open
100MHz	66MHz	1.5x	1 & 2	Open	Open	Open	Open
120MHz	60MHz	2x	3 & 4	Closed	Open	Open	Open
133MHz	66MHz	2x	1 & 2	Closed	Open	Open	Open
150MHz	60MHz	2.5x	3 & 4	Closed	Closed	Open	Open
166MHz	66MHz	2.5x	1 & 2	Closed	Closed	Open	Open
200MHz	66MHz	3x	1 & 2	Open	Closed	Open	Open
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

CPU SPEED SELECTION (INTEL MMX)							
CPU speed	**Clock speed**	**Multiplier**	**J13**	**J19**	**J20**	**J21**	**VIO**
166MHz	66MHz	2.5x	1 & 2	Closed	Closed	Open	Closed
200MHz	66MHz	3x	1 & 2	Open	Closed	Open	Closed
233MHz	66MHz	3.5x	1 & 2	Open	Open	Open	Closed
Note: Pins designated should be in the closed position. The location of VIO is unidentified.							

CPU SELECTION	
Type	**J50**
Single CPU installed	Pins 1 & 2 closed
í Dual CPUs installed	Pins 2 & 3 closed

CPU VOLTAGE SELECTION				
Voltage	**VD0**	**VD1**	**VD2**	**VD3**
2.8v	Open	Open	Open	Closed
2.9v	Closed	Open	Open	Closed
3.2v	Open	Open	Closed	Closed
3.3v	Closed	Open	Closed	Closed
3.4v	Open	Closed	Closed	Closed
3.5v	Closed	Closed	Closed	Closed

SERIAL PORT 1 SELECTION		
Setting	**J14**	**J15**
Used as serial port	Pins 1 & 2 closed	Pins 1 & 2 closed
Used as IR connector	Pins 2 & 3 closed	Pins 2 & 3 closed